

# Intel<sup>®</sup> Platform Enablement Test Suite (Intel<sup>®</sup> PETS)

**Installation and User Guide**

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**Revision 10.7.0.0**

***May 2020***

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## Revision History

Revision #	Description	Revision Date
10.7.0.0	<b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Updated Section <a href="#">2.2 Requirements for the "System Under Test"</a></li> </ul>	May 2020
10.6.1.0	<b>Chapter 1: Introduction</b> <ul style="list-style-type: none"> <li>Updated section <a href="#">1.1.4 Intel® PETS Prerequisites Manager</a></li> </ul> <b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Updated Section <a href="#">2.2 Requirements for the "System Under Test"</a></li> </ul>	April 2020
10.5.0.0	<b>Chapter 6: Running Tests with Intel® Platform Enablement Test Suite</b> <ul style="list-style-type: none"> <li>Added section <a href="#">6.1.3 Finding a Test Case</a></li> </ul>	March 2020
10.4.2.0	<b>Chapter 1: Introduction</b> <ul style="list-style-type: none"> <li>Updated section <a href="#">1.1.3 Installation, Setup and Testing Flow</a></li> <li>Updated section <a href="#">1.1.4 Intel® PETS Prerequisites Manager</a></li> </ul> <b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Updated Section <a href="#">2.1 Testing Machine Requirements</a></li> <li>Updated Section <a href="#">2.2 Requirements for the "System Under Test"</a></li> </ul> <b>Chapter 5: Defining and Intel® Platform for Testing</b> <ul style="list-style-type: none"> <li>Updated Section <a href="#">5.4.1 Adding and Defining a SUT</a></li> <li>Updated Section <a href="#">5.4.3 Modifying the Definitions of a SUT</a></li> <li>Updated Section <a href="#">5.4.5 Importing a SUT Definition File</a></li> <li>Updated Section <a href="#">5.5.1 Testing the Configuration of the SUT</a></li> </ul> <b>Chapter 6: Running tests with Intel® Platform Enablement test suite</b> <ul style="list-style-type: none"> <li>Updated Section <a href="#">6.1.10 Report Viewer</a></li> </ul> <b>Chapter A.2: Package List</b> <ul style="list-style-type: none"> <li>Removed two Notes from the chapter</li> </ul>	January 2020
10.3.0.0	<b>Chapter 5: Defining Intel® Platform Enablement Test Suite</b> <ul style="list-style-type: none"> <li>Added a note in section <a href="#">5.4.1 Adding and Defining a SUT</a></li> </ul> Changed screenshots in section <a href="#">5.4.1 Adding and Defining a SUT</a>	December 2019



10.2.0.0	<b>Chapter 2: Requirements for the System under test</b> <ul style="list-style-type: none"> <li>Updated the table under <a href="#">Section 2.2 "Requirements for the system under test"</a></li> </ul> <b>Chapter 5: Defining an Intel® Platform for testing</b> <ul style="list-style-type: none"> <li>Updated a screenshot in <a href="#">section 5.4.1 "Adding and defining a SUT"</a></li> </ul>	November 2019
10.1.0.0	<b>Chapter 1: Introduction</b> <ul style="list-style-type: none"> <li>Updated <a href="#">section 1.1.4 Intel® PETS Prerequisites manager</a></li> </ul> <b>Chapter 2: Requirements for the System under test</b> <ul style="list-style-type: none"> <li>Updated the table under <a href="#">Section 2.2 "Requirements for the system under test"</a></li> <li>Removed support for intel® CSME 11.X platforms</li> </ul> <b>Chapter 4 : Installation</b> <ul style="list-style-type: none"> <li>Removed Section 4.3: Installing local agent on android (Intel® TXE Platforms only)</li> </ul> <b>Chapter 5: Defining an Intel® Platform for testing</b> <ul style="list-style-type: none"> <li>Updated <a href="#">section 5.4.1 "Adding and defining a SUT"</a></li> </ul> <b>Chapter 6: Running tests with Intel® Platform Enablement test suite</b> <ul style="list-style-type: none"> <li>Adding <a href="#">section 6.1.6 Skip battery charge</a></li> <li>Updated <a href="#">Section 6.1.1 Loading a Test Package</a></li> </ul> <b>Chapter A: Intel® Platform Enablement test suite package list</b> <ul style="list-style-type: none"> <li>Updated the chapter</li> <li>Updated <a href="#">Section A.2 package list</a></li> </ul>	September 2019
10.0.0.0	<b>Chapter 1: Introduction</b> <ul style="list-style-type: none"> <li>Updated section 1.1.4: Intel® Prerequisites Manager as follows: <ul style="list-style-type: none"> <li>Added a new button (Save Changes) to the prerequisites Manager dialog</li> <li>Renamed Section 1.1.4.2</li> <li>Updated sections "Under Bootable Image ISO" and " VNC viewer "</li> </ul> </li> </ul> <b>Chapter 2: Requirements for the System under test</b> <ul style="list-style-type: none"> <li>Added TGL in the table under "Requirements for the system under test"</li> <li>Updated the table under "Requirements for the system under test"</li> </ul> <b>Chapter 5.5.1: Testing the configuration SUT</b> <ul style="list-style-type: none"> <li>Updated the Table.</li> </ul>	August 2019
9.19.0.1	<b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Updated KBL PCH type in the table under Requirements " for the system under Test "</li> </ul>	July 2019
9.19.0.0	<b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Added support for Comet Lake -S Platform.</li> </ul>	July 2019
9.18.0.0	<b>General:</b> <ul style="list-style-type: none"> <li>Fixed Typos.</li> </ul> <b>Introduction:</b>	May 2019



	<ul style="list-style-type: none"> <li>Added a new section explaining the usage of <a href="#">Intel® PETS Prerequisites Manager</a>.</li> </ul>	
9.17.0.0	<b>General:</b> <ul style="list-style-type: none"> <li>Added support for Comet Lake Intel® CSME 14.0 platform.</li> </ul>	April 2019
9.16.0.0	<b>Chapter 13: Launching the Intel® Automated Workflow Suite:</b> Removed from the document.	March 2019
9.15.0	N/A	January 2019
9.14.0	<ul style="list-style-type: none"> <li>N/A</li> </ul>	December 2018
9.13.0	<b>General:</b> <ul style="list-style-type: none"> <li>Fixed Typos.</li> </ul>	November 2018
9.12.0	<b>Chapter 2.1: Testing Machine Requirements</b> <ul style="list-style-type: none"> <li>Updated .NET framework requirements.</li> </ul> <b>Chapter 6.1.3.5: Saving the Changed Configuration of a Test Package</b> <ul style="list-style-type: none"> <li>Added back the Section 6.1.3.5 "Saving the Changed Configuration of a Test Package".</li> </ul>	October 2018
9.11.0	<b>Chapter 4: Installation</b> <ul style="list-style-type: none"> <li>Removed the method to verify if APS is connected.</li> </ul> <b>Chapter 5: Defining an intel® Platform for Testing</b> <ul style="list-style-type: none"> <li>Removed "Defining New SUTs with the SUT Profile Creation Assistant" section.</li> </ul> <b>Chapter 6: Running Tests with Intel® Platform Enablement Test Suit</b> <ul style="list-style-type: none"> <li>Removed "saving the changed configuration of test package" section.</li> </ul> <b>Chapter 9: Running PETS in Package Design Mode</b> <ul style="list-style-type: none"> <li>Updated Wordings.</li> </ul> <b>Chapter 13: Launching the Intel® Automated Workflow Suite</b> <ul style="list-style-type: none"> <li>Updated Intel® PETS UI to match the change in chapter 5.</li> </ul>	September 2018
9.10.0	Chapter 9: PETS in Package Design Mode <ul style="list-style-type: none"> <li>Adding the ability to edit the prerequisites on the test and step level.</li> </ul> Chapter 2: System Requirements <ul style="list-style-type: none"> <li>Updated PETS .NET Framework requirements to .Net 4.6.1</li> </ul>	June 2018
9.9.0	<b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Added support for Whiskey Lake Platform.</li> </ul>	May 2018
9.8.0	<b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Added support for Ice Lake Platform.</li> </ul>	April 2018
9.5.0	<b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Dropped support for Broadwell, Shark Bay, Denlow, Haswell Refresh, Romley Refresh, BayTrail-M/D, and Braswell platforms.</li> </ul> <b>Chapter 6: Running Tests with Intel® Platform Enablement test Suite</b> <ul style="list-style-type: none"> <li>Updated the appearance of the result statistics in Intel® PETS reports</li> <li>Added support a new feature in Intel® PETS: <i>Results Worksheet Generation</i>. Refer to section 6.5 for more information.</li> </ul>	December 2017
9.4.8	<b>Chapter 5: Defining an Intel Platform for Testing</b>	November 2017



	Added an option to ignore S5/DS5 states on power off. This option is selected in the power settings when defining an Intel® CSME 12.0 desktop platform in the SUT creation wizard.	
9.4.0	<b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Added support for CNL-Y Mobile platforms and Mehlow WS.</li> <li>Updated Intel® ME FW version for SKL, KBL, KBL-R, BSF, and Purley platforms.</li> </ul> <b>Chapter 4: Installation</b> <ul style="list-style-type: none"> <li>Added section 4.4 for instructions on Installing and Uninstalling Intel® PETS in silent mode</li> </ul> <b>Chapter 5: Defining an Intel Platform for Testing</b> <ul style="list-style-type: none"> <li>Added section 5.2.2 for description on CLI for automated tests</li> <li></li> </ul>	October 2017
9.2.0	<b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Removed redundant section with instructions for setting up a non-Intel® AMT platform.</li> <li>Updated the SUT OS requirements for supported Windows* 7 32/64 bit systems - Supported only with IE 9 and above.</li> </ul> <b>Chapter 7: Changing Package Parameters</b> Added a search box in the Package parameters dialog that filters package parameters by name.	August 2017
9.1.0	<ul style="list-style-type: none"> <li><b>Added support for CFL Desktop S and CNL Mobile U platforms</b></li> </ul>	July 2017
9.0.0	<b>Initial release based off Intel® PETS User Guide revision 8.22.0</b> <b>Chapter 2: System Requirements</b> <ul style="list-style-type: none"> <li>Added Intel® CFL CSME 12.0 Support</li> </ul> <b>Chapter 5: Defining an Intel Platform for Testing</b> <ul style="list-style-type: none"> <li>Added “Compare Deep Sx Configuration” test case to the Setup Environment test package to compare the DeepSx settings configured in BIOS with the selected DeepSx settings for the SUT.</li> <li>Added “Verify BKC Version” test case in the Setup Environment test package to compare the installed drivers on the SUT with the Best Known Configuration (BKC).</li> <li>Updated the feature selection dialog of the SUT creation wizard to allow users to select features that are enabled on the SUT instead of those that are not supported.</li> </ul> <b>Chapter 6: Running Tests with Intel® Platform Enablement Test Suite</b> <ul style="list-style-type: none"> <li>Added test step (Verify Platform Devices) at the beginning of each test to be run first.</li> </ul> Added tooltips for the HTML test results report prompting the user to filter the results accordingly.	June 2017





# 1 Introduction

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The purpose of this document is to describe the installation of Intel® Platform Enablement Test Suite (Intel® PETS) and how to use it to test Intel® Management Engine (Intel® ME) and Intel® Trusted Execution Engine (Intel® TXE) supported platforms (desktop, mobile and workstation).

## 1.1 Overview

This chapter provides a brief description of the Intel® Platform Enablement Test Suite. It contains the following sections:

- [Introduction to Intel® Platform Enablement Test Suite](#)
- [Package Contents](#)
- [Installation, Setup, and Testing Flow](#)

### 1.1.1 Introduction to Intel® Platform Enablement Test Suite

The Intel® Platform Enablement Test Suite is a one-stop GUI application for the comprehensive automated testing of Intel® ME and Intel® TXE firmware. The Intel® Platform Enablement Test Suite automates a large number of the tests in the Compliance Guide, significantly reducing testing time. The Intel® Platform Enablement Test Suite can be used by OEMs, ODMs, and Intel testing teams.

The Intel® Platform Enablement Test Suite is run on a testing machine that has access, via LAN or WLAN, to an Intel® ME/TXE System under Test (SUT), including its file system. The Intel® Platform Enablement Test Suite executes tests on the SUT and displays the test results, clearly indicating the tests that succeeded and those that failed.

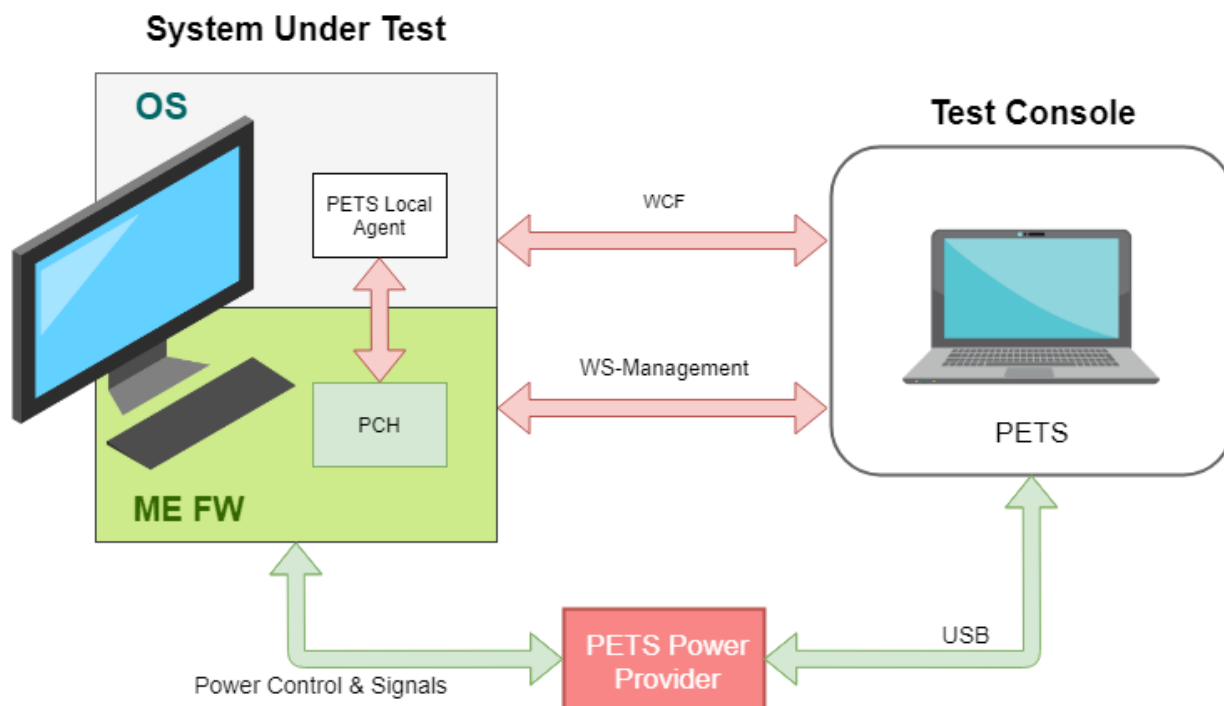
For the tests that require the performance of power-related operations, the Intel® Platform Enablement Test Suite should be connected to the SUT through one of the following methods:

- If the tests require many power-related operations - the Intel® Platform Enablement Test Suite should be connected to the SUT via a dedicated Intel® PETS Automated Power Provider (Ex: Intel® APS).
- If the tests require just a few power-related operations - the Intel® Platform Enablement Test Suite can be connected directly to the SUT.

The following diagram shows an architectural overview of the Intel® Platform Enablement Test Suite environment:

Figure 1-1. Testing Environment Diagram

## Testing Environment



### 1.1.2 Package Contents

The package contains the following:

- Intel® PETS application.
- Test Packages (including test sets, XMLs, and local agent).
- Documentation:
  - Intel® Platform Enablement Test Suite *Installation and User Guide* (this document).

### 1.1.3 Installation, Setup, and Testing Flow

To be able to use the Intel® Platform Enablement Test Suite, follow these steps:

1. Check the requirements for the testing machine and for the SUT. For details, see [Chapter 2, "System Requirements"](#).
2. Set up and configure the Intel AMT machine. For details, see [Chapter 3, "Setting Up the Intel® AMT Platform"](#).
3. Install the Intel® Platform Enablement Test Suite on the testing machine. For details, see [Chapter 4, "Installation"](#).
4. connect and set up the Automated Power Switch (Ex: see the Intel Automated Power Switch (Intel® APS) Connection and Setup Guide for OEMs)

5. Configure the connection of the Intel® Platform Enablement Test Suite with the "System under Test". For details, see [Chapter 5, "Defining an Intel® Platform for Testing"](#).
6. Run the "SetupEnvironmentTest" test package to validate all connections and their functionality. For details, see [Chapter 5, "Defining an Intel® Platform for Testing"](#).
7. Run tests. For details, see [Chapter 6, "Defining an Intel® Platform Enablement Test Suite"](#).
8. View Test Results. For details, see [Chapter 6, "Viewing, Saving, and Sending Test Reports"](#).
9. Adjust the package parameters of the test package where necessary. For details, see [Chapter 7, "Changing Package Parameters"](#).
10. Add unique SPI flash parts specifications to be checked during related tests. For details, see [Chapter 8, "Changing General Parameters"](#).

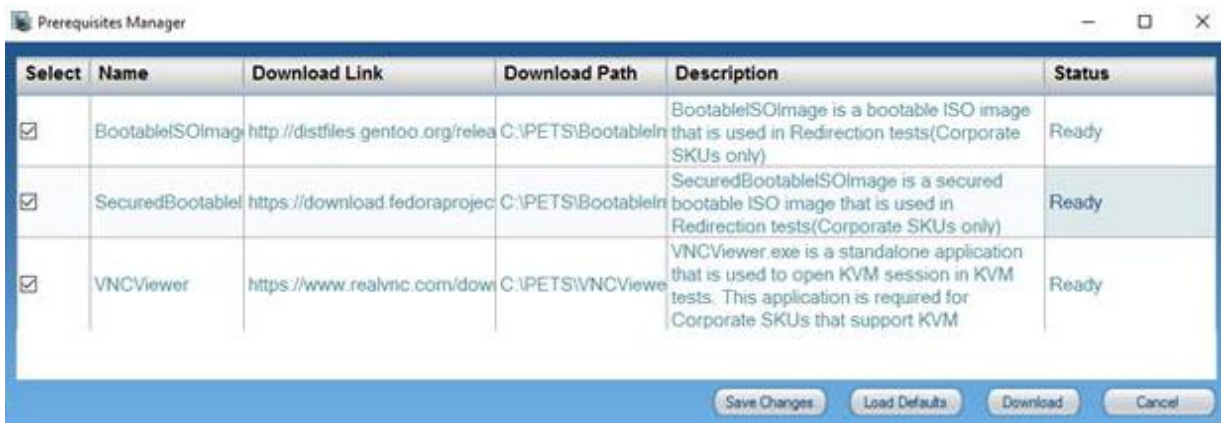
## 1.1.4 Intel® PETS Prerequisites Manager

### 1.1.4.1 Automatic Configuration for Prerequisites Manager

1. In main PETS GUI - Go to Tools > Prerequisites Manager.



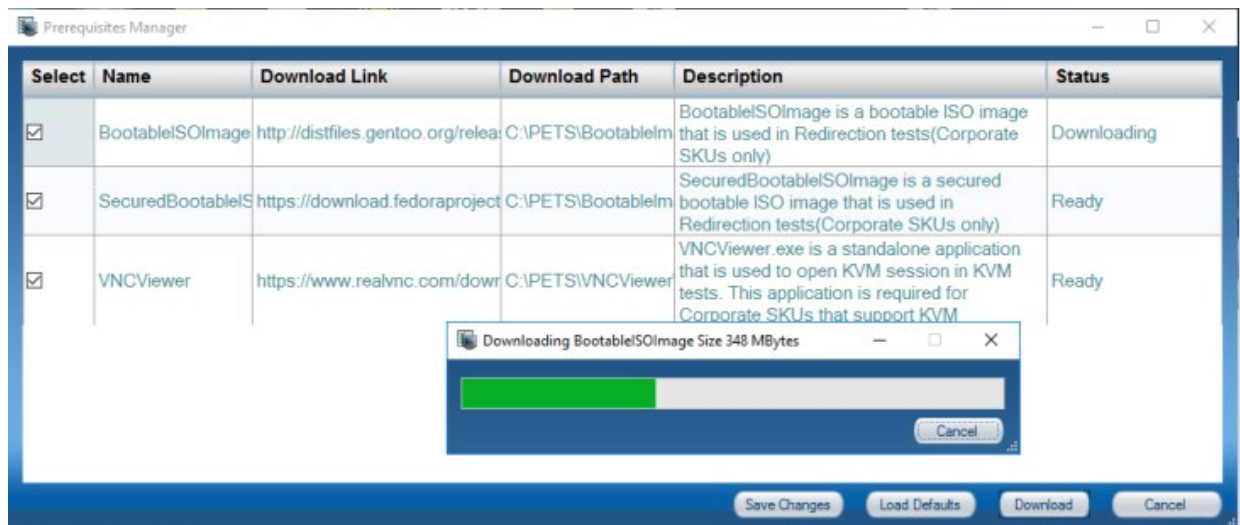
2. The Prerequisites Manager dialog will be displayed:



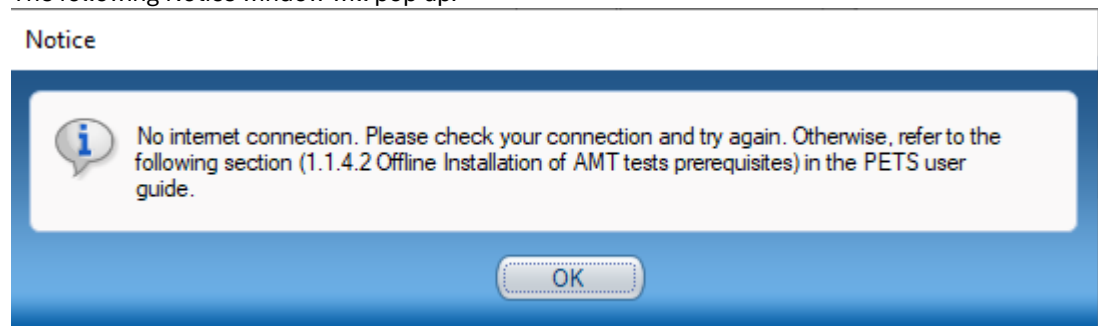
- The files specified in the dialog are required for Intel® AMT – KVM & SOL testing:
  - Secured ISO image file required for AMT\_036 test.
  - Bootable ISO Image file required for SOL tests.



- VNC Viewer executable file required for KVM tests.
- For each file, the download link and the download path are specified in the dialog with a brief description.
- The “Status” column shows the wording “Ready”, indicating the files are ready to be downloaded.
- The Dialog has 4 buttons:
  - **Load Defaults:** This will revert the “download link” which is an “editable field” to its original value in case the user changed it and wants to get back to the default link.
  - **Download:** This will start the download process. Make sure you are connected to the Internet before starting the download process.
  - **Cancel:** To cancel the process & close this dialog.
  - **Save Changes:** To save the changes the user made on the “Download link”.
- 3. Click download to start downloading, a progress dialog will appear.

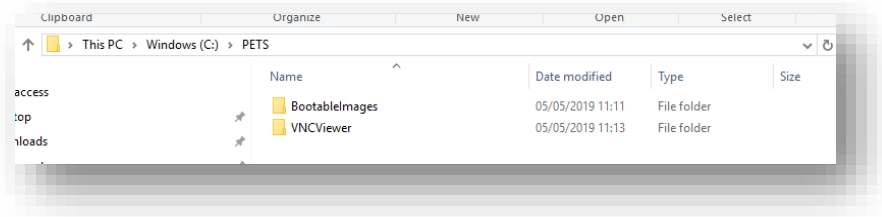


**Note:** The prerequisites Manager window will not open in case there is no internet connection. The following Notice window will pop up:



Once the process is finished, the status column will be updated with either “Succeeded” indicating the download is successfully complete and located in c:\PETS directory, or “Failed” indicating the download has been interrupted due to one of the following errors:

- o Internet Connection is lost during the download process.
  - o There's no space left on the Disk.
4. If the download is successfully complete, the files should be located in folders in the directory c:\PETS.



#### 1.1.4.2 Offline Installation of AMT tests prerequisites

In case the Management Console doesn't have an internet connection, the user has to do the following steps in order to install the following prerequisites:

##### 1. VNC Viewer:

- 1- Download VNC Viewer standalone executable version (x86) from <https://www.realvnc.com>  
For Example:

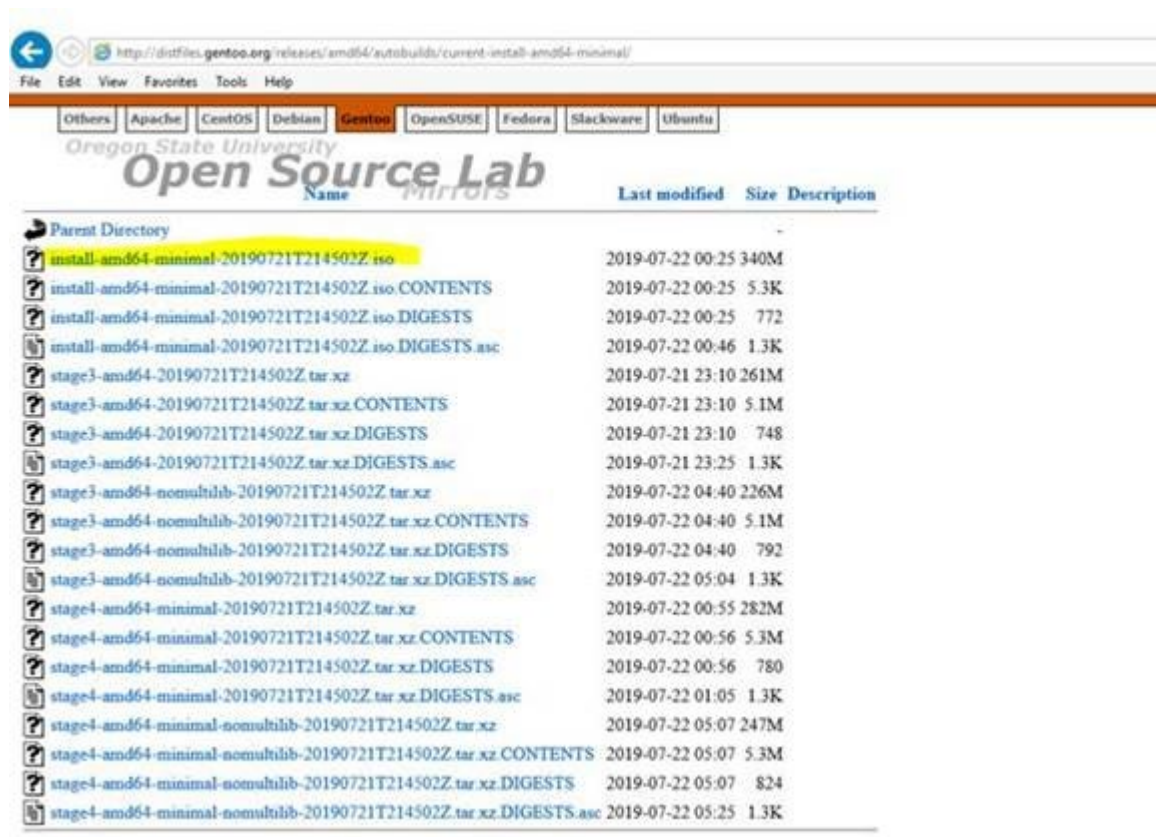
<https://www.realvnc.com/download/file/viewer.files/VNC-Viewer-6.19.715-Windows-32bit.exe>

- 2- Copy the download executable file to the Management Console
- 3- Place the downloaded executable file into "C:\PETS\VNCViewer\" directory
- 4- Rename the downloaded file to "VNCViewer"

**Note:** It is recommended to use VNC viewer version 6.19.715 or higher since the earlier versions (i.e 5.x) are not supported.

##### 2. Bootable ISO Image file:

- 1- Open <http://distfiles.gentoo.org/releases/amd64/autobuilds/current-install-amd64-minimal/>
- 2- Locate "install-amd64-minimal-[buildDate].iso" where the "buildDate" is the number of the build used for the ISO, e.g. install-amd64-minimal-20190721T214502Z.iso



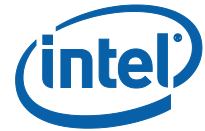
- 3- Click on the file to download
- 4- Copy the downloaded ISO image to the Management Console .
- 5- Place the downloaded Image file into "C:\PETS\BootableImages\" directory
- 6- Rename the downloaded Image file to "Non\_Secure\_Redirection\_Image.iso"

**Note:** Users can use any ISO images as long as the boot menu of the image is text only (since Putty supports texts only) by following these steps:

1. Copy the downloaded ISO image to the Management Console .
2. Place the downloaded Image file into "C:\PETS\BootableImages\" directory.
3. Rename the downloaded Image file to "Non\_Secure\_Redirection\_Image.iso"

### 3. Bootable Secured ISO image:

- 1- Open <https://getfedora.org/en/server/download/>
- 2- Download a secure ISO image from the link:  
e.g. Fedora 31: Netinstall ISO image for x86\_64
- 3- Copy the downloaded ISO image to the Management Console .
- 4- Place the downloaded Image file into "C:\PETS\BootableImages\" directory
- 5- Rename the downloaded Image file to "Secured\_Redirection\_Image.iso".



## 2 System Requirements

---

This chapter describes the requirements for installing and running the Intel® Platform Enablement Test Suite. The following topics are included:

- [Testing Machine Requirements](#)
- [Requirements for "System Under Test"](#)

### 2.1 Testing Machine Requirements

This section describes the requirements of the testing machine.

- Operating system: Windows\* 7 32 or 64 bit (with IE 9 and above) with SPI , Windows\* 8 32 or 64 bit, Windows\* 8.1 32 or 64 bit, or Windows\* Pro 10 32 or 64 bit.
- .Net framework 4.8 needs to be installed on the testing machine
  - Here is a list of windows releases and the .NET framework version which was included in each release.  
<https://blogs.msdn.microsoft.com/astebner/2007/03/14/mailbag-what-version-of-the-net-framework-is-included-in-what-version-of-the-os/>
  - Please note that PETS installers also checks for .NET 4.8 and if not installed, it should bring a dialog message asking to install it before starting the installation.
  - If the users want a manual steps to verify the .NET version installed on any OS, they can check the registry, see this link for more details:  
<https://docs.microsoft.com/en-us/dotnet/framework/migration-guide/how-to-determine-which-versions-are-installed>
- Management Console should have The Visual C++ Redistributable Packages(x86 & x64) installed.
- Microsoft\* .NET Framework 4.8 (downloadable from this <https://dotnet.microsoft.com/download/dotnet-framework/net48>).
- A network connection.
- The testing computer must be connected to the Automated Power Switch via its USB port (Ex: Intel® APS 3.x)
- Reporting. In order to avoid an Active X popping up when you open a report with a web browser, you must set your browser to allow active content to run on your computer. To set Internet Explorer to allow active content:
  - a. Choose **Tools > Internet Options**; the Internet Options dialog appears.
  - b. Select **Advanced > Security > Allow active content to run in files on My Computer**.
  - c. Click **OK**.

**Note:** Intel® Platform Enablement Test Suite has been designed and tested using the default Windows\* theme and colors. Using other themes and colors may result in making some buttons invisible.



## 2.2 Requirements for the "System Under Test"

This section describes the requirements for the system you are testing. The following table details the currently supported SUT platforms in Intel® PETS.

Refer to the latest Intel® PETS Release Notes for more information on Intel® PETS versions supporting current and legacy platforms.

Platform	PCH	CPU	Intel® ME FW	Platform version in PETS SUT Creation wizard	OS Support
Elkhart Lake (EHL)	MCC	EHL	Intel® CSE 15.40	CSE15.40 ElkartLake	Windows* 10
JasperLake (JSL)	ICL	EHL	Intel CSME 13.50	CSME13.50 JasperLake	Windows* 10
Tiger Lake (TGL)	TGL-LP	TGL-U	Intel® CSME 15.0	CSME 15 TigerLake	Windows* 10
Comet Lake (CML-V)	CML-V	CML-S	Intel CSME 14.5	CSME14.5 CometLake	Windows* 10
Comet Lake (CML)	CML_LP CML-H	CML-U/S/H	Intel® CSME 14.0	CSME 14 CometLake	Windows* 10
Ice Lake (ICL)	ICL_LP	ICL-U	Intel® CSME 13.0	CSME13 IceLake	Windows* 10
Whiskey Lake (WHL)	CNL_LP	WHL-U	Intel® CSME 12.0	CSME12 WhiskeyLake	Windows* 10
Cannon Lake (CNL)	CNL-LP	CNL-U/Y	Intel® CSME 12.0	CSME 12 CannonLake	Windows* 10
Coffee Lake (CFL)	CNL-H CNL-LP	CFL-U/ S/ H	Intel® CSME 12.0	CSME 12 CoffeeLake	Windows* 10
Mehlow WS	CNL-H	CFL-S	Intel® CSME 12.0	CSME 12 CoffeeLake	Windows* 10
Gemini Lake (GLK)	N/A	GLK A1	Intel® TXE 4.0	GLK	Windows* 10
Apollo Lake (APL)	N/A	Intel® Celeron/ Pentium Processors	Intel® TXE 3.0	APL	Windows* 7 (64 bit) Windows* 10

The following are requirements of the SUT:

- Microsoft® .NET Framework 4.8 (downloadable from this [Framework Link](#) )





- Provisioned platform (except for specific tests that require an un-provisioned system as the starting state).
- Intel® ME/TXE drivers (LMS, Intel® Management Engine Interface (Intel® MEI), SOL, WMIProvider, TXEI\_Driver) must be installed on the platform from the firmware kit.
- Graphics driver that suits the video card.
- A network connection.
- Wireless connection to the network (for applicable platforms only).
- For Intel® ME/TXE platforms, the Intel® System Scope Tool (Intel® SST) is required. The Intel® SST should be installed on the SUT. The SysScope installer is available in the Compliance kit.

**Note:** Intel® PETS currently supports Intel® SST version 3.x and will no longer supports older versions of the tool.

**Note:** In case Intel® SST was not installed on the SUT, Intel® PETS will continue to run tests and a failure dialog will NOT appear to the user. However, the report viewer will display a log message under the "System under Test" section that states "System Scope tool is Not Installed".

**Note:** In case Intel® PETS encounters issues with Intel® SST (i.e. Installation, running the tool), or an unsupported version of the tool was being used, Intel® PETS will try to run the tool for one minute. If these issues persist to occur, a failure message dialog box will pop up to the user. "System Scope tool failed to run" will be displayed in Intel® PETS logs in the report viewer "System under Test" section.

**Note:** Upon successful installation and running of Intel® SST, a link will be displayed in the log message under "System under Test" so that the user can view the system scope information details.

## 3 Setting Up an Intel® AMT Platform

This chapter describes how to set up an Intel® AMT platform for testing by the Intel® Platform Enablement Test Suite. The following topics are included:

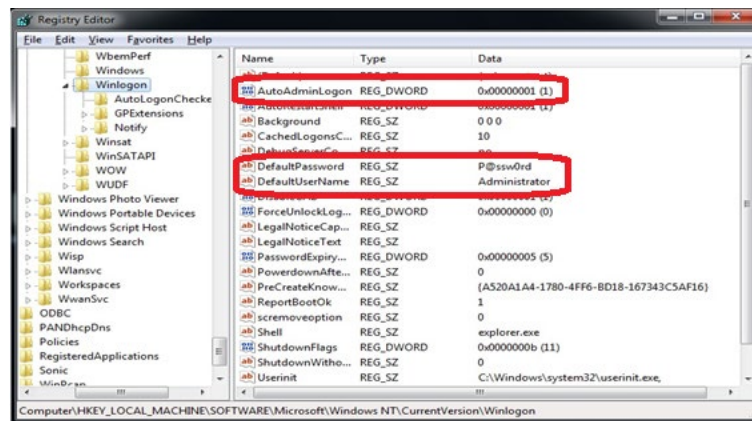
- [Setup.](#)
- [Setting up non-Intel® AMT Platforms.](#)

### 3.1 Setup

To enable the correct flow of tests in the Intel® Platform Enablement Test Suite, do the following on Intel® ME platform:

1. Create a Windows\* admin user ,or use an existing user and assign a password to that user.
2. Set up Auto-Login using this user as follows:
  - a. Click the Start Menu then type “regedit” and hit enter.
  - b. Add the following entries and values to **HKEY\_LOCAL\_MACHINE -> Software -> Microsoft -> Windows NT -> CurrentVersion -> Winlogon**:
    - AutoAdminLogon (DWORD): 1
    - DefaultUserName (string): Administrator user name
    - DefaultPassword (string): Password for the Administrator user

After you have made the changes, the Registry should look like the following screenshot:



3. Disable Windows\* firewall from the Device Manager entry.
4. If you are not using an Intel® NIC and want to run tests using the Host Wake On LAN feature, configure the LAN Driver as follows:
  - a. Right-click **My Computer** and choose **Manage**; the **Computer Management** dialog appears.
  - b. Select **Device Manager** in the left pane and expand **Network adapters** in the right pane.
  - c. Configure your network adapter to wake on magic packet and not wake on direct packet. Each driver will have different steps for enabling the 'Wake on Magic Packet' feature.

5. Choose **Start > Settings > Control Panel > Administrative Tools > Local Security Policy > Local Policies > Security Options > Network Access: Sharing and security Model for local accounts** and select **Classic** from the drop-down list.
6. Configure the system to not require a password on wakeup, and to shut down when the Power button is pressed. For example, to do this in Windows\*:
  - a. Open the Control Panel and choose **System and Maintenance > Power Options**.
  - b. Click the **"Require a password on wakeup"** link.
  - c. Select **"Don't require a password"** in the **"Password protection on wakeup"** section.
  - d. Select **"Shutdown"** in the **"When I press the power button"** list.
7. Make sure that Intel® AMT platform is provisioned with a host name and domain. (You can use any strings for the two fields - they do not need to be valid names). You can do this in a number of ways:
  - Enter the values in the System Name Settings section of the Web UI.
  - Enter the values via Intel® Management Engine BIOS Extension (Intel® MEBX) by choosing CTRL-P during the boot sequence.
  - Enter the values with a setup and configuration server.
8. Enable Host Wake On LAN (magic packet) on the system under test:
  - a. Enable Host Wake on LAN in BIOS.
  - b. Open the Device Manager.
  - c. Double-click on the Eth device; the device's Properties dialog appears.
  - d. Follow the instructions for your operating system:
    - i. Select the **"Power Management"** tab.
    - ii. Enable **"Wake on Magic Packet"** and **Wake on Magic Packet from off state"**.
    - iii. Disable **"Wake on Link"**.



## 4 Installation

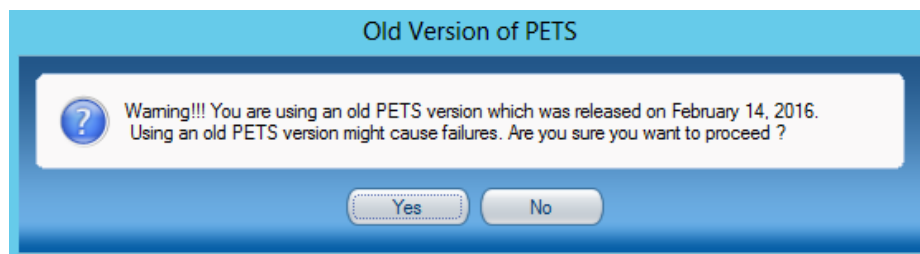
This chapter describes how to install “Intel® Platform Enablement Test Suite” and the “local agent”. The following topics are included:

- [Installing Intel® Platform Enablement test Suite](#)
- [Installing the Local Agent on Windows\\*](#)
- [Installing the Local Agent on Android \(TXE platforms only\)](#)
- [Post Validation Verification \(APS\)](#)

### 4.1 Installing Intel® Platform Enablement Test Suite

1. Verify that the testing computer meets the system requirements. For details, see [Testing Machine Requirements](#).
2. Make sure you are logged into the testing computer as an Administrator.
3. Extract the Intel® Platform Enablement Test Suite files to a folder on the testing computer.
4. Locate and double-click the installation file (**<installation package>\Console\PETSConsole.msi**) to launch the Intel® Platform Enablement Test Suite Install Shield Wizard.
5. Click **“Next”** in the Welcome screen.
6. Click **“I accept the terms of the license agreement”** and **“Next”** in the License Agreement screen.
7. Click **“Next”** in the Choose Destination Location screen to install Intel® Platform Enablement Test Suite in the default installation folder. To install in a different location, click **“Change”**. The default installation folder is **“C:\Program files\Intel\Intel(R) Platform Enablement Test Suite”**.
8. Click **“Next”** in the Setup Type screen.
9. Click **“Install”** in the **“Ready to Install the Program”** screen to begin installing Intel® Platform Enablement Test Suite; a progress bar appears showing the status of the installation.
10. Click **“Finish”** in the **“Install Shield Wizard Complete”** screen to exit Intel® Platform Enablement Test Suite Setup wizard.

**Note:** Starting from Intel® PETS version 8.19.0, a warning message will appear to the user if the release date of Intel® PETS build in use is older than 60 days. The warning message will ask for confirmation if the user chooses to proceed using the outdated build.



## 4.2 Installing the Local Agent on Windows\*

The local agent must be installed on each SUT.

### To install the Local Agent:

1. Put the Intel® Platform Enablement Test Suite files on the SUT in one of the following ways:
  - Extract the Intel® Platform Enablement Test Suite files to a folder on the SUT.
  - Copy the PETSLocalAgent folder over to the SUT.
2. Locate and double-click this installation file on the SUT: **<installation package>\Agent\PETSLocalAgent.msi**
3. Configure the Windows\* Event Viewer to Overwrite events as needed in the following way:
  - a. Choose **Control Panel > Administrative Tools > Event Viewer** to open the Event Viewer.
  - b. Choose **Action > Properties** to open the Properties dialog.
  - c. Select the **Overwrite events as needed** option and click **OK**.

**Note:** The **Overwrite events as needed** option is selected by default in Windows\* 7 and Windows\* 8 (SUT only).

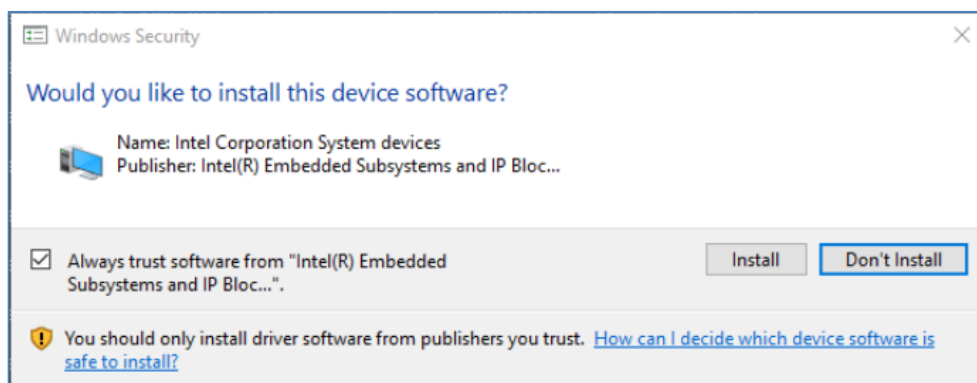
## 4.3 Installing and Uninstalling Intel® PETS in silent mode

To install Intel® PETS Console in silent mode, navigate to the Console folder in the downloaded Intel® PETS kit and run the following command "**msiexec /i PETSConsole.msi /qn**", where:

- **/i** parameter for installing the specified .msi file.
- **/qn** parameter for a silent installation that displays no user interface.

To install Intel® PETS Local Agent in silent mode, navigate to the Agent folder in the downloaded Intel® PETS kit and run the following command "**msiexec /i PETSLocalAgent.msi /qn**"

**Note:** The following dialog appears requesting confirmation from the user only for first time installation of Intel® PETS agent on a clean OS:



To uninstall Intel® PETS Console in silent mode, navigate to the Console folder in the downloaded Intel® PETS kit and run the following command "**msiexec /x PETSConsole.msi /qn**", where:

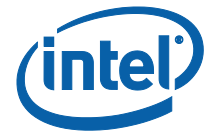
- **/x** parameter for uninstalling the specified .msi file.



To uninstall Intel® PETS Agent in silent mode navigate to the *Agent* folder in the downloaded Intel® PETS kit and run the following command “**msiexec /x PETSlocalAgent.msi /qn**”

## 4.4 Post Installation Verification

After you have installed and set up Intel® Platform Enablement Test Suite, you can use Automated Power Provider Software (Ex: Intel® APS ) to verify that it is properly connected by performing power transitions on the SUT.



## 5 Defining an Intel Platform for Testing

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This chapter describes how to define an Intel platform for testing by the Intel® Platform Enablement Test Suite. This must be done before running tests on that platform.

This chapter covers the following topics:

- [Opening Intel® Platform Enablement Test Suite](#)
- [Opening Intel® Platform Enablement Test Suite from the Command Line](#)
- [Defining the Intel® System Under Test](#)
- [Systems Under Test Dialog](#)
- [Testing the Configuration of the SUT](#)

**To configure Intel® Platform Enablement Test Suite, perform the following:**

1. Open Intel® Platform Enablement Test Suite.
2. Configure the system under test (SUT) by defining its parameters.
3. Test the connectivity with the SUT.
4. Test the setup of the SUT.

**Note:** You should also run the connectivity and setup tests whenever there is a change in the environment (e.g., you select another SUT to be tested).

### 5.1 Opening Intel® Platform Enablement Test Suite

Choose **Start > Programs > Intel > Intel® Platform Enablement Test Suite**; the Intel® Platform Enablement Test Suite window appears.

### 5.2 Opening Intel® Platform Enablement Test Suite from the Command Line

#### 5.2.1 Command line for semi-automated tests

Using the help option “-?” will list all the supported options:

- Run and Close.
- Open package in GUI.

Follow these guidelines to run those tests using the Command Line:

1. Open a new command line window as an administrator.
2. Navigate to the PETS installation director



```
Administrator: C:\Windows\System32\cmd.exe
Microsoft Windows [Version 6.1.7601]
Copyright (c) 2009 Microsoft Corporation. All rights reserved.

C:\windows\system32>cd\

C:\>cd C:\Program Files (x86)\Intel\Intel(R) Platform Enablement Test Suite
```

3. Type one of the following commands:

a. For **Run and Close** test:

Run\_PETS.bat -run -package "full package path" -sut "SUT Name in PETS".

b. For **Open package in GUI** test:

Run\_PETS.bat -Open -package "full package path" -sut "SUT Name in PETS".

Where:

- The "**full package path**" is the full path for the package you want to run.
- The "**SUT Name in PETS**" is the name of the SUT that you should define in PETS before you run the test. This SUT can only be defined in the GUI mode (but not in the command line mode).

Intel® PETS runs and closes automatically when execution finishes. If all the tests pass, an Exit Code of 0 is returned. If one or more tests fail, the Exit Code is 1, and if there is a configuration error, the Exit Code is -1. For example:

If you have a package on the C drive named **tests.xml** and you have defined a SUT in PETS with the name **SUT**, then type the following command to run your package: **Run\_PETS.bat -run -package "C:\tests.xml" -sut "SUT"**. Press <Enter> to begin execution.

## 5.2.2 Command line for automated tests

CLI for automated tests runs the tests in silent mode. It can be launched by following these steps:

1. Open a new command line window as an administrator.
2. Navigate to Intel® PETS installation directory.

```
Administrator: C:\Windows\System32\cmd.exe
Microsoft Windows [Version 6.1.7601]
Copyright (c) 2009 Microsoft Corporation. All rights reserved.

C:\windows\system32>cd\

C:\>cd C:\Program Files (x86)\Intel\Intel(R) Platform Enablement Test Suite
```

3. Type the following command to get usage: **PETSCommandLine.exe -?** Or **PETSCommandLine.exe -help**

### 5.2.2.1. The command line syntax for Intel® PETS executable

**PETSCommandLine.exe** [-? | -help] [-sutimport "SUT file path"] [-suts] [-sutsettings -sutselect "name of sut"] [-sutinfo -sutselect "name of sut"] [-testconn -sutselect "name of sut"] [-testmeconn -sutselect "name of sut"] [-getdeepsex -sutselect "name of sut"] [-package "package file path" -tests] [-package "package file path" -teststeps "test name"] [-package "package file path" -tests -sutselect "name of sut" -run] [-package "package file path" -sutselect "name of sut" -runtest "test name"] [-package "package file path" -sutselect "name of sut" -testcase "test name" -runsubtest "sub test name"] [-repeat "number of times to run the test"] [-failurebehavior "behavior when test fail"] [-htmllog] [-spreadsheet]





The following table explains the command line options:

Option	Description	How to use?
<b>-?/-help</b>	Displays all the arguments supported in PETS CLI.	-? Or -help
<b>-sutimport</b>	Imports any SUT file to Intel® PETS.	-sutimport "SUT file path"
<b>-suts</b>	Displays all SUTs in the DefaultSut.xml file.	-suts
<b>-sutsettings</b>	Displays SUT information, platform version, type, SKU and network, Intel® ME FW version, Power, Intel® APS settings, and features that are enabled.	-sutsettings -sutselect "SUT Name"
<b>-sutinfo</b>	Retrieves the following information about the SUT: Firmware, MEBx, LMS, BIOS, MEI Driver, GFX Driver, LAN Driver Versions, and Configuration State.	-sutinfo -sutselect "SUT Name"
<b>-testconn</b>	Tests the connectivity with Local Agent, that includes: 1. Ping Local Agent. 2. Verify communication with Local Agent. 3. Local agent version matches to PETS version.	-testconn -sutselect "SUT Name"
<b>-testmeconn</b>	Tests Intel® ME connectivity.	-testmeconn -sutselect "SUT Name"
<b>-getdeepsex</b>	Gets the DeepSx configuration from BIOS and saves the setting in DefaultSut.xml.	-getdeepsex -sutselect "SUT Name"
<b>-tests<sup>(1)</sup></b>	Prints all test within the selected package.	-package "Path\PackageName.xml" -tests
<b>-teststeps<sup>(1)</sup></b>	Prints all test steps within a test in the selected package.	-package "Path\PackageName.xml" -teststeps "Test Name"
<b>-run<sup>(1)</sup></b>	Runs all tests within the selected package.	-package "Path\PackageName.xml" -sutselect "SUT Name" -run
<b>-runtest<sup>(1)</sup></b>	Runs the selected test.	-package "Path\PackageName.xml" -sutselect "SUT Name" -runtest "Test Name"
<b>-runsubtest<sup>(1)</sup></b>	Runs the selected subtest.	-package "Path\PackageName.xml" -sutselect "SUT Name" -testcase "Test Name" -runsubtest "SubTest Name"
<b>-repeat<sup>(1)</sup></b>	Runs the selected Package/Test/Subtest more than 1 time (depends on user decision).	1. -package "Path\PackageName.xml" -sutselect "SUT Name" -run -repeat 2 2. -package "Path\PackageName.xml" -sutselect "SUT Name" -runtest "Test Name" -repeat 2 3. -package "Path\PackageName.xml" -sutselect "SUT Name" -testcase "Test Name" -runsubtest "SubTest Name" -repeat 2
<b>-failurebehavior<sup>(1)(2)</sup></b>	Decides the behavior upon failure of a subtest, a test, a package.	1. -package "Path\PackageName.xml" -sutselect "SUT Name" -run -failurebehavior "NextTest"



Option	Description	How to use?
		2. -package "Path\PackageName.xml" -sutselect "SUT Name" -runtest "Test Name" -failurebehavior "NextSubTest" 3. -package "Path\PackageName.xml" -sutselect "SUT Name" -testcase "Test Name" -runsubtest "SubTest Name" -failurebehavior "NextStep"
<b>-htmllog</b> <sup>(1) (3) (4)</sup>	Creates the HTML log for the running Package/Test/Subtest.	-package "Path\PackageName.xml" -sutselect "SUT Name" -run -htmllog -logspath "Path\LogFolder"
<b>-spreadsheet</b> <sup>(1) (3) (4)</sup>	Create the spreadsheet for the running Package/ Test/ Subtest.	-package "Path\PackageName.xml" -sutselect "SUT Name" -run -spreadsheet -logspath "Path\LogFolder"

<sup>(1)</sup> If Path was not provided as "Path\PackageName.xml", write only "PackageName.xml" the path will be the default folder as defined in SUT, example: ME\CNL\Windows\Consumer\Mobile\Wired\_Only

- <sup>(2)</sup>
- Failure behaviors supported for running package are: NextStep, NextSubTest, NextIteration, NextTest, Stop
  - Failure behaviors supported for running subtest are: NextStep, NextIteration, Stop
  - Failure behaviors supported for running test are: NextStep, NextSubTest, NextIteration, Stop

<sup>(3)</sup> -htmllog and -spreadsheet commands can be used together

<sup>(4)</sup> If the path was not provided as "Path\LogFolder", the logs will be saved in "PETS root\Logs\Log\_XMLs"

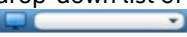
#### 5.2.2.2. Exit Codes

CLI returns 3 exit codes as follows:

- **0**: Test passed/command succeeded
- **1**: Test Failed
- **-1**: Error occurred

## 5.3 Defining the Intel® System Under Test

An Intel® SUT is an Intel platform that was defined for testing by the Intel® Platform Enablement Test Suite. In order to run a test package:


- An Intel® ME or TXE platform must be defined as a SUT in the Systems Under Test dialog.
- A SUT must be selected for testing from the drop-down list of SUTs in the Intel® Platform Enablement Test Suite window's toolbar's Platform field ().

You can only test one SUT at a time even if more than one SUT has been defined or imported for testing. The Platform field displays the name of the SUT being tested. All the SUTs that are available for testing by Intel® Platform Enablement Test Suite are listed in the Platform field's drop-down list. If the Platform field is empty, a SUT has not yet been defined or imported for testing.

If the wrong SUT is displayed in the Platform field, you can select a different SUT from the Platform field's drop-down list. If the correct SUT is not listed there, you can use the Systems Under Test dialog to add or import the correct SUT. The names of all the SUTs that have been defined or imported for testing in the Intel® Platform Enablement Test Suite are saved in the default SUT file (**defaultSut.xml**).

## 5.4 Systems Under Test Dialog

You can open the Systems Under Test dialog in the following ways:

- Select **SUT Management** from the drop-down list in the Platform field.
- Choose **Settings > Systems Under Test**.
- Click the Platform button (  ) in the toolbar.

The Systems Under Test dialog has up to seven tabs. Each tab defines a different set of parameters:

- SUT Overview - general data about SUT; Systems Under Test dialog opens to this tab by default.
- Intel® ME Settings - authentication data, IP and ability to enable and disable some logging features for Intel® ME.
- General Host Settings - contains the information used by the Intel® Platform Enablement Test Suite to connect to the local agent on the SUT.
- PETS Automated Power Provider - selects and configures the SUT's power provider.
- Power Settings - power settings of the SUT.
- System Information - displays the SUT's ME (or TXE) Information.

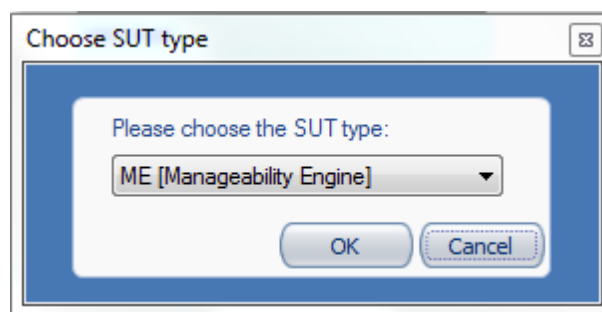
You can do the following in the Systems Under Test dialog:

- Add a new SUT to the Intel® Platform Enablement Test Suite and define its parameters.
- Select an already-defined SUT.
- Import an existing SUT definitions file.
- Redefine the parameters of an already-defined SUT.
- Delete a SUT from Intel® Platform Enablement Test Suite.
- Export the SUT definitions to a file.
- Check if the Intel® Platform Enablement Test Suite is properly connected to the SUT.

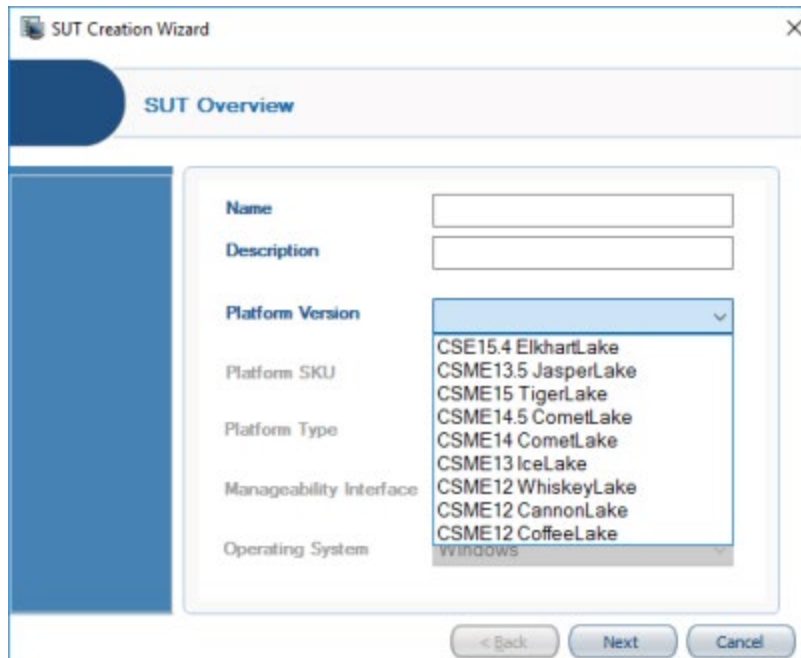
### 5.4.1 Adding and Defining a SUT

**To add a SUT and define its security settings:**

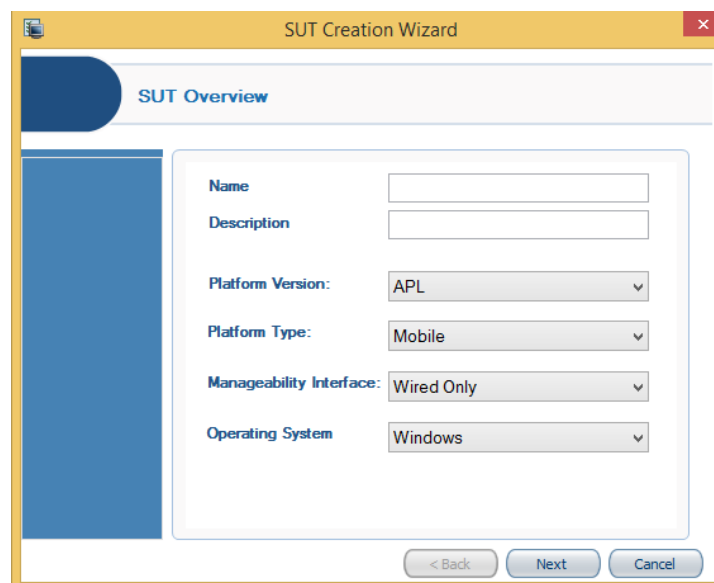
1. Open the Systems Under Test dialog. The Systems Under Test wizard opens.
2. Click the New button; a dialog prompts you to choose the type of SUT that you are defining. Select the SUT type you want (ME, TXE, or Simple) and click OK.

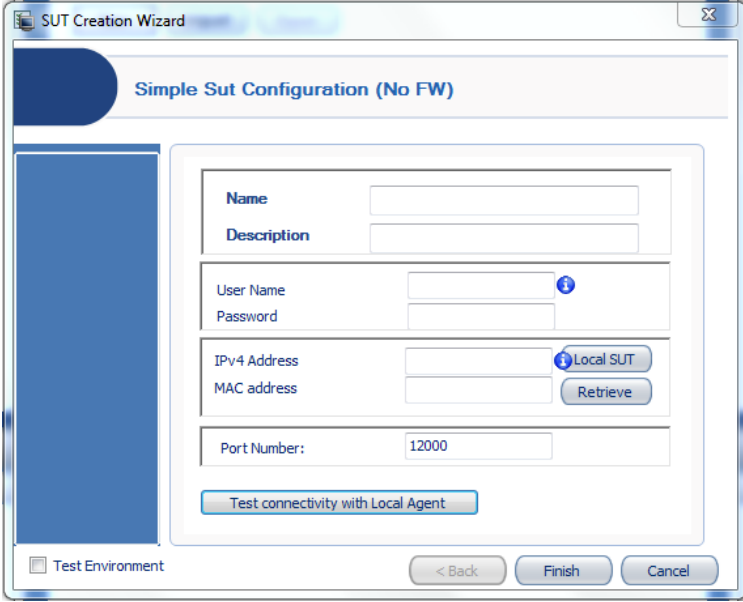


3. The SUT Creation Wizard opens to the Description tab.



**Note:** The SUT Creation Wizard "New" screen shows minor differences, depending on the SUT type that you previously selected in step #2. The figure above shows the configuration fields for an Intel® ME platform; the figure below shows the configuration fields for an Intel® TXE tablet platform. The next figure shows the configuration fields for simple SUT platforms. Throughout the SUT definition process, the configuration fields provided on each screen: SUT Overview, General Host Settings and Intel® PETS Automated Power Provider will vary according to the SUT type you choose.





**Note:** Simple SUT is appropriate when you want to work with a SUT regardless of its firmware, for example, if your tests are not related to a specific firmware feature. Simple SUT is a one-step wizard that only asks for operating system information.

4. Enter data about the SUT into the following fields:
  - **Name**
  - **Description**
  - **Platform Version**
  - **Platform SKU:** Select Corporate (used to be called 5MB) or Consumer (used to be called 1.5MB),
  - **Platform Type:** Select Desktop, Mobile or Server/Workstation system for Intel® ME platforms; Mobile or desktop for Intel® TXE platforms.
  - **Manageability Interface:** Select Wired Only, Wireless Only, Wired and Wireless for Intel® ME platforms; Wired Only or Wireless Only for Intel® TXE platforms.
  - **Operating System:** Windows\* is selected by default. This setting It is the only relevant OS that is currently supported for Intel® ME and TXE platforms.
  
5. Click **Next**; the following General Host Settings dialog appears:



SUT Creation Wizard

General Host Settings

User Name

Password

LAN IPv4 Address

MAC address  Retrieve

WLAN IPv4 Address

MAC address  Retrieve

Port Number: 12000

Test connectivity with Local Agent

☐ Test Environment

< Back Next Cancel

If the system has wired and wireless network connection, the following General Host Settings dialog appears:

SUT Creation Wizard

General Host Settings

User Name

Password

LAN IPv4 Address

MAC Address  Retrieve

WLAN IPv4 Address

MAC Address  Retrieve

Port Number: 12000

Test connectivity with Local Agent

☐ Test Environment

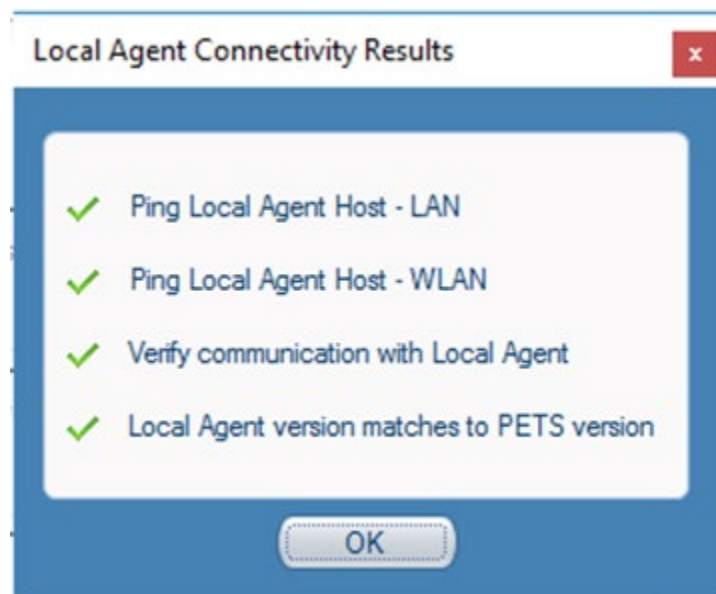
< Back Next Cancel

6. Enter data into the following fields:

- **User Name:** OS user name.
- **Password:** OS password.

**Note:** Replace the password with dots in OS and Intel® CSME credentials pages to prevent un-authorized access to information through keyboard caches and shared dictionaries

- **LAN IPv4 (WLAN IPV4):** IP address of the SUT's operating system.
- **Port Number:** The port that the local agent listens on. If this port is used by another application, you need to change the port number. To change the local agent's port number, open the **PETSlocalagnet.exe.config** file in the installation directory and change the **address** field so it contains the new port number. For example, to change the port to 60000, change the address line from `<add key="address" value="net.tcp://localhost:12000" />` to `<add key="address" value="net.tcp://localhost:60000" />`  
The default port for Windows\* is 12000 and for Android\* it is 6500.
- **LAN MAC address (WLAN MAC address):** LAN card MAC address for magic packet; either enter manually or press **Retrieve**.
- **Test connectivity with Local agent:** Clicking this option verifies whether the Intel® Platform Enablement Test Suite is able to connect with the PETS local agent on the SUT host and displays a dialog of Local Agent Connectivity Results. When verification is performed on a Wired and Wireless LAN Test package, an additional entry appears for the Host WLAN connectivity.



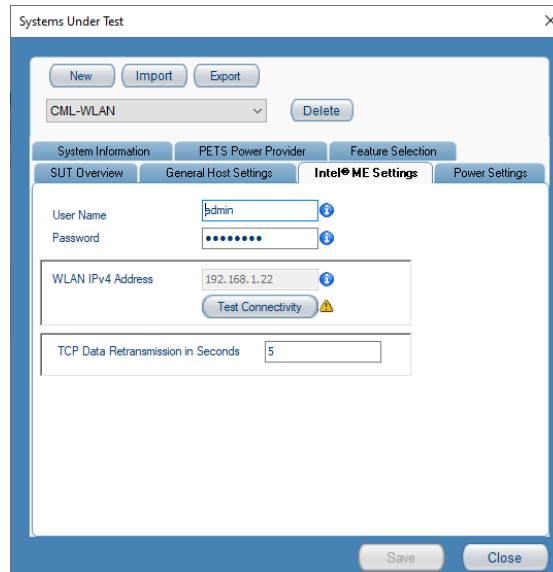
- **Test Environment:** If checked, on each screen in the SUT wizard flow where there is a test button, the test will be run before the **Next** button flow is executed. If unchecked, at the end of the wizard flow, the System Information tab will not be updated with data, until Refresh is pressed.



**Note:** The example screens shown in the following steps are for an ME test platform.

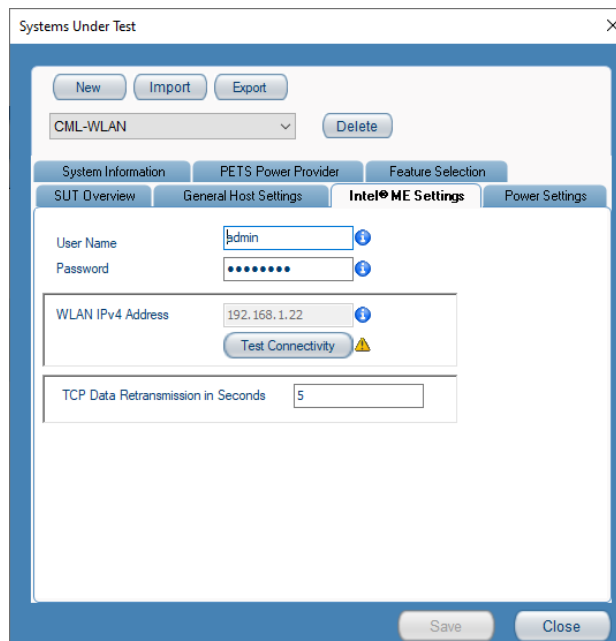


7. If your SUT is a corporate system, click **Next**; the Intel® ME Settings dialog will appear. If the SUT is a consumer system, continue with **step 12**.



The screenshot shows the 'Systems Under Test' dialog box with the 'Intel® ME Settings' tab selected. The dialog has a title bar with a close button (X). Below the title bar are buttons for 'New', 'Import', and 'Export'. A dropdown menu shows 'CML-WLAN' with a 'Delete' button next to it. Below this are four tabs: 'System Information', 'PETS Power Provider', 'Feature Selection', and 'Power Settings'. The 'Intel® ME Settings' tab is active, showing fields for 'User Name' (admin), 'Password' (masked with dots), 'WLAN IPv4 Address' (192.168.1.22), and 'TCP Data Retransmission in Seconds' (5). There is a 'Test Connectivity' button with a warning icon. At the bottom are 'Save' and 'Close' buttons.

If the system is connected through wireless only network interface, the following Intel® ME Settings dialog appears:



This screenshot is identical to the one above, showing the 'Systems Under Test' dialog box with the 'Intel® ME Settings' tab selected. The fields and buttons are the same: 'User Name' (admin), 'Password' (masked), 'WLAN IPv4 Address' (192.168.1.22), 'TCP Data Retransmission in Seconds' (5), and 'Test Connectivity' button. The 'Save' and 'Close' buttons are at the bottom.

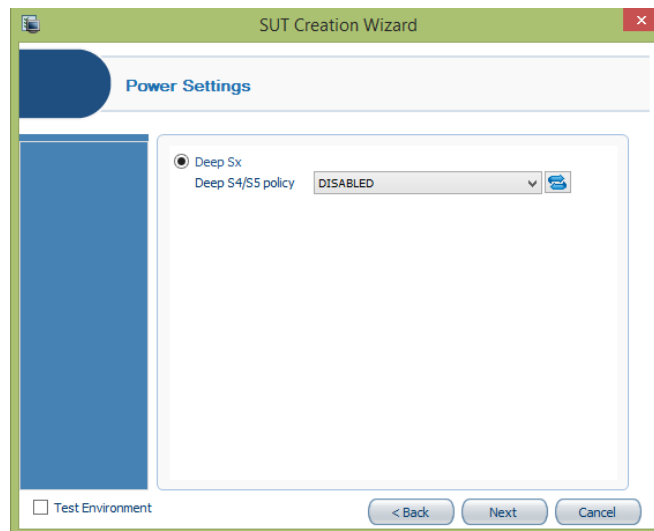


8. Enter the Intel® ME Settings in the fields provided:
    - **Digest Authentication (AMT/ME login):** Enter the user name and password of an Intel® AMT user on the SUT who is the default Admin user or another user that has Administrator rights on that system. (These rights are set via the Web UI or the Intel Setup and Configuration Service.)
      - This should be the user name and password of the Intel® AMT FW running on the Intel® ME, not the Host OS.
      - The Intel® AMT/ME password can be added during the initial MEBx (CTRL+P) session during the boot sequence.
      - The password must be changed after burning Intel FW or clearing CMOS.
    - **LAN IPv4 (or WLAN IPv4) address of the ME:**
      - If the platform uses a static IP address, enter the IP address of the ME as defined in the MEBx.
- Note:** Don't use the same static IP address for both Host OS and ME on the SUT.
- If the platform has a dynamic IP address, enter the IP address provided for the platform by the DHCP server.
- Click **Test ME connectivity** to check the ME connection.
9. Click **Test ME connectivity to PETS local agent** to check whether Intel® Platform Enablement Test Suite connects with the PETS local agent on the SUT host.
  10. If you are defining a corporate Wired and Wireless system, the following Intel® ME Settings dialog appears. For all other systems, continue with **step 12**.

The screenshot shows the 'Systems Under Test' dialog box with the 'Intel® ME Settings' tab selected. The dialog contains the following elements:

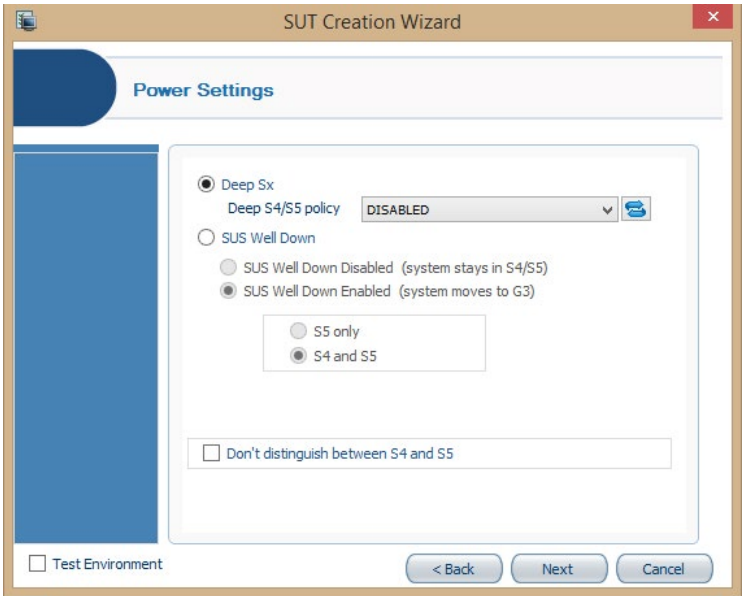
- Buttons: New, Import, Export, Delete, Test Connectivity, Save, Close.
- Dropdown menu: CML-WLAN.
- Tabs: System Information, PETS Power Provider, Feature Selection, SUT Overview, General Host Settings, **Intel® ME Settings**, Power Settings.
- Fields:
  - User Name: admin
  - Password: masked with dots
  - WLAN IPv4 Address: 192.168.1.22
  - TCP Data Retransmission in Seconds: 5

11. Enter the ME Wireless information in the fields provided.
  - **WLAN IPV4 Address.**
  - **TCP data retransmission in seconds:** minimum (default) value is 5 and maximum value is 7.
    - Tests affected by this new feature are AMT\_013, AMT\_031, AMT\_033, AMT\_035-038, AMT\_041, AMT\_043, AMT\_045, AMT\_047, AMT\_049-058, AMT\_082-083, and AMT\_085
12. Click **Test Connectivity** to check whether Intel® Platform Enablement Test Suite connects with the wireless ME.
13. Click **Next**:
  - If you are defining a desktop client platform (or workstation), the following dialog appears.

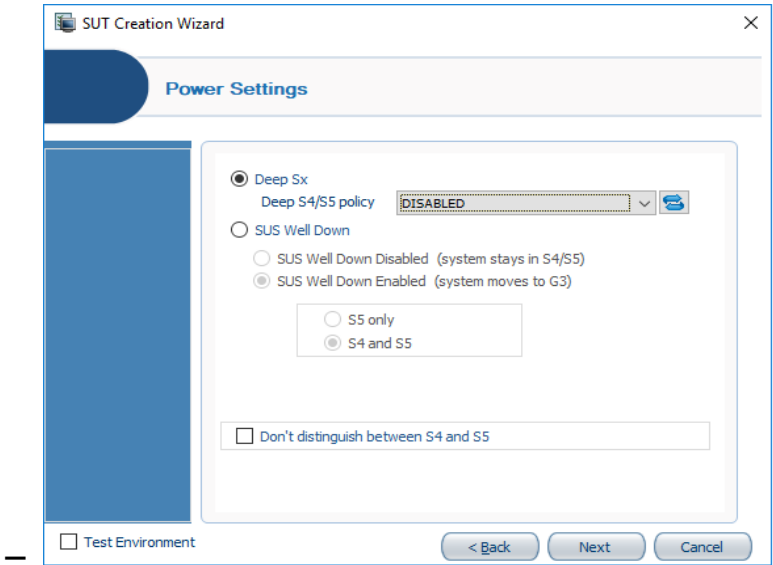


- If you're defining an Intel® CSME 12.0 desktop client platform, the following dialog appears

**Note:** When choosing the option "Don't distinguish between S4 and S5", the system states S5/DS5 will be ignored on power off.

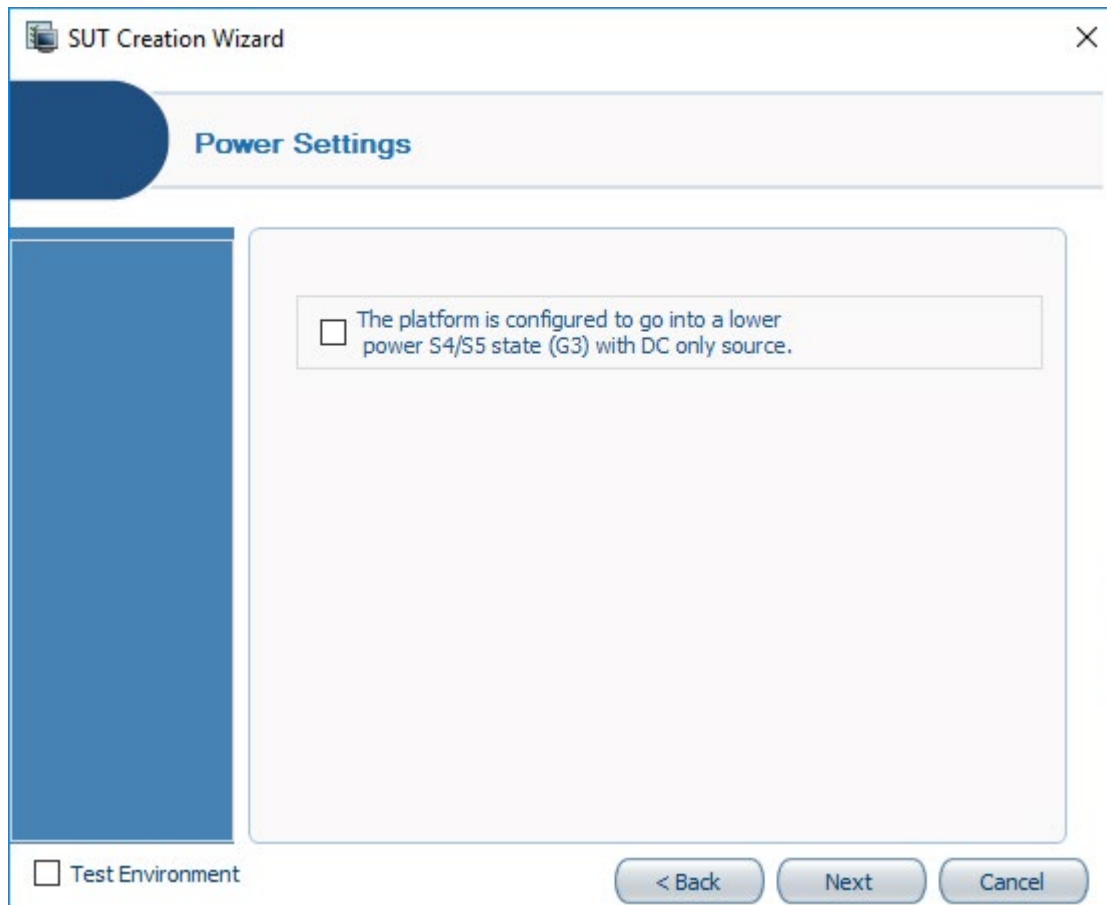


— If you are defining a mobile client platform, the following dialog appears.



— If you're defining a TXE mobile client platform, the following dialog appears:

**Note:** When choosing the option "The platform is configured to go into a lower power S4/S5 state (G3) with DC only source.", PETS will expect for hibernate either G3 or S4, and for power off either G3 or S5.



The screenshot shows the "SUT Creation Wizard" window with the "Power Settings" tab selected. The window has a blue header bar with the title "SUT Creation Wizard" and a close button. Below the header, the "Power Settings" tab is active. The main content area contains a checkbox labeled "The platform is configured to go into a lower power S4/S5 state (G3) with DC only source." which is currently unchecked. At the bottom left, there is a checkbox labeled "Test Environment" which is also unchecked. At the bottom right, there are three buttons: "< Back", "Next", and "Cancel".



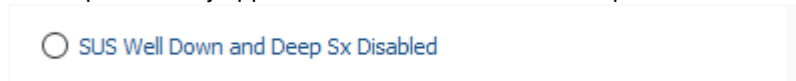
In the Power Settings dialog, select the appropriate options to configure the SUT's power provider. The fields can be understood as follows:

**For Desktop or Workstation platforms:**

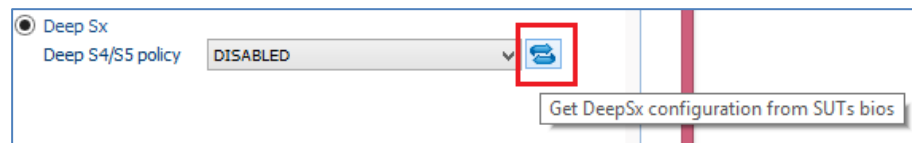
- Check if Deep Sx is enabled, and then click the **Retrieve Deep Sx** button placed next to the Deep Sx policy drop down menu. This feature retrieves the Deep Sx settings from BIOS and then sets the same value for the SUT Deep Sx settings.

**For mobile platforms:**

- Check the following radio button in case your platform doesn't support DeepSX and SUSwelldown . This option is only applicable for Mobile and Mobile-H platforms.

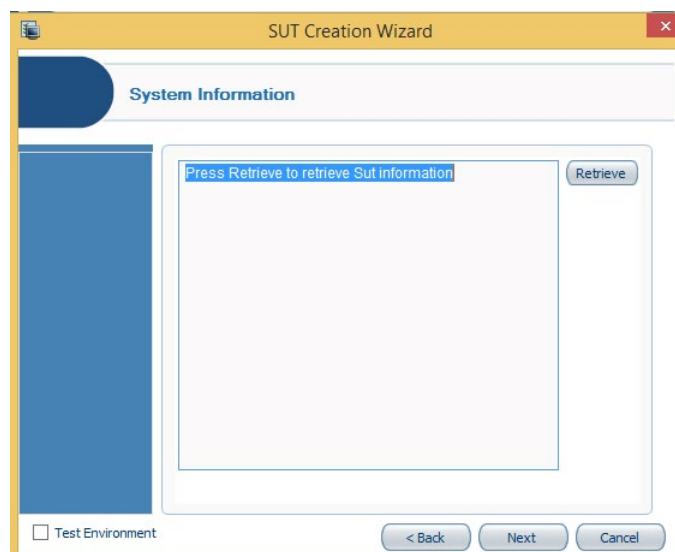


- Check the radio button to define if Deep Sx is enabled, or if SUS Well Down mode behavior is defined instead.
- If Deep Sx is enabled, select the Deep Sx policy from the combo

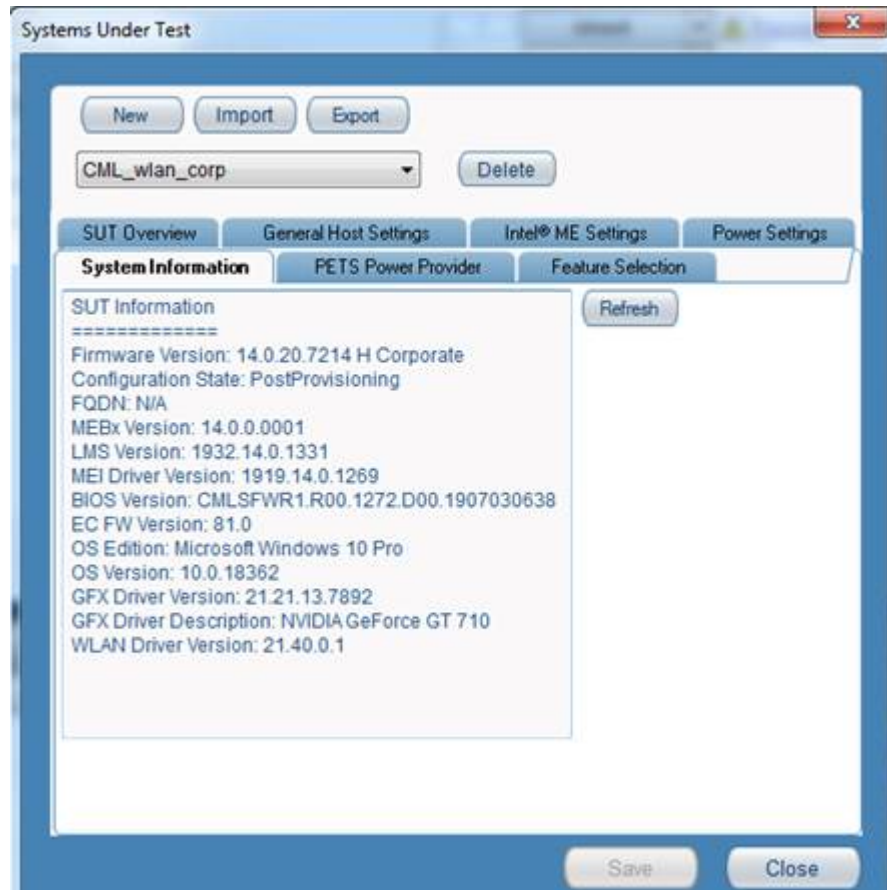


- Deep Sx Retrieve button placed next to the Deep Sx policy drop down menu. This feature retrieves the Deep Sx settings from BIOS and then sets the same value for the SUT Deep S4/S5 policy settings.
- If Suspend mode behavior is defined, select the SUS Well Down mode behavior of your system. You can select one of the following:
  - SUS Well Down Disabled (system stays in S4/S5)
  - SUS Well Down Enabled (system moves to G3). If you select this option, you must also select the radio buttons to define if this behavior is in DC only, or in ACDC as well. Also, select the radio buttons to define if this behavior is in S5 only, or both S4 and S5.

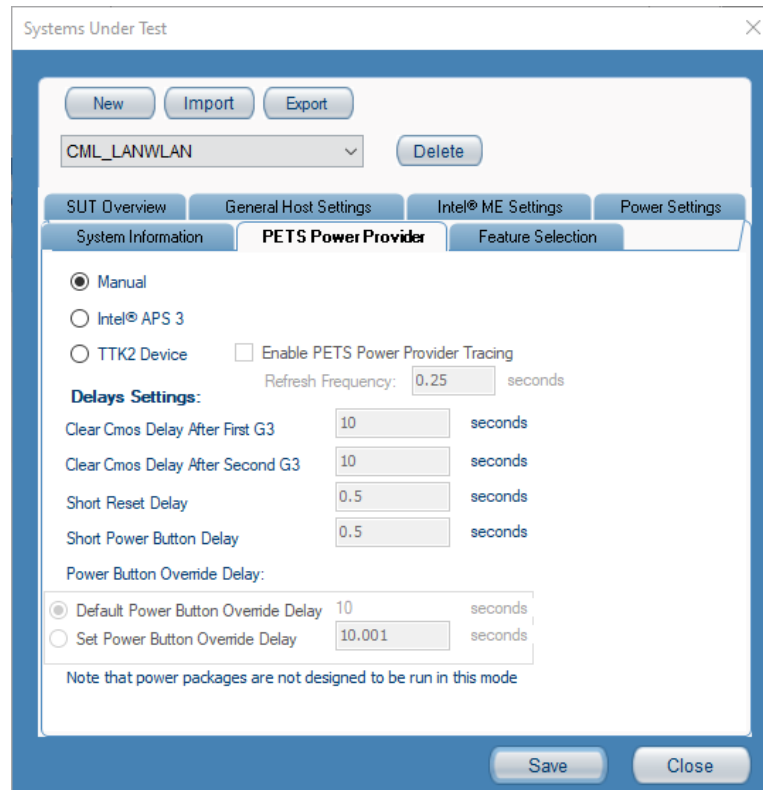
14. Click **Next**; The System Information prompt appears.



- Click “Retrieve” to obtain the system information . This step is not mandatory since PETS will automatically retrieve the information upon closing the SUT wizard and the button will change to “Refresh”.



15. Click **Next**; PETS Power Provider dialog appears.

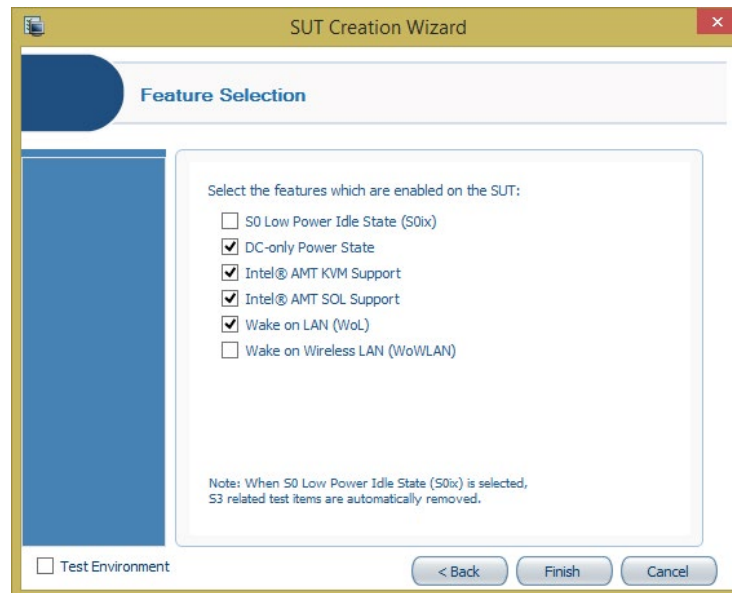


In the PETS Power Provider dialog, select the chosen Power Provider (Manual, Intel APS 3 or TTK2 Device), then select the appropriate options to configure the Delays Settings. The file can be understood as follows:

- Enable PETS Automated Power Provider Tracing:
  - When selected, PETS starts logging every change to Automated Power Switch (e.g. system power state / ME state / Power source). The changes are included in the test's execution report.
- Refresh Frequency:
  - This field is enabled when the user checks the "Enable PETS Power Provider Tracing" option and specifies the time period in which PETS will check for Automated Power Switch changes.
- Delays Settings:
  - Short Reset Delay: defines how long the delay takes to perform a Reset (in seconds).
  - Short Power Button Delay: defines how long the delay takes to perform a power button (in seconds).
  - Power Button Override Delay:
    - Default Power Button Override Delay: if it is selected, the power button override delay (in secs) will be the default value (APS default value) which is 10.
    - Set Power Button Override Delay: if it is selected, the power button override delay will be the value that the user enters (in seconds).



16. Click **Next**; The feature selection dialog appears. In this dialog, select the feature(s) that are enabled on the SUT.



Features that are not selected cause the relevant tests/sub-tests/test steps to be hidden. Please note that the feature selection options that appear in this dialog depend on the platform version, SKU and type defined previously. For example, features that depend on the platform version are listed below:

- **S0ix** is supported in Intel® ME 12 and onwards.
- **WoL and WoWlan** are supported in Intel® ME 12 and onwards
- **DC, KVM, and SOL** are supported in Intel® ME 12

**Note:** When S0 Low Power Idle State (S0ix) feature is selected, S3 related test items are automatically removed.

**Note:** Pointing the mouse icon at any feature displays a tooltip with a brief description of the selected feature.

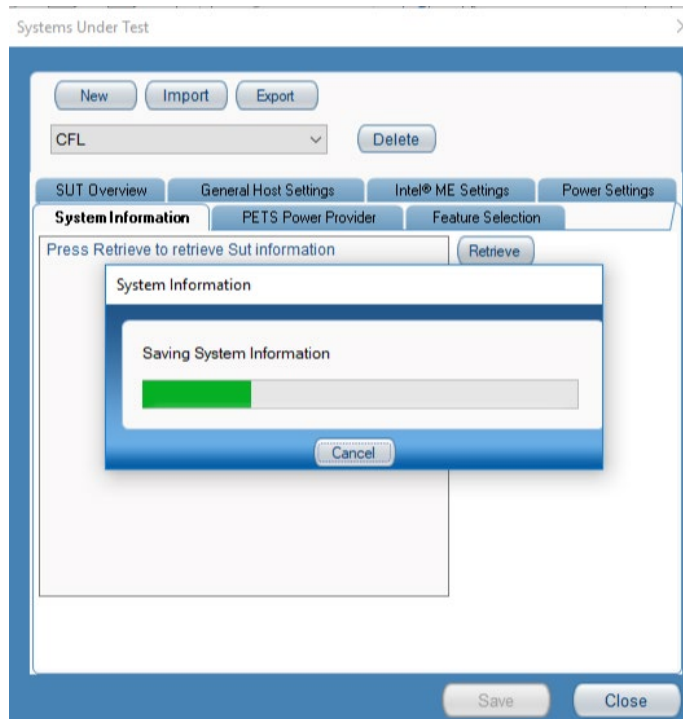
17. Click **Finish**; the following information screen appears providing current ME information for the newly defined SUT.



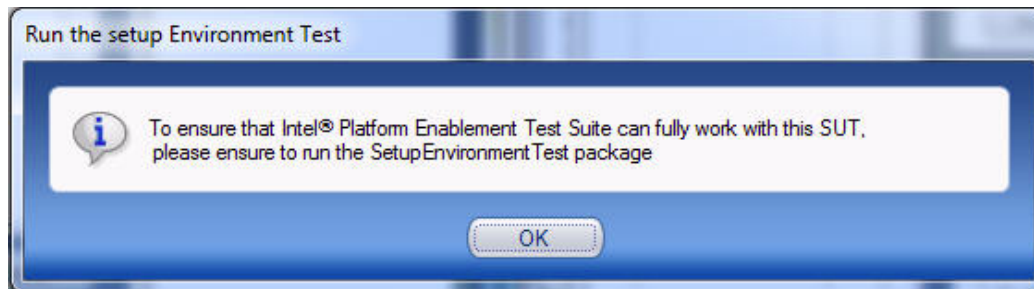
18. Click the **Save** button to save the information entered to the SUT dialog into the Platform information file.

**Note:** The information entered into the dialog only becomes active after being saved.

19. Click the Close button; the "Saving System Information" dialog appears indicating the PETS is trying to save the SUT information that it just retrieved from the SUT.



the SUT dialog closes and the system prompts you to run the Intel® Platform Enablement Test Suite.



20. Click **OK**; *Run the setup* prompt disappears and the Wizard closes.

21. Run the setup test package to check if the SUT was configured correctly. (For more details, see ["Testing the Configuration of the SUT"](#).)

### 5.4.2 Selecting a Previously-Defined SUT

The SUTs that are listed in the drop-down list of SUTs at the top of the Systems Under Test dialog have already been defined.

#### To select an already-defined SUT:

Select one of the SUTs listed in the drop-down list of SUTs; the values of the parameters of the selected SUT appear in the Systems Under Test dialog's fields, where they can be viewed and changed.

**Note:** If you do not click the **Save** button before selecting another SUT, any changes you made to the definition of the current SUT will be lost.



### 5.4.3 Modifying the Definitions of a SUT

You can modify the contents of any field in any tab of the Systems Under Test dialog.

**Note:** You cannot change any of the settings in the Description tab. If you need to change them, delete the SUT and define a new SUT with the desired settings.

#### To modify an already-defined SUT:

1. Select the SUT you want to modify from the drop-down SUT list; the dialog's fields and tabs are populated with the selected SUT's data.
2. Make the relevant changes to the contents of the dialog's fields.
3. Click **Save**.

### 5.4.4 Saving the SUT Defined Parameters

After filling in all the relevant fields and tabs in the Systems Under Test dialog:

1. Click **Save** to apply all your changes.
2. Click **Close** to close the dialog; the names of all of the defined SUTs appear in the Platform field's drop-down list in the Intel® Platform Enablement Test Suite window.

### 5.4.5 Importing a SUT Definition File

1. Click on the **Import** button; the Import SUT file dialog appears.
2. Select a SUT file and click **Open**; the imported SUT is listed in the SUT drop-down list and its definitions appear in the Systems Under Test dialog's fields.

### 5.4.6 Deleting a SUT from Intel® Platform Enablement Test Suite

#### To delete a SUT from the Intel® Platform Enablement Test Suite:

1. Select one of the SUTs listed in the drop-down SUT list.
2. Click the **Delete** button; then "Are you sure you want to delete this SUT" confirmation prompt appears.
3. Click **Yes**; the definitions of the deleted SUT disappear from the Systems Under Test dialog's fields.

### 5.4.7 Exporting SUT Definitions to a Definition File

#### To export SUT definitions:

1. Select a SUT listed in the drop-down SUT list.
2. Click the **Export** button; the Save As dialog appears.
3. Enter the name of the definitions file into the **File Name** field and click **Save**.
4. Intel® Platform Enablement Test Suite populates the definitions that you have entered into the *Add New SUT* dialog.

## 5.5 Testing the SUT Configuration

The Setup Environment test package checks if the SUT was configured correctly. If the SUT passes all the tests in this package, it is configured correctly and other test packages can be run on it. If the SUT fails a test, check which test or test step fails in order to know how to reconfigure the SUT correctly.



The setup Environment test package file name is "SetupEnvironmentTest.xml". Loading and running this test package in Intel® Platform Enablement Test Suite Setup has the same procedure of loading and running any other test package. (For more information, see ["Configuring the Tests in the Test Pane"](#) and ["Configuring a Test Package"](#).)

**Note:** Running the setup environment test package is optional but highly recommended to be run before running other PETS general test packages.

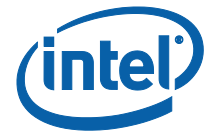
### 5.5.1 Testing the Configuration of the SUT

The following table shows the tests included in the Setup Environment Test package:

Test Name	Description	Relevant to Platform (Corporate/Consumer)
<b>Check Intel® AMT Connectivity</b>	This test verifies the connectivity with the Intel® AMT through either LAN-only, WLAN-only, or both LAN and WLAN network interfaces.	Corporate
<b>Check Intel® AMT Feature Support</b>	This test verifies Intel® AMT feature configuration.	Corporate
<b>Local agent and SUT connectivity setup</b>	The test checks connectivity between the console and the local agent (SUT) through: <ol style="list-style-type: none"> <li>1. Verifying PETS agent version.</li> <li>2. Retrieving ME information from PETS agent.</li> </ol>	Both
<b>Check S3</b>	This test checks the SUT power flow from S0/CM0 to S3/CM3 to S0/CM0 via Host OS suspend cycle.	Both
<b>Check S4</b>	This test checks the SUT power flow from S0/CM0 to S4/CM3 to S0/CM0 via Host OS hibernate cycle.	Both
<b>Check S5</b>	This test checks the SUT power flow from S0/CM0 to S5/CM3 to S0/CM0 via Host OS shutdown cycle.	Both
<b>Check Deep S4</b>	This test checks the SUT power flow from S0/CM0 to S4/CM-Off to S0/CM0 via Host OS hibernate cycle.	Both
<b>Check Deep S5</b>	This test checks the SUT power flow from S0/CM0 to S5/CM-Off to S0/CM0 via Host OS shutdown cycle.	Both
<b>Compare Deep Sx Configuration</b>	This test compares Deep Sx configuration in PETS with BIOS to avoid a mismatched configuration.	Both
<b>Check DC Power</b>	This test checks the SUT power flow from AC and DC to DC-only.	Both *(Mobile platforms only)
<b>Check AC Power</b>	This test checks the SUT power flow from AC and DC to AC-only.	Both



Test Name	Description	Relevant to Platform (Corporate/Consumer)
<b>Check G3 State</b>	This test checks the SUT power flow from S0/CM0 to G3/CM-Off via Power loss.	Both
<b>OS Configuration checks</b>	Verifies both OSs on the console and the local agent are supported by Intel® PETS.	Both
<b>InstantGo configuration match</b>	Verifies if S0 low-power idle setting in PETS SUT configuration matches the SUT configurations.	Both *(InstantGo supported platforms only)



## 6 Running Tests with Intel® Platform Enablement Test Suite

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This chapter describes how to use the Intel® Platform Enablement Test Suite to run tests on an SUT. The following topics are included:

- [Configuring a Test Package](#)
- [Viewing PETS Automated Power Provider Controls](#)
- [Running Tests](#)
- [Viewing, Saving, and Sending Test Reports](#)
- [Using the Compliancy Guide](#)

### 6.1 Configuring a Test Package

The Intel® Platform Enablement Test Suite is supplied with one or more test packages. Each test package contains the XML files that define its tests. All test packages must be loaded into Intel® Platform Enablement Test Suite. Each test package - including its tests and test steps - are already configured with default values that can be changed in the Intel® Platform Enablement Test Suite.

**Note:** Different platforms require different test packages.

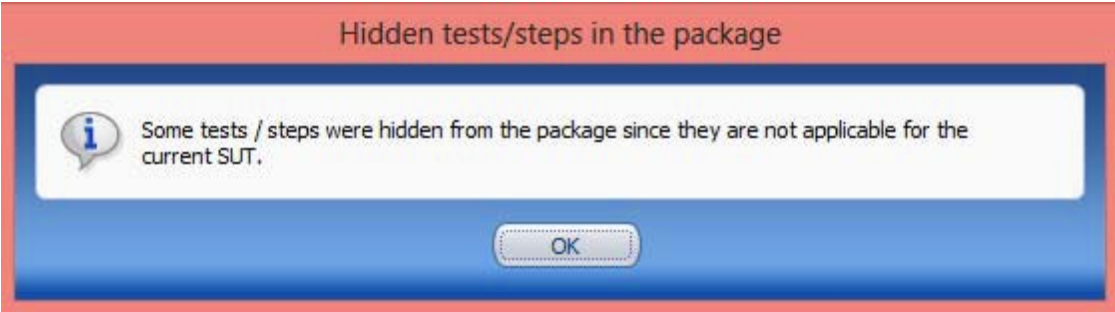
#### 6.1.1 Loading a Test Package

Intel® PETS is able to open the correct packages' sub-folder based on the SUT settings.

1. Choose **Package > Open**; the *Open Intel® PETS Package* dialog appears, displaying the contents of the folder **<Intel® Platform Enablement Test Suite folder>\Packages>** depending on the SUT configurations.
2. Select the test package that is appropriate for the SUT. The name of the selected test package and the number of tests it contains appear in the Intel® Platform Enablement Test Suite window above a list of tests in the package.

**Note:** The package's test names map directly to tests that are listed in your Intel® AMT platform's Compliancy Requirements and Testing Guide, which contains a detailed description of each test (with the exception of the Validation package tests, which do not appear in the Guide). Each Test Name maps to the test ID of a test in the Compliancy Requirements and Testing Guide.

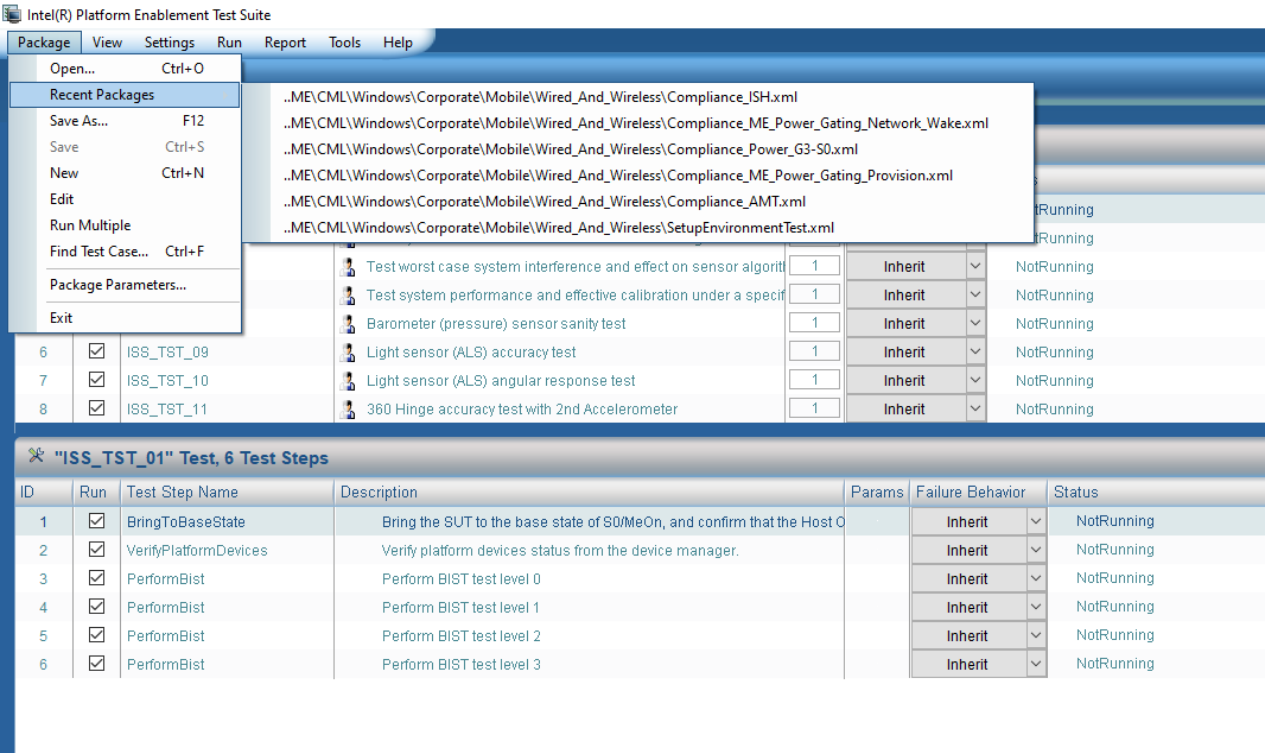
**Note:** If **Supports S0 Low Power Idle** option in the SUT Configuration under Power Settings is set, and the loaded package contains tests, sub tests or test steps related to S3, a message appears to inform the user that these tests will be hidden.



6.1.2 Opening a Recent Package

The Intel® Platform Enablement Test Suite allows you to select a package from a list of the 10 most recently opened packages.

- 1. Choose **Package > Recent Packages**; a list of the 10 most recently opened packages appears.



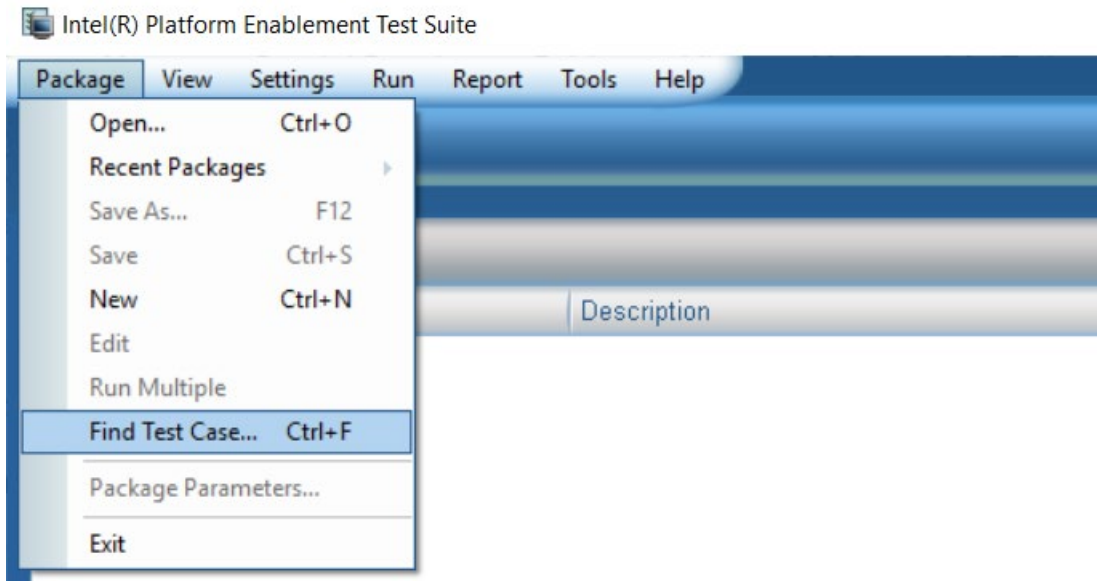
- 2. Select the package you wish to open from the list.

6.1.3 Finding a Test Case

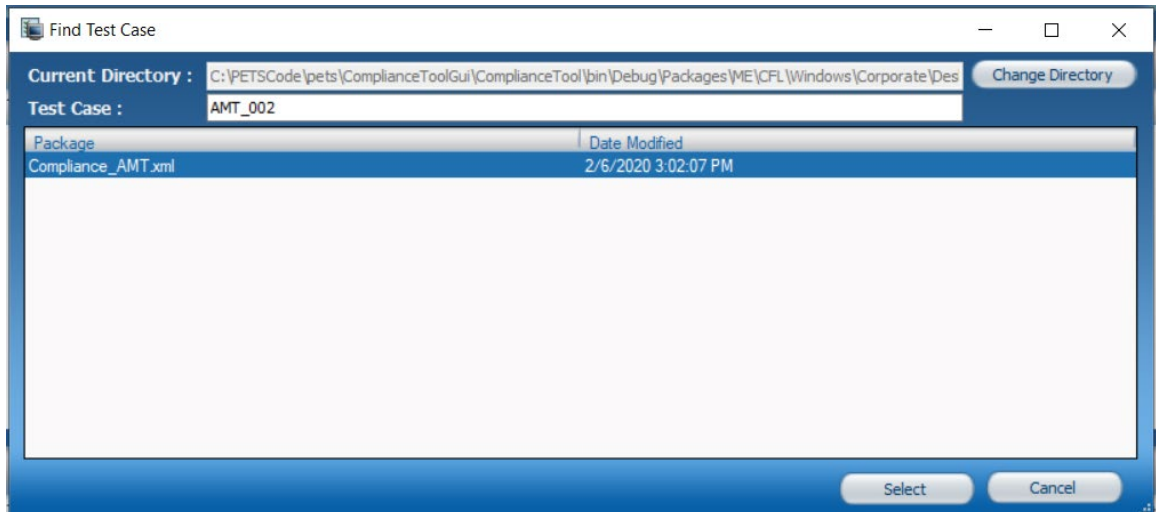
The Intel® Platform Enablement Test Suite enables the user to search for a Test Case, in case the user doesn't know in which package the test case exists.



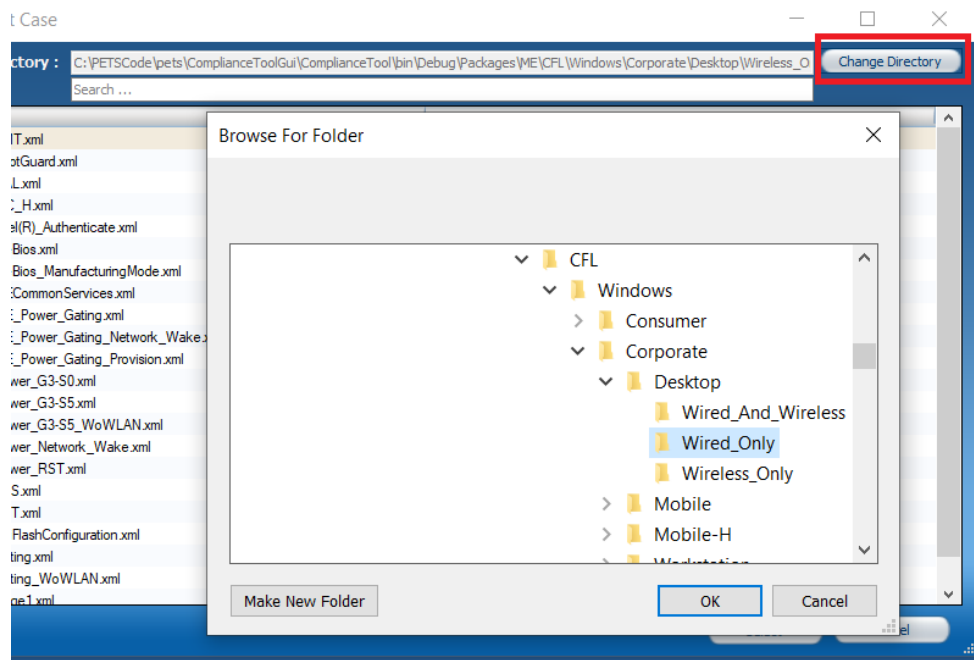
1. Choose **Package > Find Test Case**; The Find Test Case dialog will be displayed.



2. Type any Test Case name in search text box, the Grid will be filtered and will only show the packages that have that Test Case:



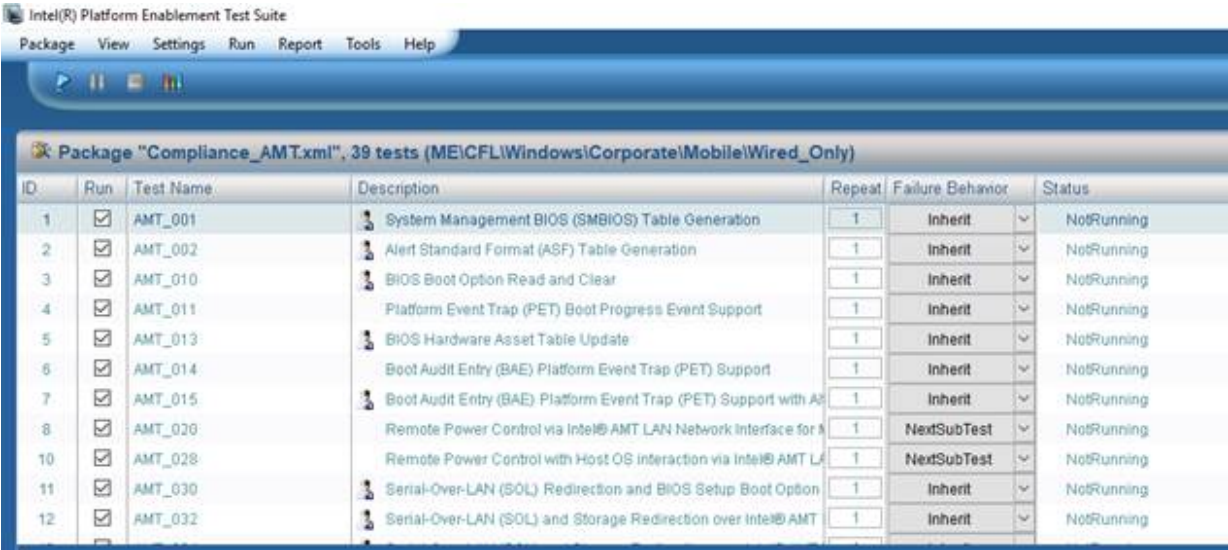
3. The user can change the Default SUT xml packages Directory and browse for another directory that has a valid xml packages, PETS will show the selected folder and sub folders packages.



4. If the selected directory doesn't contain any xml packages, a message appears to the user 'This directory does NOT contain any XML Packages'.
5. Double click on the package name or select the package.
6. Click the select button then the package will be loaded to PETS GUI.



### 6.1.4 Using the Intel® Platform Enablement Test Suite Window

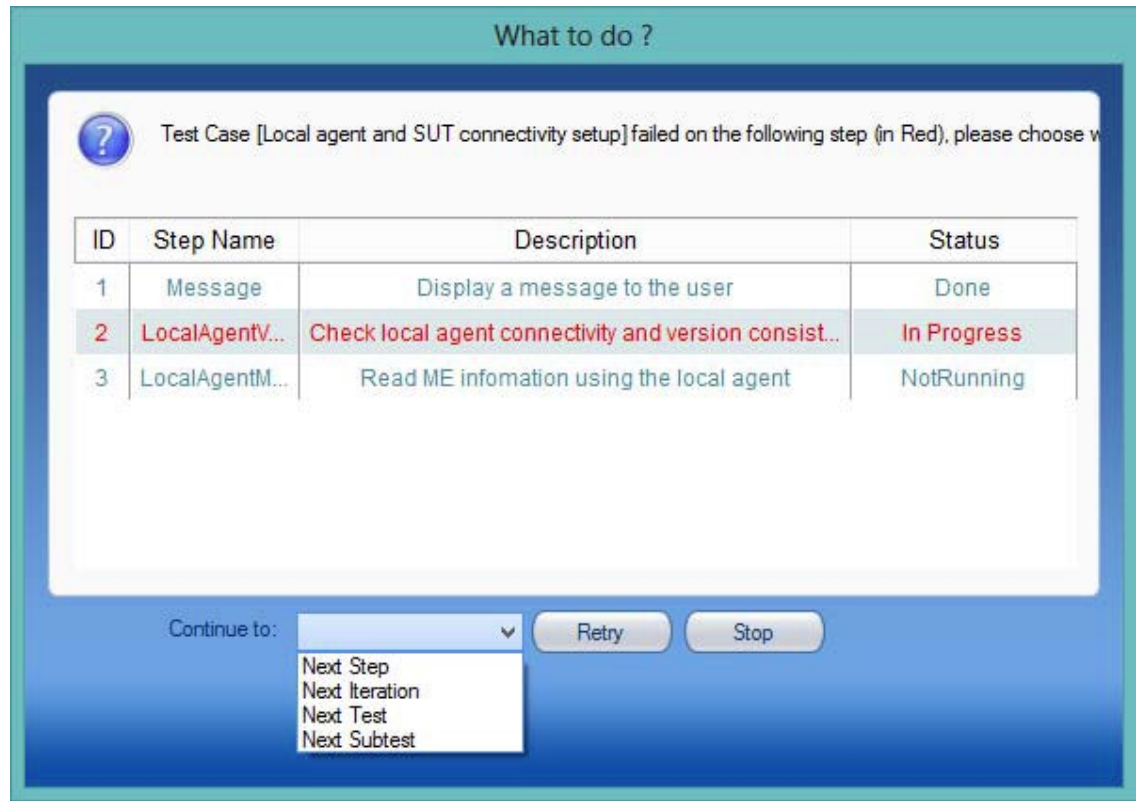
The Intel® Platform Enablement Test Suite displays the tests in the loaded test package by default.



The Intel® Platform Enablement Test Suite displays the following information about each test/test step:



- **Test Name:** Name of the test/test step as it appears in the relevant Compliance Requirements and Testing Guide
- **Description:** Description of the test/test step's functionality. The icon next to the description shows what is being tested and how:
  -  - Setup test – A test that configures the SUT in preparation for an upcoming test.
  -  - A test with manual steps.
- **Repeat:** Number of cycles of test repetition before the next test in the package is to be run (tests only).
- **Failure Behavior:** How Intel® Platform Enablement Test Suite responds to the failure of a test package, test, or test step:
  - Next step - If a test step fails, Intel® Platform Enablement Test Suite runs the following step in the test.
  - Next iteration - If a test fails, Intel® Platform Enablement Test Suite runs the next iteration of the same test. If the last iteration of the test that fails, Intel® Platform Enablement Test Suite runs the next test.
  - Next test - If a test or test step fails, Intel® Platform Enablement Test Suite runs the next test instead of continuing to run any more of the failed test's test steps or iterations (This is the default failure behavior option for test packages).
  - Stop - If a test or test step fails, Intel® Platform Enablement Test Suite stops running that test package.
  - Inherit - Inherits the failure behavior of the level above (i.e., a test inherits the failure behavior of its test package and a test step inherits the failure behavior of its test).
  - Next Subtest - If one of the test steps failed and there exists other subtests in the test, the next subtest is executed.
  - Manual - Upon failure, choosing this failure behavior will allow the user to control the failure/recovery process by showing the following dialog:

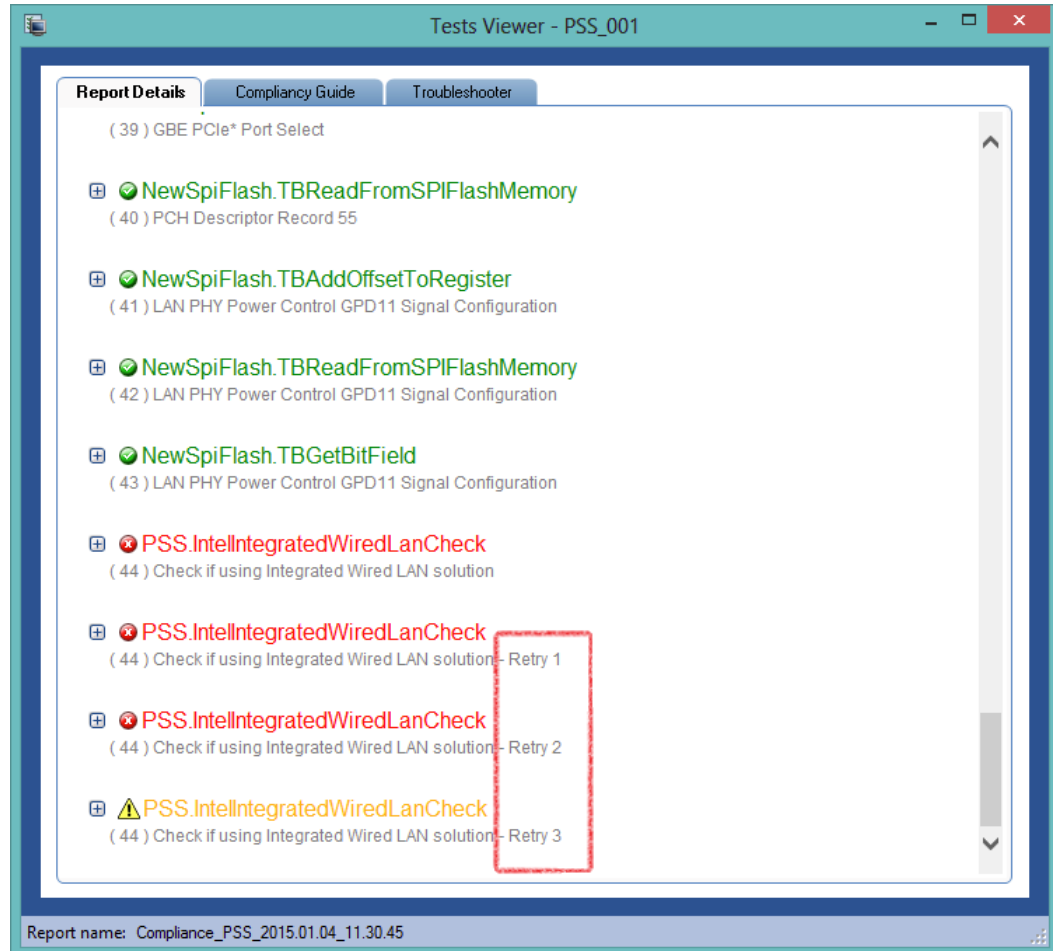


The example screen shows the failing test step highlighted in red, and the test steps above and below it as well. The user can choose one of the following options:

- **Retry:** This lets PETS re-run the failing step before proceeding to the next one. If the test step passes after retrieval, the status will be then passed with warning. Refer to the "Status" section below for more










details. The success details are added to the PETS Log. The PETS log details all step events, displaying the status of each retry as shown below.



- **Continue to** and select one of the following options:
  - Next Step - The current step is marked as passed with a warning, and the next test step is executed.
  - Next Iteration - The current step is marked as passed with a warning, and the next test iteration is executed.
  - Next Test - The current step is marked as passed with a warning, and the next test is executed.
  - Next Subtest - The current step is marked as passed with a warning, and the next subtest is executed.

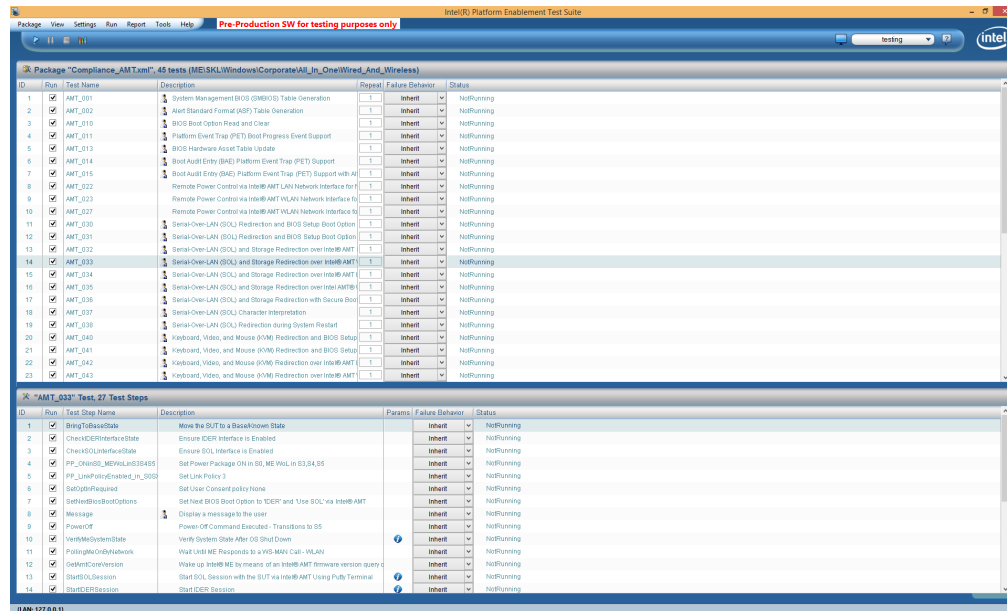


- **Stop:** This will stop the execution of the current test and will run the next test or stop execution if it is the last test in the package.
- **Status:**
  - Not Running - The test has not yet been run.
  -  Iteration indicator - The test is currently running that iteration(e.g., 1/3).
  -  Passed - The test was run successfully.
  -  Failed - The test was run unsuccessfully.
  -  Aborted - The testing process was aborted by the operator.
  -  Paused - The testing process was paused by the operator
  -  Passed with warnings - This status appears along with the "Manual" failure behavior when the test step is failed, retried or continued by the user.
  -  Failed with warnings - This status appears for optional test steps, for example, CheckFWSTS.

#### 6.1.4.1 Viewing a Test's Steps

##### To view a test's steps:

1. Choose **View > Steps Pane**; the Intel® Platform Enablement Test Suite window is divided into two panes:
  - Upper pane - the Test pane - lists all the tests in the test package. Each test has **Run**, **Failure Behavior** and **Repeat** options that configure how that test is run.
  - Lower pane - the Test Step pane - lists all the steps in the test selected in the Test pane (the first test is selected by default). Each test step has **Run** and **Failure Behavior** options that configure how it is run.
2. Click on one of the tests listed in the Test pane; the test steps of the selected test are displayed in the lower pane.



### 6.1.4.2 Configuring the Tests in the Test Pane

#### To configure the tests in the Test pane:

1. Verify that the checkbox in the **Run** column is selected for each test you want to run and deselected for each test you do not want to run (The **Run** checkboxes are selected for all the tests by default). To select or unselect all the tests, right-click the heading (**Run**) and choose **Select All** or **Unselect All**. Alternatively, you can select/unselect multiple tests/test steps together.
2. To change the number of cycles that a test should be performed for, double-click the value in the test's **Repeat** column and enter the number of cycles.
3. Click the help icon in the **Params** column to view and edit package parameters related to the test under which the selected test step is nested.
4. Select one of the following options in the **Failure Behavior** drop-down list to define what will happen if one of the test's test steps fails: Next step, Next test, Stop, and Inherit.

**Note:** The "Failure Behavior" option provides simultaneous changeability of multiple selected tests.



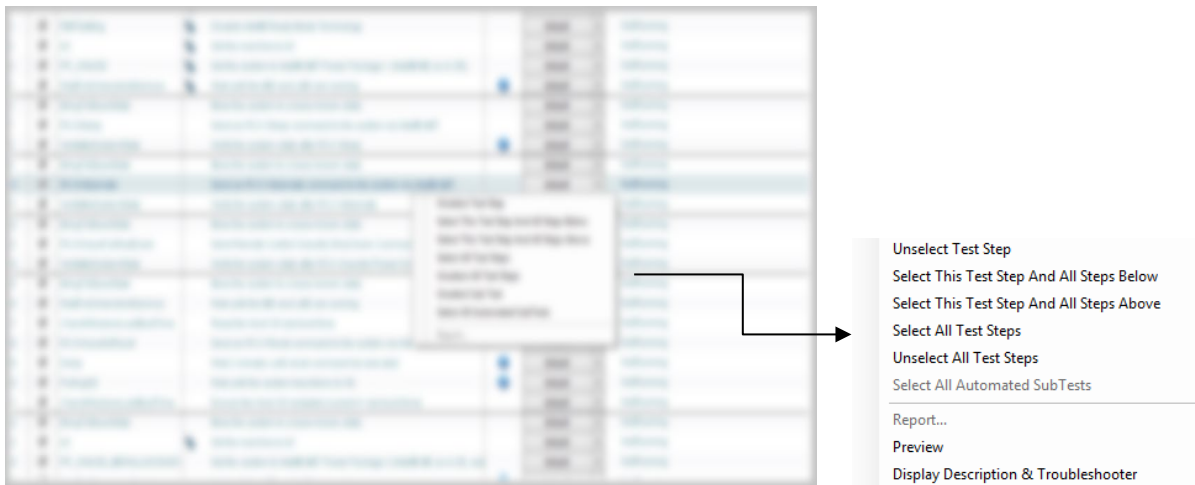
### 6.1.4.3 Configuring the Test Steps in the Test Step Pane

#### To configure the test steps in the Test Step pane:

1. Verify that the checkbox in the **Run** column is selected for each test step you want to run and deselected for each test step you do not want to run. To select or unselect all the tests, right-click the heading (**Run**) and choose **Select All** or **Unselect All**, respectively. (By default, the **Run** checkboxes are selected for all of the test steps.)
2. Select one of the following options in the **Failure Behavior** drop-down list to define what happens if a test step fails: Next step, Next test, Stop, and Inherit.

#### Using Group Select/Unselect in Power Packages

For Power Packages only, Intel® PETS groups steps into sub-tests, and allows an entire sub-test to be selected or unselected. Simply right-click on a test and you will get the following screen (example shown):



You are also able to group select/unselect the relevant test steps with a single command.

#### Using the Right-click menu:

Open a test in Intel® PETS.

The steps in the Test Steps (lower) pane will be delineated by a horizontal line. The steps below the horizontal line are the Sub Test steps. Right-clicking would open a menu with the following options:

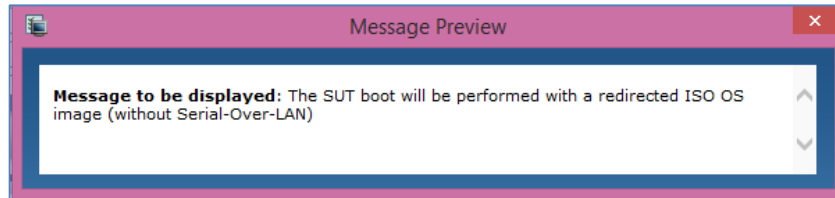
1. **Select Sub Tests:** To select all Sub test steps; alternatively, choose **Unselect Sub Test** to deselect all Sub Test steps.
2. **Select This Test Step And All Steps Below/Above:** These options would select the current step and mark either all above or below remaining test steps.
3. **Unselect/Select All Test Steps:** This will select or unselect all test steps in the current test.
4. **Select All Automated Sub Tests:** This will select only automated sub tests of a test.

Alternatively, users can use hotkeys such as **shift** or **ctrl** to select multiple tests. Users can mark the selected test to be performed by right-clicking and choosing the **Select Test Steps/s** option from the menu.

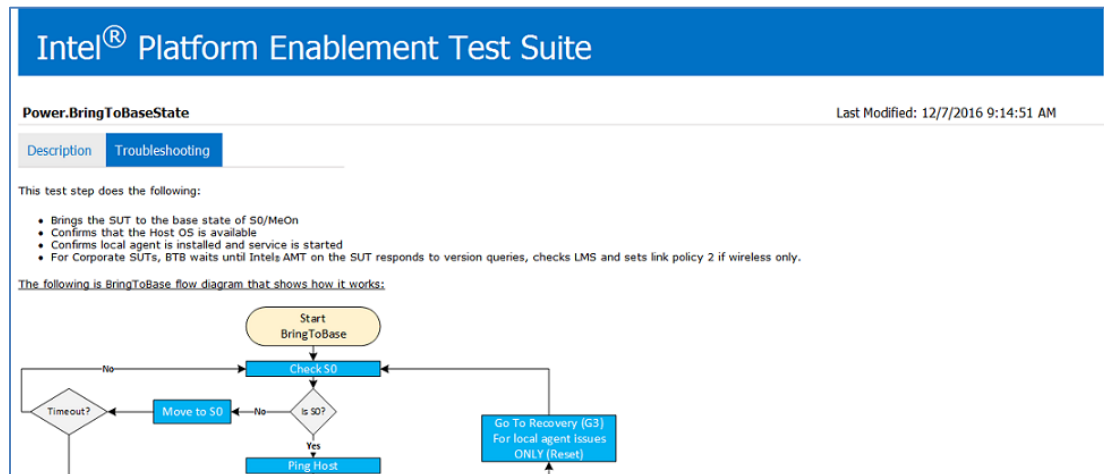


5. **Preview:** A preview dialog will display how the test step will look like when it is run. This usually appears for test steps that display a message or question to the user as part of the execution.

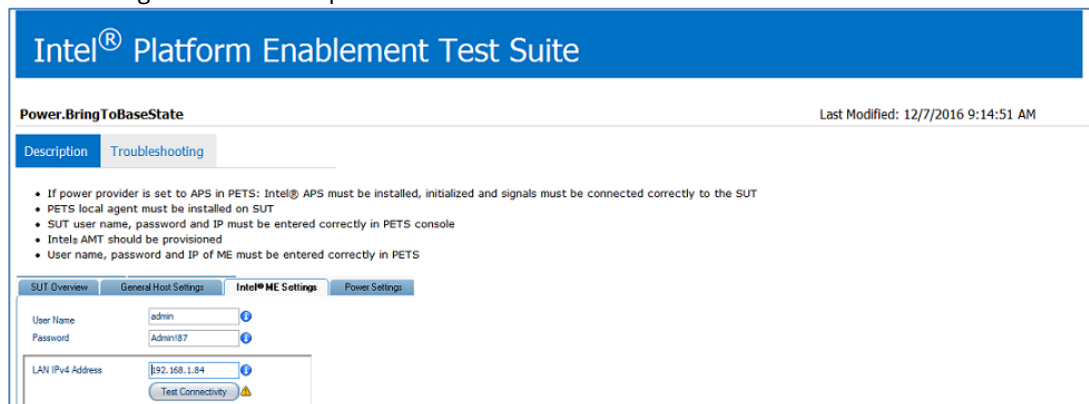
**Note:** For now, this feature is only available for the Message and Question test steps. The below screenshot shows an example of how the preview dialog would look like:



6. **Display Description & Troubleshooter:** This will activate a dialog that includes the following information:
- The name of test step and last date of modification of the test step's description and troubleshooter.
  - Description tab: This description is usually populated by Intel® PETS developers and reflects the techniques and logic behind a specific test step and how it was programmed. As an example, when activating this feature for the "BringToBaseState" step, the user will get the following flow chart diagram explaining the process of bringing the SUT to the S0 state before running the test.



- Troubleshooting tab: This tab contains debug information for the test operation in case of failures. Such as the configurations which must be set in order to run the test step without failures. Here is an example for the "BringtoBaseState" step Troubleshooter:





#### 6.1.4.4 Configuring how a Test Package is run

##### To configure how the Test Package is run:

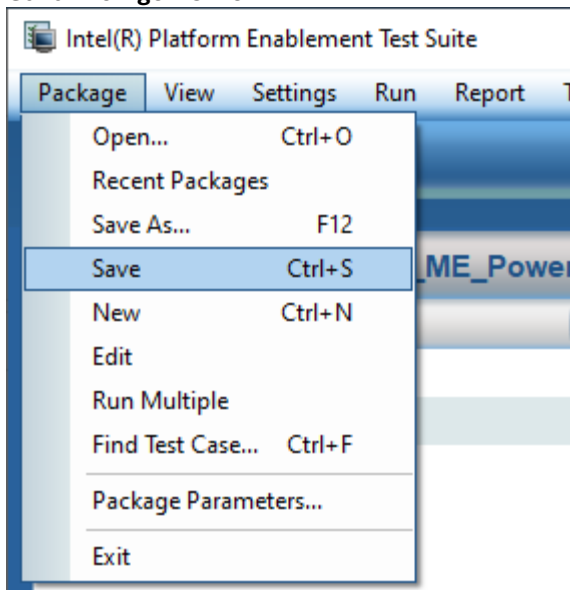
1. Go to **Run>Failure Behavior** and select which of the following options will occur if one of the tests in that test package fails: Next iteration, Next test, and Stop.
2. Configure the Package Parameters of the test package. (For information on how to configure package parameters, see [Changing Package Parameters](#).)

#### 6.1.4.5 Saving the Changed Configuration of a Test Package

You can save the changed configuration of a test package as an original version or as a new version of that package. Either one of the versions (original/modified) of the test package can be loaded into the Intel® Platform Enablement Test Suite at a later date.

##### To save the changed configuration of a test package over the original version:

1. Go to **Package > Save**

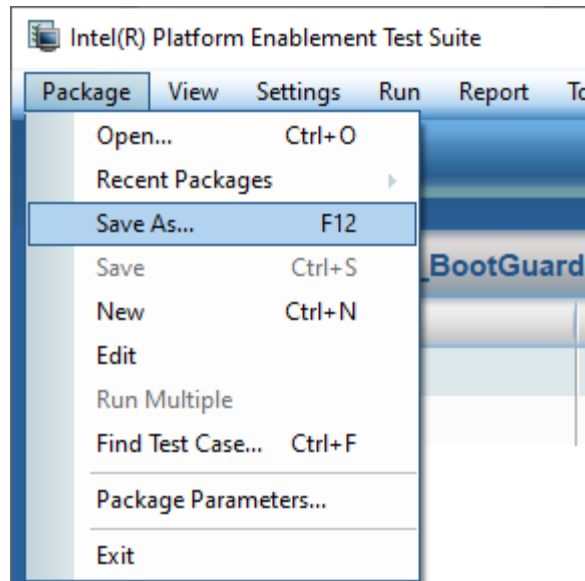


Typically, you can use this function when editing an existing package and you wish to save changes as you work (prevent loss of information if a crash occurs). Save replaces the original file.

##### To save the changed configuration of a test package as a new version:

1. Choose **Package > Save As**.

You can save the changed configuration of a test package as a new version basing the new package on one you have already opened/edited. This is useful if you want to save an existing package with a new name and then make changes rather than start from scratch. **Save As** leaves the original package unchanged.

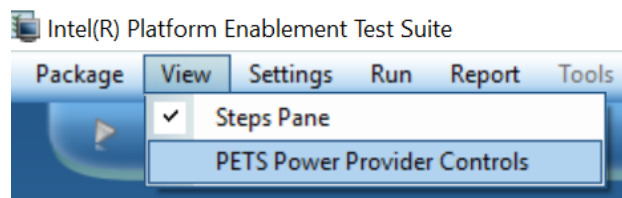


2. Enter the name of the new package in the **Name** field of the Save As dialog.
3. Specify the desired location and click **Save**. The new version of the test package is saved in the desired location.

## 6.2 Viewing Intel® PETS Automated Power Provider Controls

To view PETS Power Provider controls:

- Choose **View > PETS Power Provider Controls**.



**Note:** Intel® Platform Enablement Test Suite must be connected to the SUT via an Automated Power Provider Hardware device that is attached to the SUT. The SUT must be defined to be configured with an Intel® PETS Automated Power Provider (Ex: Intel® APS).

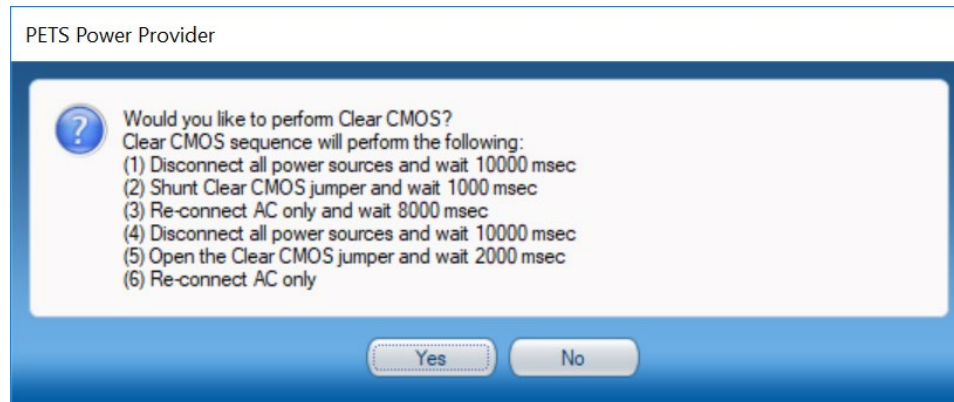
The APS controls are divided into the following two groups: Power Operations and Power Sources:



- **Power Operations** - Control the power transitions of the SUT:



- Power On/Off - Turns the power to the SUT on and off by causing the Power button to be pressed on the Intel® PETS Automated Power Provider. Power On appears when the SUT is off. Clicking this button turns the power on. Power Off appears when the SUT is on. Clicking this button turns the power off.
- Reset - Restarts the SUT by causing the Reset button on the Intel® PETS Automated Power Provider to be pressed.
- Clear CMOS - Performs Clear CMOS. A message appears, describing the Clear CMOS operation and prompting the user for confirmation.



- Unconditional Power Down - Turns off the SUT by causing the Power button on the Intel® APS to be pressed for 7 seconds.
- **Power Sources** - Connect or disconnect the SUT from its power source:
  - AC - Connects only the AC power supply to the SUT.
  - ACDC - Connects both the AC and DC power supplies to the SUT.
  - DC - Connects only the DC power supply to the SUT.
  - G3 - Disconnects all power sources from the SUT.

**Note:** When transitioning from AC to ACDC to DC or from DC to ACDC to AC, make sure the SUT remains in ACDC power state for 7 seconds at least. Otherwise, the SUT may shut down (enters G3 power state).

The DC and ACDC options are not available in Desktop platforms and Workstations.



## 6.3 Running Tests

### To run a test package:

Click the **Start** button or choose **Run > Run Package** to run the test package's selected test(s) and test step(s).

**Note:** Once the Run button is clicked, you will see a progress window showing that the test operator is synchronizing the time of the SUT clock with that of the MC clock. This is done in order to make sure the test's logs will be arranged in the correct sequence especially when the test's step event is executed on the SUT.

### To manually stop running a test:

Click the **Stop** button or choose **Run > Stop**.

### To pause a test while it is being run:

Click the **Pause** button or choose **Run > Pause**.

### To resume running a paused test:

Click the **Start** button.

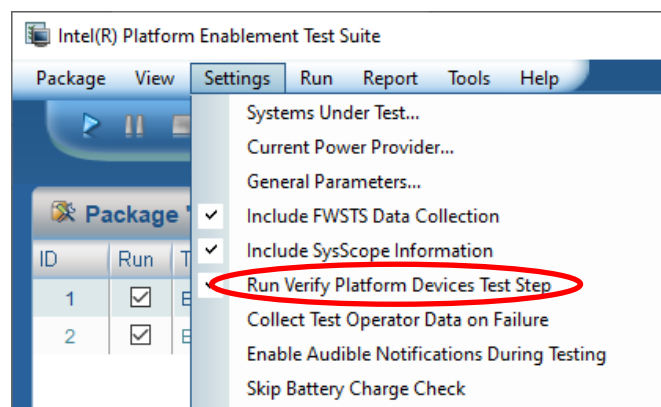
**Note:** If a test step is stopped or paused, Intel® Platform Enablement Test Suite does not stop the test immediately but allows it to continue until the test step being run has finished. The messages and dialogs that appear as part of that test step will continue to appear even though the test step has been stopped/paused.

### 6.3.1 Verify Platform Devices Test Step

Intel® PETS has a new test step named "VerifyPlatformDevices" added at the beginning of each test in most of the testing packages to verify platform devices in the device manager. In case the device manager shows unknown devices with a yellow bang, or uninstalled devices, or devices in faulty state, the step will fail and be marked as a warning. Its logs will display a full description of the errors.

ID	Run	Test Step Name	Description
1	<input checked="" type="checkbox"/>	BringToBaseState	Move the SUT to a Base/Known State
2	<input checked="" type="checkbox"/>	VerifyPlatformDevices	Verify platform devices status from the device manager

The user has the choice to either run this test step or skip it by checking/unchecking the following option in the Settings menu-bar item. However, it is highly recommended to execute it.



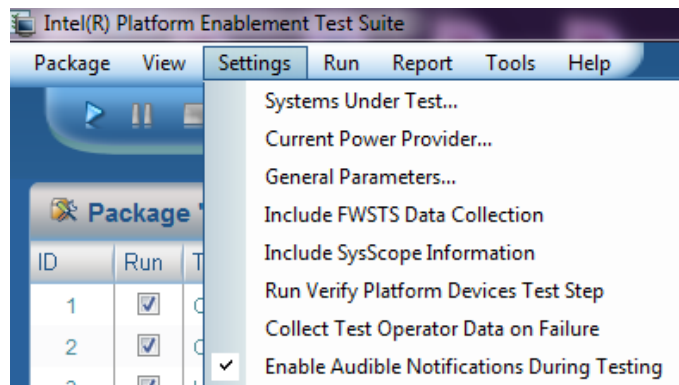


### 6.3.2 Audible notification during testing

PETS introduces a new feature that provides an alternate method to notify the test operator of the testing status in two situations:

- When multiple systems are running at the same time (Workshops), or when they're running on long flows and other tasks could be started (multitasking).
- When testing is complete but the system screen is out of view.

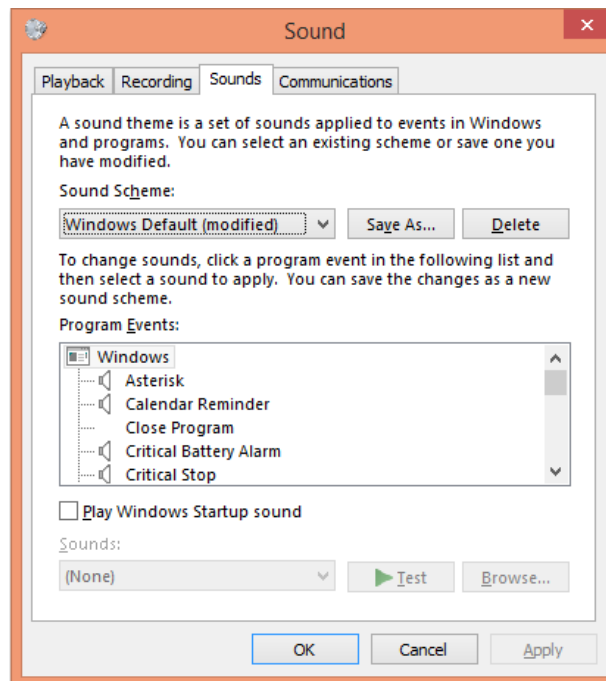
**Note:** Audible Notifications feature is disabled during testing by default. It can be switched on or off from the Main menu -> Settings tab -> Enable Audible Notifications During Testing, see the image below.



Audible Notifications during testing feature has three use-cases:

1. When a test step has one of the following results:
  - **Failed**, if the test failure behavior is set to:
    - i. 'Manual', the default registered windows exclamation sound is played upon displaying the 'What to do?' dialog box.
    - ii. 'Stop', no audio is played. The audible notifications will be covered during test finishing flow in Step 2.
    - iii. Any other failure behavior, the default registered Windows System Notification sound is played.
  - **Warning**, the default registered Windows Exclamation sound is played. This is for test steps which may only result in WARNING, and does not abide by the test failure behavior flows listed above.
  - **Passed**, no audible notification is played as this may cause constant distraction during testing.
2. When a test is finished and the overall test result is:
  - **Failed**, the default registered Windows Critical Stop sound is played.
  - **Warning**, the default registered Windows Exclamation sound is played.
  - **Passed**, the default registered Windows Asterisk sound is played.
3. When the whole testing process is complete and the message box containing "Test Complete" appears, PETS plays the default registered Windows NEW Mail Notification.

Customizing the audible tones is available by changing their values from the Windows OS Sound Configuration dialog. To Open Windows OS Sound Configuration dialog, go to the **personalization screen > sounds**. The following dialog appears and new sounds can be mapped to the previous five registered sound names.

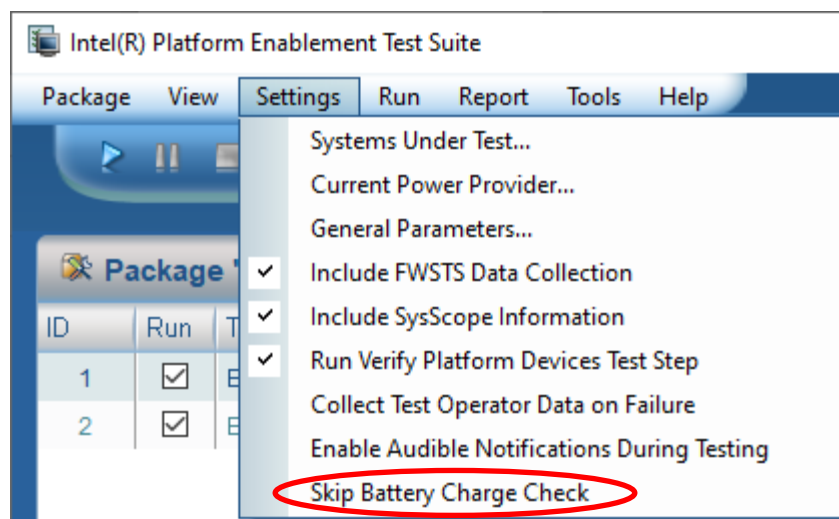


### 6.3.3 Skip Battery Charge

“Skip Battery Charge Check” option under the settings tab in the PETS Main menu-bar is used when PETS couldn't detect the battery due to the following :

- 1- The OS cannot recognize the DC battery when it is connected directly to the APS.
- 2- The OS detects multiple batteries including the virtual ones which makes it hard for PETS to verify the battery status

The “Skip Battery Charge Check” option is unchecked by default, so PETS will run the “GetBatteryChargeRemaining” Test step as part of its main flow. However, the user can check this option and accordingly PETS won't run this test step and move to run the next test step.





### 6.3.4 Viewing the Progress of a Test

A summary of the progress of the test currently being run appears in the Steps pane.

#### To display the Steps pane:

- Choose **View > Steps Pane**; the Progress pane appears under the test pane's title bar.

Detailed information about how a test is progressing can be obtained by clicking on the test to display its steps in the lower pane. The status of each test step appears in its **Status** column.

Additionally, the status bar at the bottom of the Intel® PETS main screen displays the following information:

- Network types and IP addresses for the SUT.
- Local agent connection status (connected or disconnected) and the local Agent version installed on the SUT when the connection is active. This saves the time taken to open the SUT configuration window to test local agent connectivity.
- System state: S0/S3/S4/S5
- ME state: ME on, ME off
- Power source: AC/ACDC/DC/G3
- Time elapsed (the elapsed time for the current step is displayed in the status bar during execution in the following format: DD HH:MM:SS (DD - Days, HH - Hours, MM - Minutes, SS - Seconds). The progress timer starts counting from zero for each new test step until it has finished (or timed-out). The timer freezes if the test is paused, continues when running is resumed, is reset to zero if the step is retried, and stops if the test is aborted.

"PTT_001" Test, 28 Test Steps					
ID	Run	Test Step Name	Description	Params	Failure Behavior
1	<input checked="" type="checkbox"/>	BringToBaseState	Bring the machine to S0		Inherit
2	<input checked="" type="checkbox"/>	WriteToMemory	Request locality 0: Write 1 to the register TPM_LOC_CTRL_0.RequestA		Inherit
3	<input checked="" type="checkbox"/>	ReadFromMemory	Read the value of the register TPM_LOC_STATE		Inherit
4	<input checked="" type="checkbox"/>	GetBitField	Verify TPM_LOC_STATE_1.locAssigned field is set to 1		Inherit
5	<input checked="" type="checkbox"/>	GetBitField	Verify TPM_LOC_STATE_2.activeLocality field is set to 0		Inherit
6	<input checked="" type="checkbox"/>	GetBitField	Verify TPM_LOC_STATE_3.activeLocality field is set to 0		Inherit
7	<input checked="" type="checkbox"/>	GetBitField	Verify TPM_LOC_STATE_4.activeLocality field is set to 0		Inherit
Status					
					NotRunning
					NotRunning
					NotRunning
					NotRunning
					NotRunning
					NotRunning
					NotRunning

(WLAN: 192.168.1.104) Local Agent: Available (Version: 8.1.42) System State: S0 ME State: MeOn Power Source: ACDC Elapsed Test Step Time: 00:00:00.05

Detailed information about all the tests and test steps in the test package, while the package is being run can be obtained by:

- Opening the test package's test results log in the web browser (for information on how to locate the test results log, see [Viewing the results of running a test package](#).)
- Clicking the **Refresh** button in the web browser whenever requiring an update of the information in the report.

The test's status (**Passed** or **Failed**) appears in its **Status** column once all the test steps in a test have been run. If one or more steps end with the status **Failed**, the status of the test is changed to **Failed**. A message pops up when the test package finishes running displaying "tests completed".



The screenshot shows the Intel(R) Platform Enablement Test Suite interface. The top bar indicates 'Pre-Production SW for testing purposes only'. The main window displays a list of tests under the package 'Compliance\_Power\_G3-S5.xml'. The tests are listed in a table with columns for ID, Run, Test Name, Description, Repeat, Failure Behavior, and Status. Below this, a detailed view for 'ME\_PM\_19' Test, 42 Test Steps is shown, listing individual test steps with their descriptions and parameters.

ID	Run	Test Name	Description	Repeat	Failure Behavior	Status
10	<input checked="" type="checkbox"/>	ME_PM_10	S4-S5CM-Off (Suspend Well On) to S5CM3	1	Inherit	NotRunning
11	<input checked="" type="checkbox"/>	ME_PM_11	S5CM3 to S4-S5CM3	1	Inherit	NotRunning
12	<input checked="" type="checkbox"/>	ME_PM_12	S4-S5CM3 to S5CM3	1	Inherit	NotRunning
13	<input checked="" type="checkbox"/>	ME_PM_13	S4-S5CM3 to S4-S5CM-Off (without Intel® ME Wake)	1	Inherit	NotRunning
14	<input checked="" type="checkbox"/>	ME_PM_14	S4-S5CM3 to S4-S5CM-Off (with Intel® ME Wake)	1	Inherit	NotRunning
15	<input checked="" type="checkbox"/>	ME_PM_15	Q3 or S4-S5CM-Off (Suspend Well Off) to S4-S5CM3	1	Inherit	NotRunning
16	<input checked="" type="checkbox"/>	ME_PM_16	S4-S5CM-Off (Suspend Well On) to S4-S5CM3	1	Inherit	NotRunning
17	<input checked="" type="checkbox"/>	ME_PM_19	Straight-to-S5, ME Power Policy is S5-Only	1	Inherit	NotRunning
18	<input checked="" type="checkbox"/>	ME_PM_20	Straight-to-S5, ME Power Policy Calls for Sx Operation	1	Inherit	NotRunning
19	<input checked="" type="checkbox"/>	ME_PM_23	Q3 or S4-S5CM-Off (without Intel® ME Wake) to S4-S5CM-Off (with	1	Inherit	NotRunning
20	<input checked="" type="checkbox"/>	ME_PM_26	Warm Reset	1	Inherit	NotRunning

ID	Run	Test Step Name	Description	Params	Failure Behavior	Status
10	<input checked="" type="checkbox"/>	PolingMeOnByNetwork	Wait until ME responds to a W5-MAN call - WLAN		Inherit	NotRunning
11	<input checked="" type="checkbox"/>	Suspend	The machine transitions to S3		Inherit	NotRunning
12	<input checked="" type="checkbox"/>	VerifyMeSystemState	Verify system state after OS Sleep		Inherit	NotRunning
13	<input checked="" type="checkbox"/>	PolingMeOffBySignal	Inverted - Check that there is no ME signal		Inherit	NotRunning
14	<input checked="" type="checkbox"/>	PowerButtonOverride	PowerButton pressed for S5ec - transitions to S0->S5		Inherit	NotRunning
15	<input checked="" type="checkbox"/>	VerifyMeSystemState	Verify system state after Power Button Override		Inherit	NotRunning
16	<input checked="" type="checkbox"/>	PolingMeOffBySignal	Inverted - Check that there is no ME signal		Inherit	NotRunning
17	<input checked="" type="checkbox"/>	BringToBaseState	ME_PM_19.9 - S4-S5CM-Off to S5CM-Off - Straight to S5 ME is in PP1 (P)		Inherit	NotRunning
18	<input checked="" type="checkbox"/>	PP_OnInS0	Set PP1		Inherit	NotRunning
19	<input checked="" type="checkbox"/>	PP_LinkPolicyEnabled_in_S0S	Set Link Policy 3		Inherit	NotRunning
20	<input checked="" type="checkbox"/>	PingHost	Check that the Host responds - LAN		Inherit	NotRunning
21	<input checked="" type="checkbox"/>	PingHost	Check that the Host responds - WLAN		Inherit	NotRunning
22	<input checked="" type="checkbox"/>	PolingMeOnByNetwork	Wait until ME responds to a W5-MAN call - LAN		Inherit	NotRunning
23	<input checked="" type="checkbox"/>	PolingMeOnByNetwork	Wait until ME responds to a W5-MAN call - WLAN		Inherit	NotRunning
24	<input checked="" type="checkbox"/>	Hibernate	The machine transitions to S4		Inherit	NotRunning
25	<input checked="" type="checkbox"/>	VerifyMeSystemState	Verify system state after OS Hibernate		Inherit	NotRunning
26	<input checked="" type="checkbox"/>	PolingMeOffBySignal	Inverted - Check that there is no ME signal		Inherit	NotRunning
27	<input checked="" type="checkbox"/>	PowerButtonOverride	PowerButton pressed for S5ec - transitions to S0->S5		Inherit	NotRunning
28	<input checked="" type="checkbox"/>	VerifyMeSystemState	Verify system state after Power Button Override		Inherit	NotRunning
29	<input checked="" type="checkbox"/>	PolingMeOffBySignal	Inverted - Check that there is no ME signal		Inherit	NotRunning
30	<input checked="" type="checkbox"/>	BringToBaseState	ME_PM_19.9 - S5CM-Off to S5CM-Off - Straight to S5 ME is in PP1 (P)		Inherit	NotRunning
31	<input checked="" type="checkbox"/>	PP_OnInS0	Set PP1		Inherit	NotRunning
32	<input checked="" type="checkbox"/>	PP_LinkPolicyEnabled_in_S0S	Set Link Policy 3		Inherit	NotRunning
33	<input checked="" type="checkbox"/>	PingHost	Check that the Host responds - LAN		Inherit	NotRunning
34	<input checked="" type="checkbox"/>	PingHost	Check that the Host responds - WLAN		Inherit	NotRunning

**Note:** The current SUT power status can be seen in the status bar.

## 6.4 Viewing, Saving, and Sending Test Reports

Once a test package finishes running, the results report of each test and each test step that was run can be seen. The report can be viewed in the Intel® Platform Enablement Test Suite or in the web browser. The reports have the same appearance and contain the same information regardless of the viewing location.

### 6.4.1 Viewing the results of running a test package

The following information describes how a test package's test results can be viewed:

- The Run Test Results window appears in the Intel® Platform Enablement Test Suite window when performing one of the following actions:
  - Double-clicking a test's **Status** field
  - Clicking on the Test results button in the toolbar ( )
  - Choosing **View > Report**.
- Double-clicking on the appropriate test result log in the web browser. The test results log files are located in **<Intel(R) Platform Enablement Test Suite folder>\Logs**. The location of the Intel® Platform Enablement Test Suite folder is set during the installation process. To make it easy to locate a specific log file, the format of the name of all test results log files is **<package><date><time>.XML** (e.g. Compliance\_ME\_Power\_Gating\_2020.01.19\_10.42.53.xml)



## 6.4.2 Example

Intel® PETS results for running Intel® DAL Compliance test package on a CFL-S Corporate platform are updated in the excel sheet below.

A		B		C		D		E		F		G		H		I		J		K		L		M			
1 Project: CFL S ME12 FW - CNP-H based platform Corporate SKU										Intel CFL S RVP CORPORATE																	
2 Phase: Alpha										WLAN & LAN Package																	
3 Notes: N/A																											
4										Total														Percentage			
5										3.1%														Complete Rate			
6										0.7%														Pass Rate			
7										0.0%														Fail Rate			
8										0.0%														Block Rate			
9										88.9%														To Do			
10 Intel Confidential																											
Test Name		Description			Form factor	PETS / Manual	Network Factor (LAN/WLAN)	Result	Block Reason				Intel Defect ID														
239 Integrated Clock Control (ICC) Compliance																											
239 ICC_TST_01		ICC boot profile selection from ICC configuration			DT	PETS	Both	ToDo																			
240 ICC_TST_02		Test default settings for Standard configuration			DT	PETS	Both	ToDo																			
241 ICC_TST_03		Test default settings for Adaptive configuration			DT	PETS	Both	ToDo																			
242 ICC_TST_06		Test default settings for Overclocking Extended configuration			DT	PETS	Both	ToDo																			
243 ICC_TST_10		Display Chipset Initialization Header data			DT	PETS	Both	ToDo																			
244 Media Playback Compliance																											
245 Media_001		Verify default configuration settings for Protected Audio Video Path (PAVP) in Firmware Image Tool (FIT)			DT	Manual	ToDo	ToDo																			
246 Media_002		Verify LPC00 Port configuration in Firmware Image Tool (FIT) (only for CFL)			DT	Manual	ToDo	ToDo																			
247 Media_003		Verify Internal Port configuration in Firmware Image Tool (FIT)			DT	Manual	ToDo	ToDo																			
248 Media_004		Verify PAVP Enabled in BIOS (Only if the SUT BIOS menu displays PAVP Mode)			DT	Manual	ToDo	ToDo																			
249 DAL Compliance																											
249 DAL_001		Intel® DAL applications cleanup			DT	PETS	Both	Pass																			
250 DAL_002		Intel® DAL test application installation and load			DT	PETS	Both	Pass																			
251 DAL_003		Intel® DAL communication channel exercise			DT	PETS	Both	Pass																			
252 Intel® Platform Trust Technology (Intel® PTT) Compliance																											
252 PTT_001		OSB Interface Communication Test			DT	PETS	Both	ToDo																			
253 PTT_002		Intel® PTT Windows® 10 Basic Functionality			DT	PETS	Both	ToDo																			
254 PTT_003		TPM Clear and Physical Presence			DT	PETS	Both	ToDo																			
255 PTT_004		Windows® 10 BitLocker Integration			DT	PETS	Both	ToDo																			
256 PTT_005		Windows® 10 BitLocker TPM Protection			DT	PETS and Manual	Both	ToDo																			
257 PTT_006		Windows® 10 Virtual Smart Card (VSC) Tests			DT	PETS	Both	ToDo																			
258 PTT_007		Microsoft® Windows® HCK TPM Tests			DT	Manual	ToDo	ToDo																			
259 PTT_008		Intel® PTT Enable/Disable from BIOS			DT	Manual	ToDo	ToDo																			
260 PTT_009		Power Transition Testing with Intel® PTT Enabled			DT	PETS	Both	ToDo																			
261 PTT_S10		Dictionary Attack Lockout After Coin Battery Removal with BOM Commit			DT	Manual	ToDo	ToDo																			
262 Boot Guard																											
262 BTG_001		Successful VM Boot to Windows®			DT	Manual	ToDo	ToDo																			
263 BTG_002		Unsecure Boot to OS			DT	Manual	ToDo	ToDo																			
264 BTG_003		Failed VM Boot fail to fallback			DT	Manual	ToDo	ToDo																			
265 BTG_004		Successful V Boot to Windows®			DT	Manual	ToDo	ToDo																			
266 BTG_005		Platform Public Sensor Key Provisioned			DT	Manual	ToDo	ToDo																			
General Info										Legal Disclosure																	
CFL-S ME12 FW Consumer										CFL-H ME12 FW Corporate																	
CFL-S ME12 FW Consumer										CFL-S ME12 FW Consumer																	
CFL-S ME12 FW Consumer										CFL-S ME12 FW Corporate																	
CFL-S ME12 FW Consumer										CFL-U ME12 FW Consumer																	
CFL-S ME12 FW Consumer										CFL-U ME12 FW Consumer																	
ATA																											

## 6.4.3 Report Viewer

The Report Viewer allows easy navigation between reports by clicking on them in the left hand pane. It is possible to export PETS logs, import previously exported logs, and delete test result log files. Also, viewing, navigating, and selecting several older/newer logs is possible in the list prior to saving or copying them into other folders. Additionally, simultaneous export/import actions for multiple PETS logs is possible as well.

**Report Viewer**

Select / Unselect All Export Selected Logs Import Logs Delete Selected Logs Maximum number of displayed logs: 100

Run Date: 4/15/2020 1:05:35 AM Console Version: 10.1530.0.0 Agent Version: 10.1530.0.0

**System Under Test**

**SUT Overview**

Name	EHL
Description	
Platform Version	ELKHARTLAKE
Platform SKU	Consumer
Platform Type	Mobile
Manageability Interface	WIRED_ONLY
Operating System	Windows

**General Host Settings**

User Name	admin
LAN IPv4 Address	10.12.227.35
LAN MAC Address	00-E0-4C-00-06-9B
Port Number	12000

**Power Settings**

Low Power Enabled	DeepSx
Low Power Policy	Disabled
Low Power Source Policy	Dc
Deep Sx Enabled	False

**Intel® APS Settings**

Power Provider	Intel(R) APS 3
PETS power provider Tracer	True

**Result Statistics**

Passed	2	100.0%
Failed	0	0.0%
Aborted	0	0.0%
Condition Skip	0	0.0%
Not-Running	0	0.0%

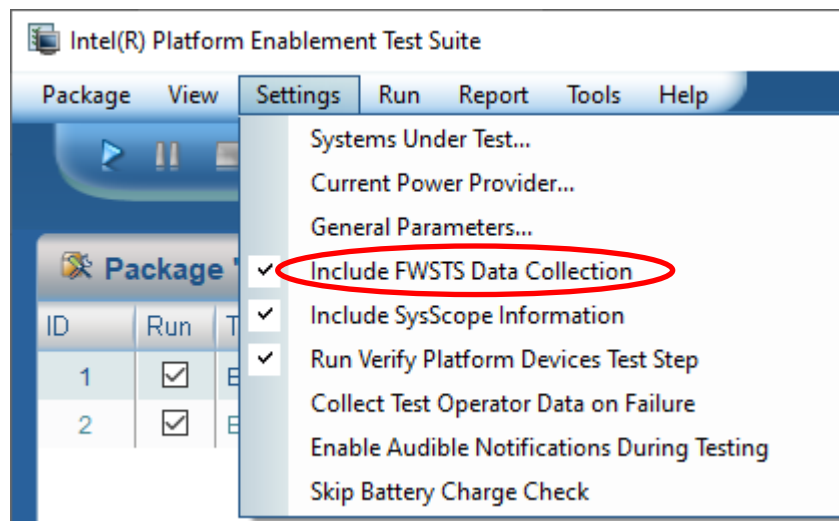
Location: C:\Program Files (x86)\Intel\Intel(R) Platform Enablement Test Suite\Logs\Log\_XMLs\

**The Viewer includes the following information:**

- Statistics on the tests run.
- Tests skipped (deselected ones).
- Steps not run because they were deselected, or earlier failure/abortion of a step failed (clicking the stop button).

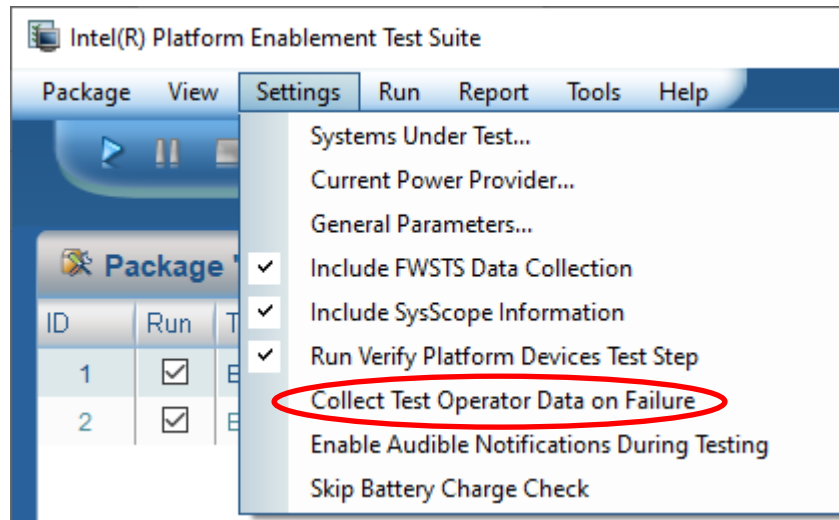
**Note:** Aborted tests may also include some failed tests. It is therefore recommended to view the PETS logs to check if any aborted tests actually failed before being aborted.

- Information about the SUT on which the tests were run, including:
  - **ME Platform:** BIOS Version, Intel® MEBx Version, LMS Version, MEI Driver version, Graphics Driver version, LAN Driver version, WLAN Driver version, OS Details [Edition, Version ,Build Number, Build Type, OS Architecture, OS Physical Memory Size, OS Virtual Memory Size] and System Directory
  - **TXE Platform:** Host Information, Firmware Information, and Graphics Driver.
- Information is collected using the FWSTS Data Collection and SysScope Information tools.

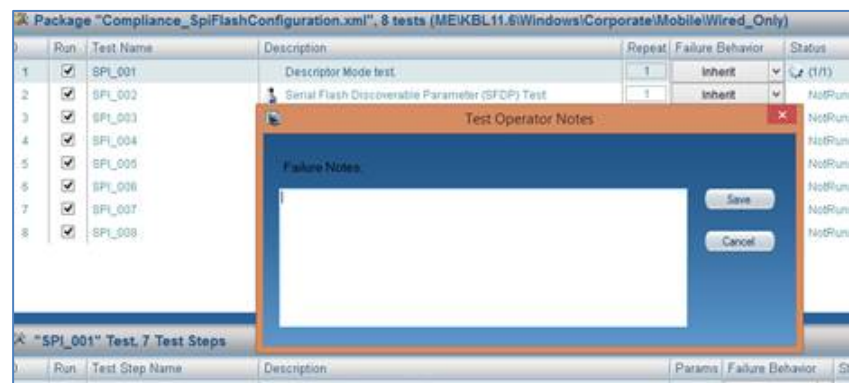


**Test Operator Additional Feedback upon Test Failure.**

'Collect test operator data on failure' feature is disabled by default. It is enabled by checking "Collect Test Operator Data on Failure" checkbox under Settings in the Main Menu



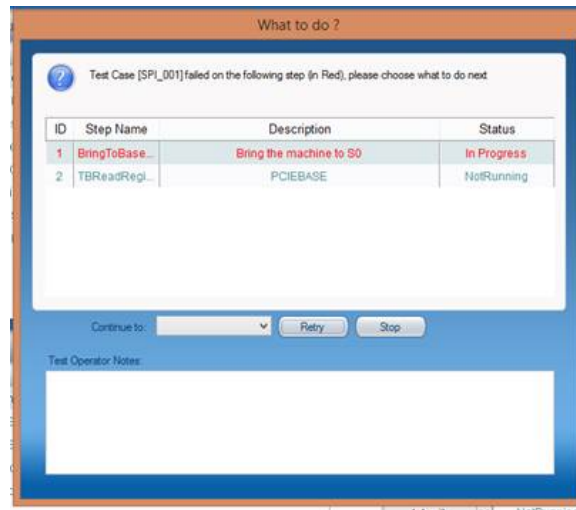
When enabled, regardless of the failure mode (except for Manual), a message box with a single input window appears allowing the test operator to add additional data about the failure at that point.



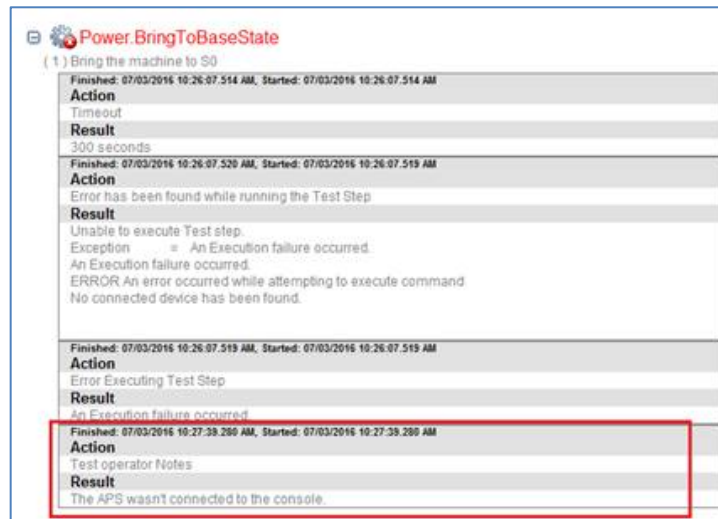
For all Question/Answer test steps, the window above will not appear. Instead, a single input window will be added within the same message box that allows collecting of additional information at the time the test operator selects Yes/No, at all times.



For all Manual failure behavior tests, a single input window is added to the bottom of the "What to do" window allowing the test operator to provide additional data/information/observations about the failure at that point.



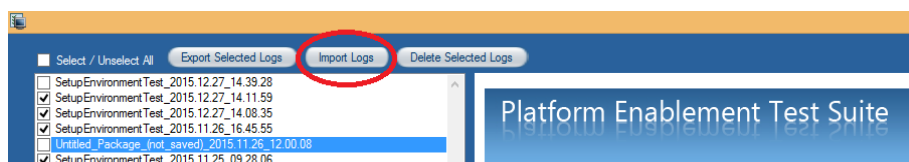
In all cases above, the window will allow up to 1024 input characters. The test operator's additional input will be copied into (appended to) the Intel® PETS log once the dialog box/message box hosting the input window is closed/cleared.



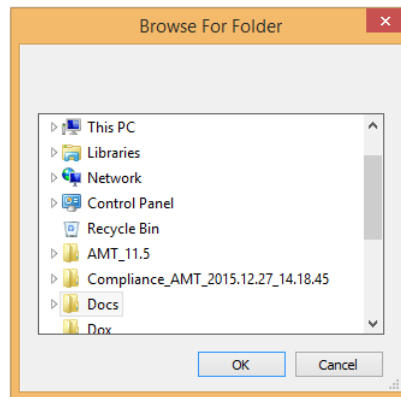
### 6.4.3.1 Importing Logs

To import log files:

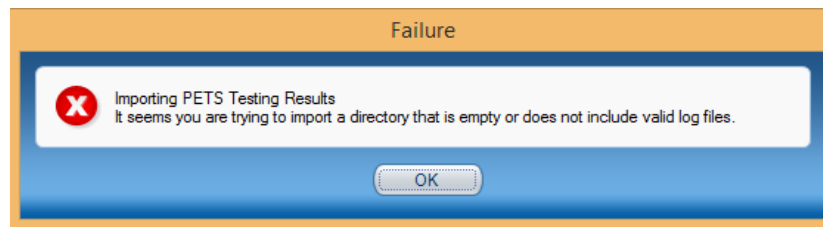
1. Open the Report Viewer.
2. Click **Import Logs**.



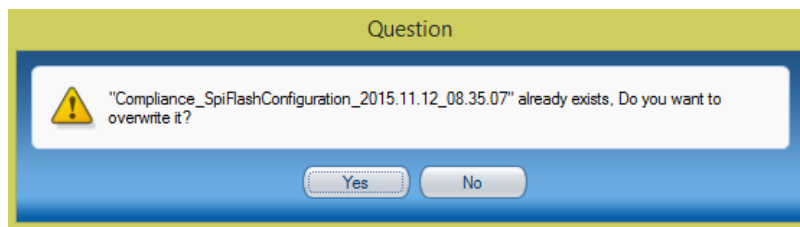
3. The "Browse For Folder" window opens. Navigate to the log folder.



**Note:** An error will be received when trying to import PETS testing results from an empty directory or a directory that does not contain valid log files.



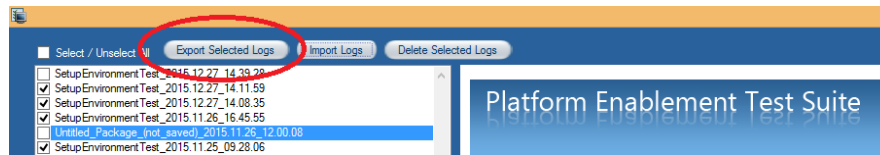
**Note:** When importing a log from a directory that already exists, a message appears informing the user that the "Log directory <dir name>" already exists. Do you want to overwrite it? This message only appears when the content in the directory conflicts with an existing one.



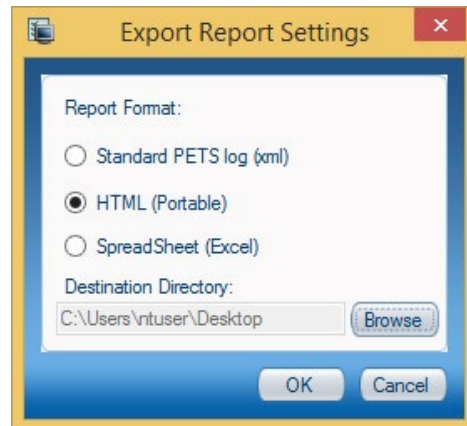
### 6.4.3.2 Exporting Logs

#### To export log files:

1. Open Report Viewer.
2. Browse the report/s and select the log file/s you want to export from the list.
3. Click **Export Selected Logs**.



4. The Export Report Settings dialog opens. Set the desired export format from the following report formats: Standard PETS log (XML), HTML (Portable), or Excel Spreadsheet.



5. Click **Browse** to set the Destination Directory. A dialog opens prompting you to select the target directory in which the logs are saved. Clicking Browse again sets the previously selected target directory to be the new default directory.
6. Click **OK**. Intel® PETS saves the logs in the requested format in the target directory.

**Note:** Intel® PETS remembers the last destination a report export was saved to.

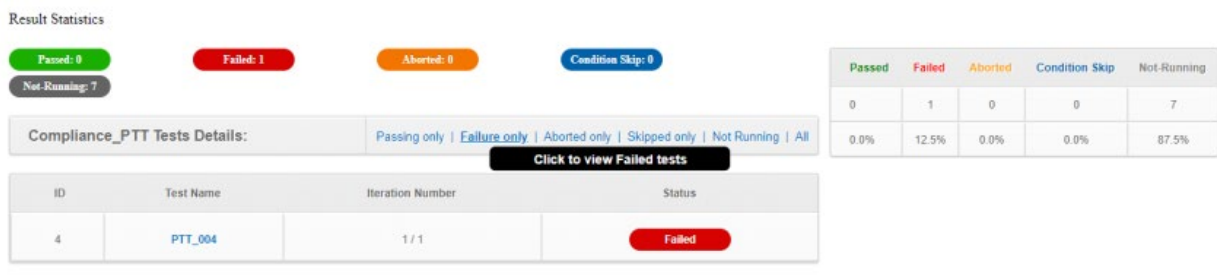
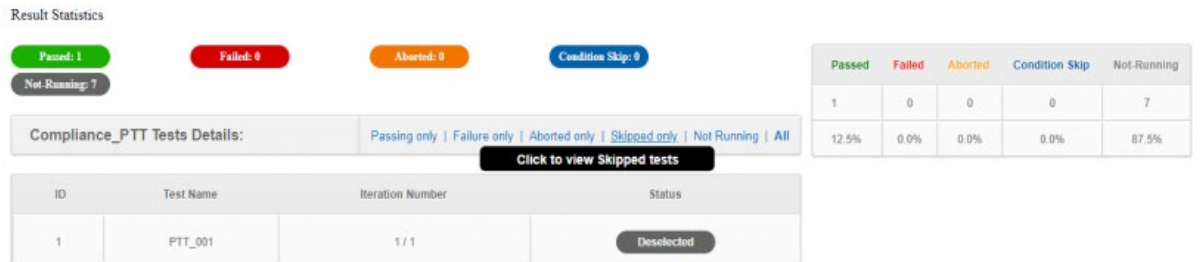
**Note:** Intel® PETS generates the HTML output and Spreadsheet only when choosing to export logs. They are not available by default in the "Logs\Log\_XMLs" folder.

When exporting an HTML report, the file can be shared with other people without consuming any extra resources and can view it using any Internet browser. This report captures all the test steps for each test.

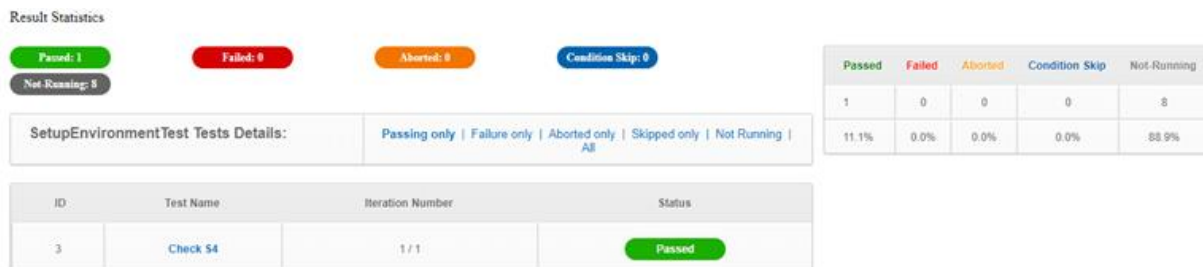
The HTML Report Type includes the following:

- Information about the SUT on which the tests were run.
- System Scope information.
- Statistics on the tests run.
- Status for each test/s and steps.

**Note:** Users can filter the test results on either the "test level" or "step level" in the same manner for both cases according to their ending results whether they are passed or failed. A tooltip will appear guiding the user to filter the test results accordingly.



**Note:** Another tooltip will appear when pointing the mouse at the Passed ,and Failed ,Aborted and Not Running results labels, prompting the user to click in order to filter the test results accordingly. For example, Clicking on “Passed” label as shown below, filters the test results to show only passed ones. The same applies for “Failed” test results label as well.



Intel® PETS also allows users to export and view the Test Results Report in Excel spreadsheet format. You will see tabs for multiple sheets.

- Pre-Work format sheet - Displays the test results in the pre-work format (only test case and sub tests are included).
- Verbose format sheet - Holds a verbose version of the log (all test steps are included).
- SUT Info sheet – Displays the System under Test information
- MC Info sheet – Displays the Management Console information
- Parameters sheet – Displays General and Package information



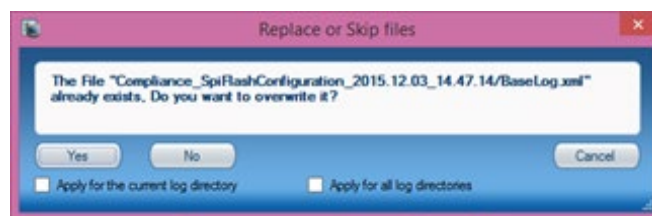


ID	Test Name	Test Case Description	Result	Failure Reason
000001_01	Check the SPI of PCH	Check the SPI of PCH	Pass	
000001_02	Check the SPI of PCH	Check the SPI of PCH	Pass	
000001_03	Check the SPI of PCH	Check the SPI of PCH	Pass	
000001_04	Check the SPI of PCH	Check the SPI of PCH	Pass	
000001_05	PCH I/O and SPI programming test	PCH I/O and SPI programming test	Fail	None

**Note:** By default, **Export Selected Logs** and **Delete Selected Logs** are disabled until the user selects a log.

**Note:** When exporting a log/s from a directory that already exists, a message appears informing you that the "Log file <log file name>" already exists. Do you want to overwrite it? If "Yes" is clicked, the change will be applied to the current file only.

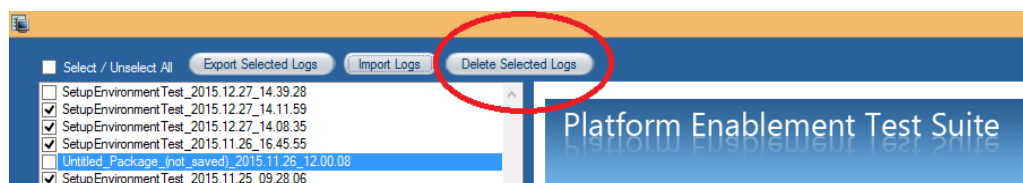
On the other hand, clicking on "No" will skip the current file and "Cancel" will terminate the whole copy process. This message only appears when the content in the directory conflicts with an existing one.



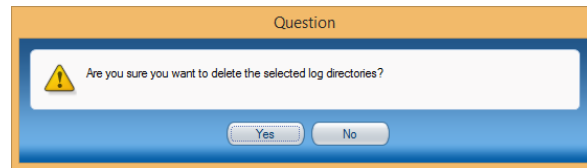
### 6.4.3.3 Deleting Logs

**To delete log files:**

1. Open Report Viewer.
2. Browse the report/s and select the log file/s that you want to delete from the list.
3. Click **Delete Selected Logs**.



4. When prompted for confirmation, click **Yes**.



## 6.4.4 Test Results Report SUT Information

To display information about the SUT in the Test Results report:

- Click the **Show** button at the top of the report.

To hide information about the SUT in the Test Results report:

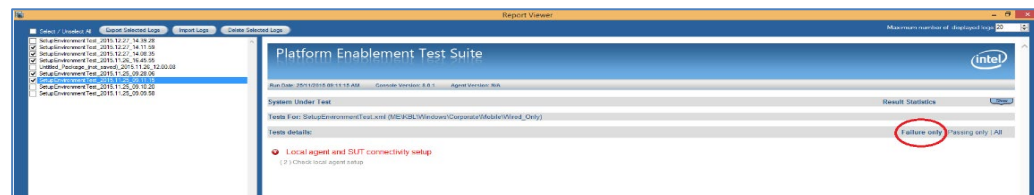
- Click the **Hide** button at the top of the report.

## 6.4.5 Test Results Report Test Information

The Test Results Report provides a high level summary for each test. You can also view detailed information—as explained below.

❖ **You can filter the test results according to one the following criteria:**

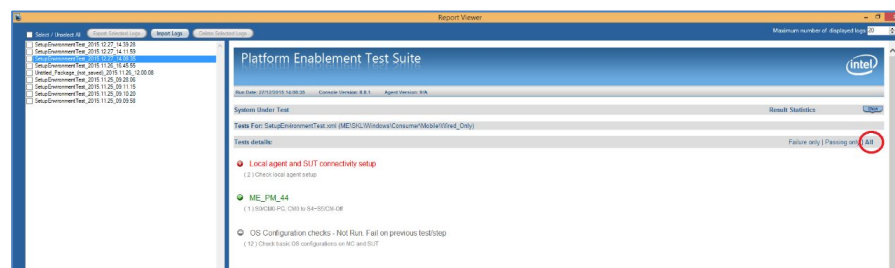
- **Failure Only:** PETS only displays failed and aborted tests.



- **Passing Only:** PETS only displays tests that passed and conditionally passed tests

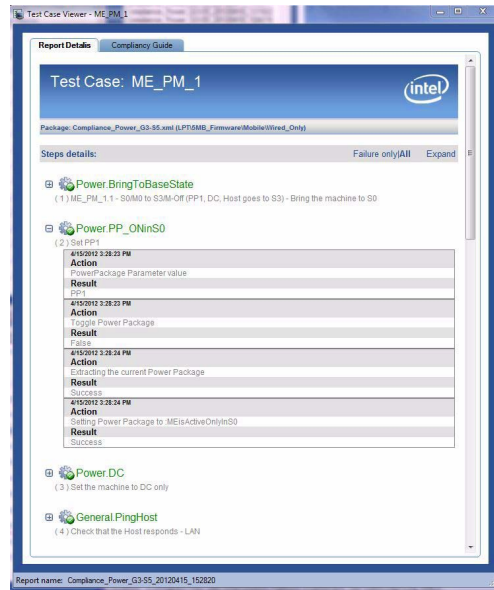


- **All:** PETS displays all the tests (passed, failed, ,aborted,conditionally passed and deselected tests).

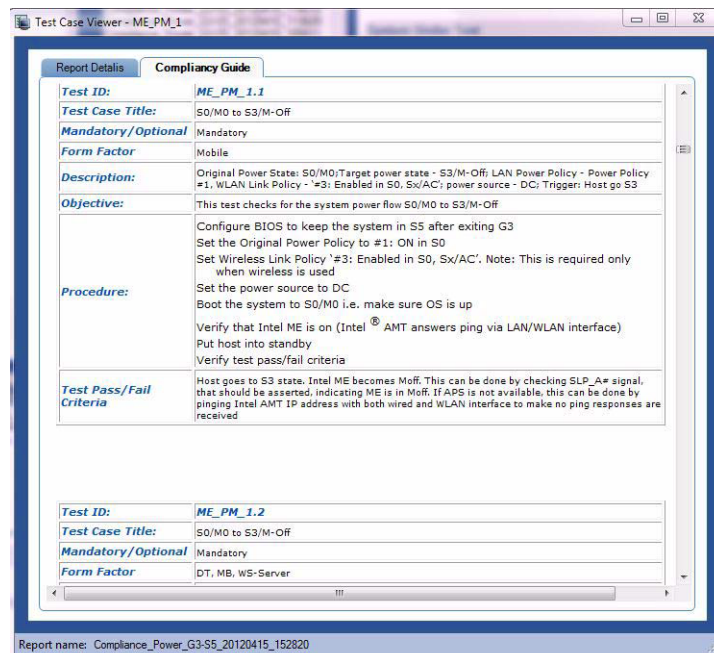




- ❖ **To view more information on a specific test, click on it to see the following:**  
A detailed secondary window appears that allows you to open the details of each step in the test, expand all, collapse all, or see all failures of that test.



- ❖ **You can also click the Compliance Guide tab on this window to open this test in the Compliance Guide.**





## 6.5 Results Worksheet Generation

The Results Worksheet is a new addition to Intel® Platform Enablement Test Suite that enables Intel® PETS users to update an excel sheet with the latest results of a set of tests performed on a defined SUT. This excel sheet provides statistics on the percentage of tests not performed, passing, failing, and blocked per SUT. Example and usage instructions are given below.

### 6.5.1 Important notes

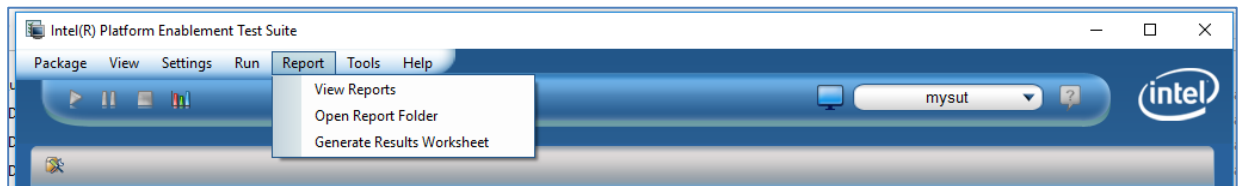
The excel sheet template is not embedded into Intel® PETS. To get the template for Intel® CSME 12.0 based platforms, contact your CE representative. Excel templates for other Intel® ME/ TXE projects will be available in the future.

Please note that the results worksheet will be updated and saved in an excel file regardless if the console has Microsoft Excel or not. To be able to view the worksheet with all results statistics for your SUT, please make sure your console has Microsoft Excel.

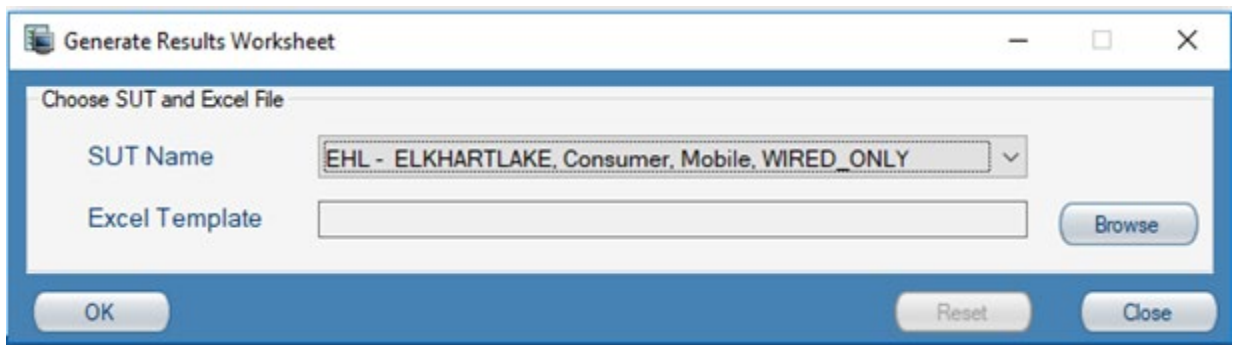
### 6.5.2 Usage

Once Intel® PETS logs are ready, follow the steps below:

1. Select "Generate Results Worksheet" option from the "Report" menu-bar item as seen below.



2. Choose the desired SUT then import an excel template, where:
  - a) **SUT Name:** The drop down menu shows information about user-defined SUTs currently available in Intel® PETS. The selected option should belong to the desired SUT to generate the results sheet for.
  - b) **Excel Template:** Click "Browse" to import the Microsoft Excel template file containing the set of tests and results to be updated.





3. After importing is complete, the “Generate Results Worksheet” dialog will be updated with additional requested input for mapping with excel, where:

- a) **Worksheet to Fill:** The drop down menu shows options of all sheets in the imported excel template file.
- b) **Test Name Column:** Refers to the column name containing the list of tests.
- c) **Result Column:** Refers to the column name containing the test results to be updated

Generate Results Worksheet

Choose SUT and Excel File

SUT Name: EHL - ELKHARTLAKE, Consumer, Mobile, WIRED\_ONLY

Excel Template: .Desktop\Desktop\EHL -ELKHARTLAKE - PETS Compliance Test Results.xlsx [Browse]

Mapping with excel

Worksheet to Fill: EHL Consumer

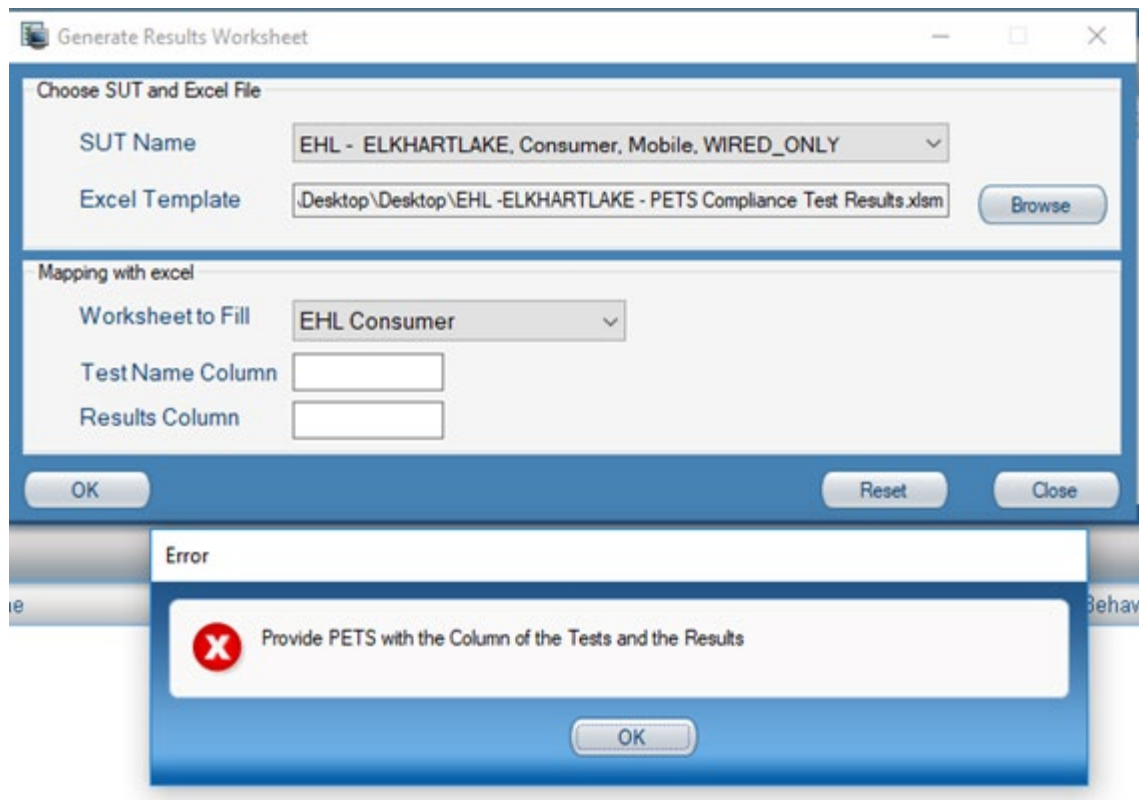
Test Name Column: A

Results Column: F

OK Reset Close

**Note:** Click the **Reset** button to clear all input values.

**Note:** The input for mapping with excel will be updated automatically by Intel® PETS according to the imported excel file. The user might have to update these fields manually if Intel® PETS cannot locate the “Test Name” or “Result” columns (i.e. value will be N/A).



4. After successfully setting all input values, click the **OK** button to save the updated excel file in a user defined location.

## 6.6 Using the Compliancy Guide

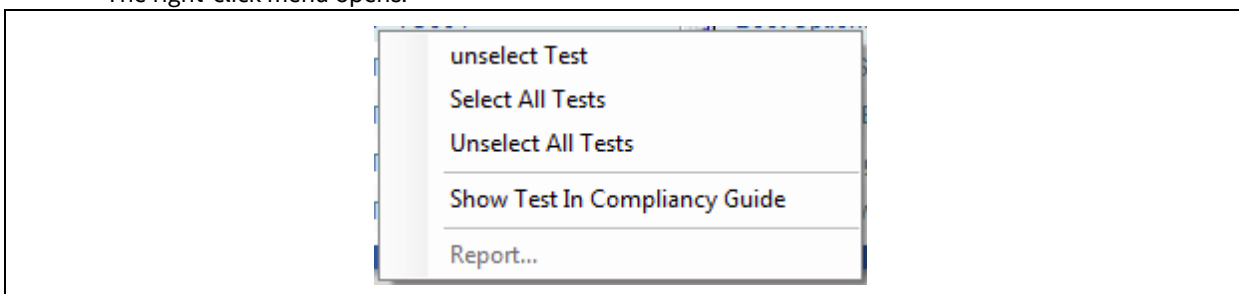
Intel® Platform Enablement Test Suite allows you to display any test in the Compliancy Guide.

**Note:** This feature is supported in Internet Explorer 7 or above, and in Firefox 3 or above.

### To display the test in the Compliancy Guide:

1. Right-click on any test in the PETS test pane.

The right-click menu opens.



2. Select "Show Test In Compliancy Guide".

The relevant test opens in the Compliancy Guide in a separate pop-up window.

## 7 Changing Package Parameters

You can use the Package Parameters dialog to modify the time-out times, LPT Port, WLAN IP, WLAN and LAN Mac Addresses, and other parameters of a test package, to reflect the settings required for the specific system being tested.

If you change a package parameter and then save the test package, the new settings will be used when you next open the package.

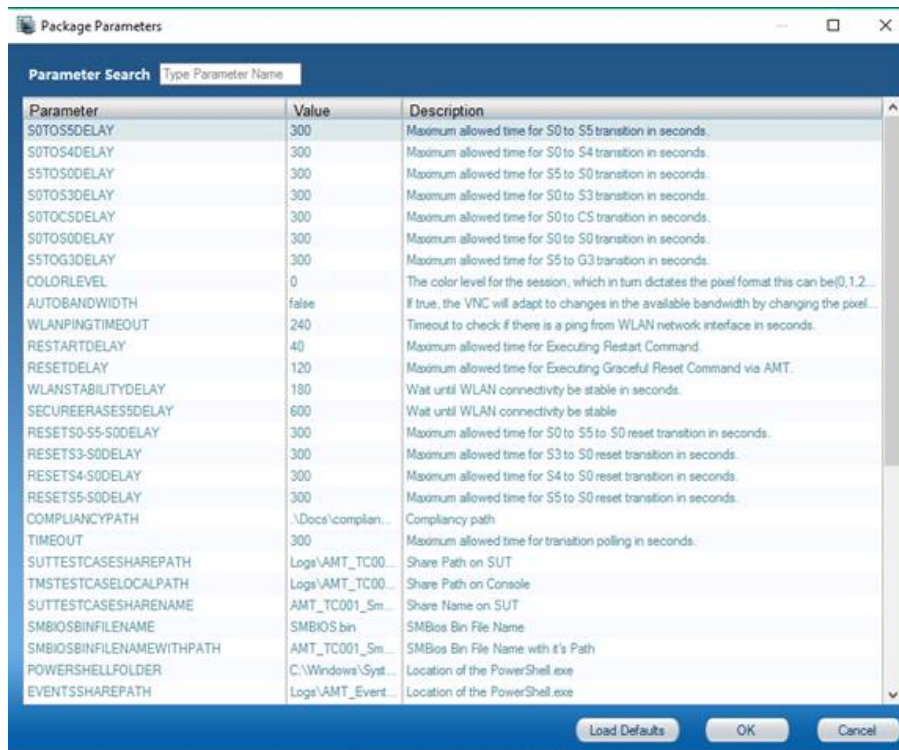
To revert to the original default settings, navigate to the **<PETS Installation Folder>\Packages\<Package Name>.xml.global** file and delete it. When you subsequently open the package, it will use the original settings.

Package Parameters are specific to each test package. Any changes in the Package Parameters do not affect the Package Parameters for any other package, even if they have the same name and meaning. Different packages have different lists of Package Parameters.

### 7.1 Changing Parameter Values

**To open the Package Parameters dialog in Intel® Platform Enablement Test Suite:**

1. Open a test package.
2. Choose **Package > Package Parameters**.





### To search for a package parameter:

Go to the search box next to the “Search Parameter” label and type in the package parameter name that you're looking for. The parameters will be filtered where the package parameters specification table will only show and highlight the parameters that have the user searched text in their name.



### To change parameter values:

**Note:** The values of Package Parameters that are time-related are represented in seconds.

1. Double-click the value of the parameter you want to change; the value is selected.
2. Type in a new value for the parameter.
3. Repeat steps 1-2 for all the parameters you want to change.
4. Click **OK**.





The following table describes the package parameters of test packages.

Parameter	Description
APSDelay	<p>Many of the Intel® Platform Enablement Test Suite tests include a step called <b>Bring To Base State</b>. This operation includes sending the machine a G3 command (disconnecting all power sources) followed by an ACDC command (connecting the AC and the DC power supplies), followed by a power-up command.</p> <p>The APSDelay parameter allows you to adjust the delays between these three operations. For the power packages, the default delay is 5 seconds.</p> <p>Example of use: Some platforms require more than 5 seconds to complete a G3 operation, so you should lengthen this parameter when running tests on these boards.</p>
S0ToS5Delay	Maximum allowed delay time for transition from S0 (platform is on) to S5 (power cable is connected but platform is off)
S0ToS4Delay	Maximum allowed time for transition from S0 (platform is on) to S4 (hibernate)
S4ToS0Delay	Maximum allowed time for transition from S4 (hibernate) to S0 (platform is on)
S5ToS0Delay	Maximum allowed time for transition from S5 (power cable is connected but platform is off) to S0 (platform is on)
S0ToS3Delay	Maximum allowed time for transition from S0 (platform is on) to S3 (standby)
S3ToS0Delay	Maximum allowed time for transition from S3 (standby) to S0 (platform is on)
ResetS0-S5-S0Delay	Maximum allowed time for S0 to S5 to S0 transition
IdleTimeExpireDelay	Maximum allowed time before ME goes to MOff in Power Policy 2 (M3->MOff)
MEWakeUpPing	Time to send pings when waking ME from M-Off
PingTimeout	Time to wait when verifying system is not responding to pings
STABLEDELAY	Wait after 'sensitive' steps to ensure system is ready for next step. 'Sensitive' steps are those that involve changing the SUT power state, for example, moving to ACDC or G3. Such operations leave the SUT unstable for several seconds, and PETS needs to wait for the system to become stable before continuing to the next step. This value (in seconds) tells PETS how long to wait.
CheckMETimeout	Time to wait when verifying ME is off or on. Check that ME is on for 20 seconds. This is only for a negative test when ME is off.
AfterPingDelay	Delay after doing ping till ME goes to M3 state
G3Exit	Maximum allowed time for exiting G3 (all power sources disconnected)
ACReconnect	Maximum allowed time for reconnecting AC power source
COMPLIANCYPATH	Path to the Compliance Guide help for each test
WirelessDelay	Delay before wireless link preference could be set again
Timeout	Time to wait in 'Delay' steps and many other cases
AfterRcpOpDelay	Time to wait after a Remote Control operation
RestartDelay	Maximum allowed time for restart
AMT_TC002_AsfTablesGeneration_TmsFolderPath	Path to place the tables file in test AMT_TC002
RemoteInterface	Remote interface to use for AMT and KVM operations, can either be LAN or WLAN
AfterPopDelay	Delay after displaying a pop up window



Parameter	Description
KVMOptinFWTimeout	Timeout for Intel® KVM technology Opt-in message to close
CD_IMAGE_PATH	Path of CD image used in IDER Boot
Floppy_IMAGE_PATH	Path of floppy image used in IDER Boot
1MinuteIdleTimeExpireDelay	Idle time of ME to go from M3 to M-Off when Idle time is set to 1 minute. The actual time the system takes to go to M-Off is typically at least 70-100 seconds. Units are seconds.
PreS0TransitionDelay	Wait when in Sx state before performing a transition to S0. Some platforms cannot perform the transition without waiting some time in Sx. Units are seconds.
S0initializationDelay	Wait in S0 before transitioning to Sx state. Units are seconds.
RCOPowerCycleDelay	Maximum allowed time for RCO Power Cycle operation to complete. Units are seconds.

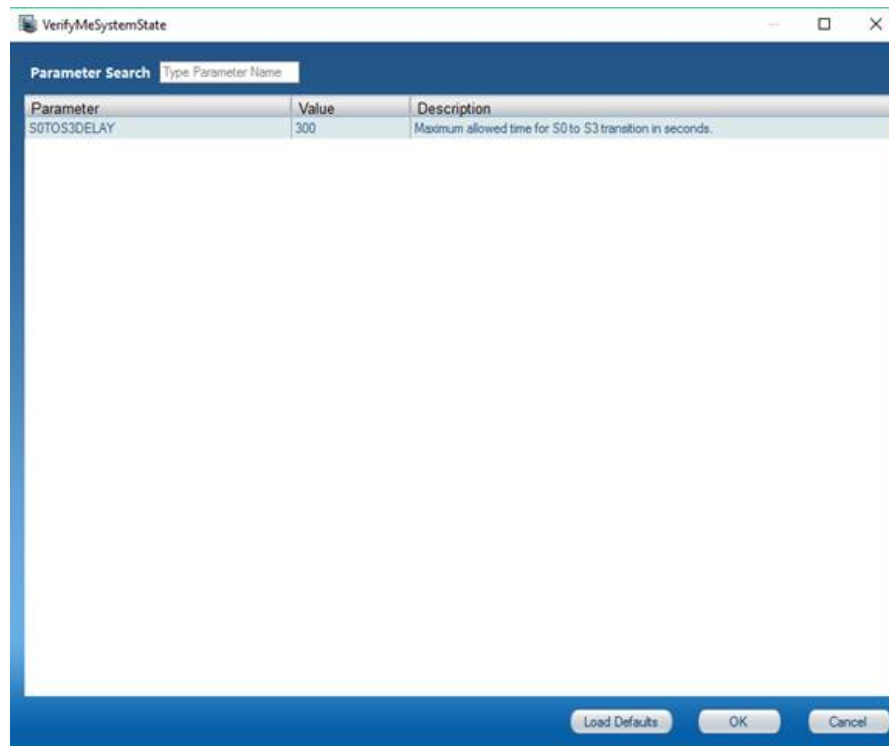
**Note:** Please be aware that not all package parameters will be the same for all packages.

#### To load package parameter default values:

- Click "**Load Defaults**" and then "**OK**".

**Note:** By clicking **Load Defaults**, PETS resets the package parameter values to their original values (the initial values when PETS ran for the first time). Each package has its own parameters.

## 7.2 Test Step Parameters




### To load test step parameter default values:

- Click **Load Defaults** and then **OK**.

**Note:** By clicking **Load Defaults**, PETS resets the test step parameter values to their original values (the initial values when PETS ran for the first time). If a parameter is shared between the test step and the package, the changes are reflected on both sides.

### To view step parameters:

- Click the help icon beside in the **Params** column for steps that accept parameters.

"ME_PM_1" Test, 24 Test Steps					
ID	Run	Test Step Name	Description	Params	Failure
1	<input checked="" type="checkbox"/>	BringToBaseState	ME_PM_1.1 - S0/M0 to S3/M-Off (PP1, DC, Host goes to S3) - Bring the		
2	<input checked="" type="checkbox"/>	PP_ONinS0	Set PP1		
3	<input checked="" type="checkbox"/>	DC	Set the machine to DC only		
4	<input checked="" type="checkbox"/>	PingHost	Check that the Host responds - LAN		

A window opens displaying the parameters related to the selected step (similar to that shown in [Changing Parameter Values](#)). You can view and change the parameter values as required.



## 7.3 Power Transition Timeouts

The power management packages contain a definition of the power transition time-out. When the Intel® Platform Enablement Test Suite sends a command to perform a power transition, it polls the Intel® PETS Automated Power Provider for the power state to verify that the power transition took place. When the time specified by the time-out has passed, the test is considered to have failed.

The following table lists the time-out for each power transition.

Power Transition	Macro Name	Time-out (sec)
Unknown State to S0	UnkToS0Delay	5
S0 To S3	S0ToS3Delay	5
S3 To S0	S3ToS0Delay	5
S0 To S4	S0ToS4Delay	5
S4 to S0	S4ToS0Delay	5
S0 to S5	S0ToS5Delay	5
S5 to S0	S5ToS0Delay	5

**Note:** These values are valid for an Intel® CRB and you may need to change them for other systems. To change the delay times for your specific platform, you must modify the settings in the Intel® Platform Enablement Test Suite's Package Parameter's dialog.

## 8 Changing General Parameters

The General Parameter dialog can be accessed through **Settings > General Parameters** menu item.

General parameters are the parameters that are shared among all the packages in PETS. General Parameters are used to resolve the differences between different platforms supported by PETS. Modifications to one of the parameters will be affect all the steps that utilize it.

You can use the General Parameters dialog to modify the general parameters listed in it. Currently, PETS defines some general parameters for the commonly used "BringToBase" step as shown in the image below:



### 8.1 Changing General Parameter Values

#### To modify the General Parameters:

1. Double-click the value of the parameter you want to change; the value is selected.
2. Type in a new value for the parameter.
3. Repeat steps 1-2 for all the parameters you want to change.
4. Click **OK**.

#### To load general parameter default values:

- Click **Load Defaults** and then **OK**.

**Note:** By clicking **Load Defaults**, PETS resets the general parameter values to their original values (the initial values when PETS ran for the first time). If a parameter is shared between the test step and the general parameters, the changes are reflected on both sides.



## 9 Running PETS in Package Design Mode

---

Package Design Mode provides additional capabilities such as creating packages and tests from scratch, modifying existing tests, or adding extra tests and steps.

Test steps are the building blocks for PETS test packages as they determine the specific functionality for each package. For example, you might need to ping the SUT as part of your test; a test step named "Ping" provides this functionality. If you need to add a delay after changing the SUT power state, you can use the "Delay" step to provide a delay with a configurable duration.

PETS provides a large selection of out-of-the-box steps which are grouped into "Test Sets" depending on the functionality they provide. For example, the power steps can be found under the Power plug-in.

Each step has a name, description, input parameters and output parameters. Sometimes, you might need to add a more specific description for the step depending on the functionality the step provides for your test. For example, the CMD step is a very general step used to run a command. After adding it as part of your test, you might wish to call it "Running the <Name of tool> tool" or "changing a setting" this will make your test easier to understand.

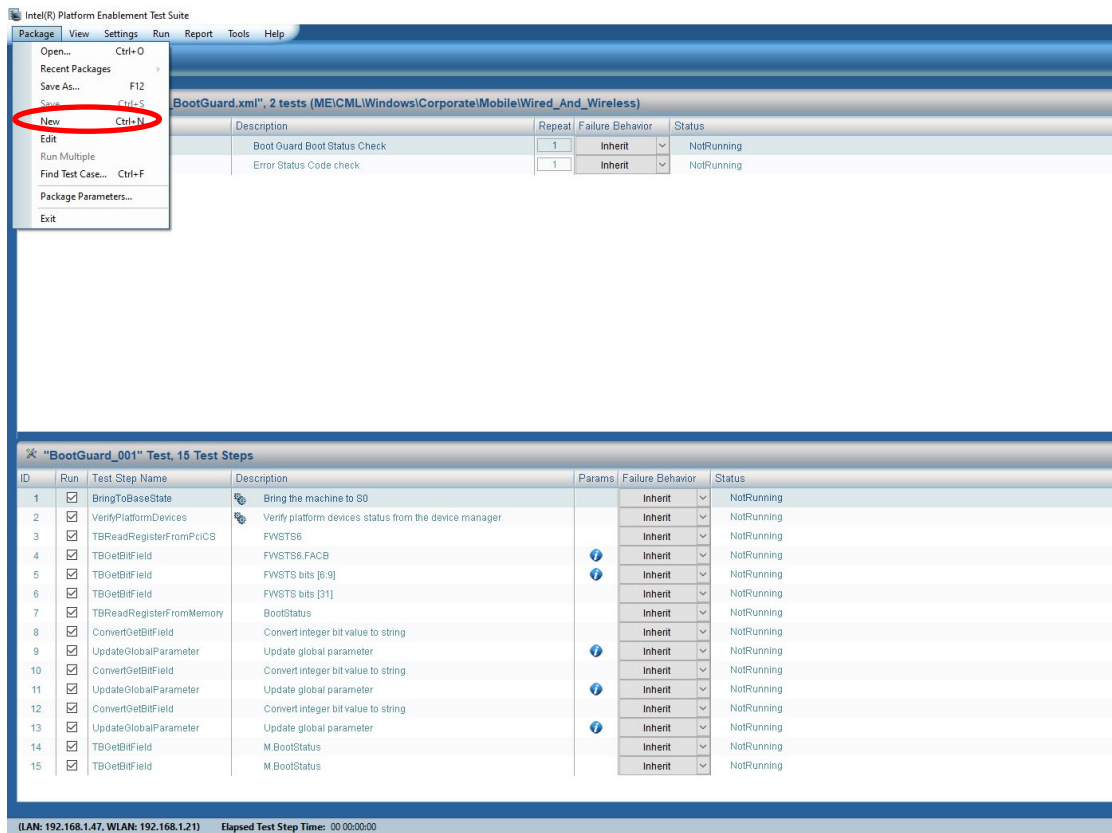
You can filter the steps to get a specific plug-in step or use the free text search option to search for the step in all plug-ins and various tests or search for one in the Search Step bar.

### 9.1 Creating a Blank Test Package

You can create new packages and tests, add steps, and run the test. This enables you to run tests during package building and verify the results are as expected, and modify the test package as needed.

#### To create a new test package:

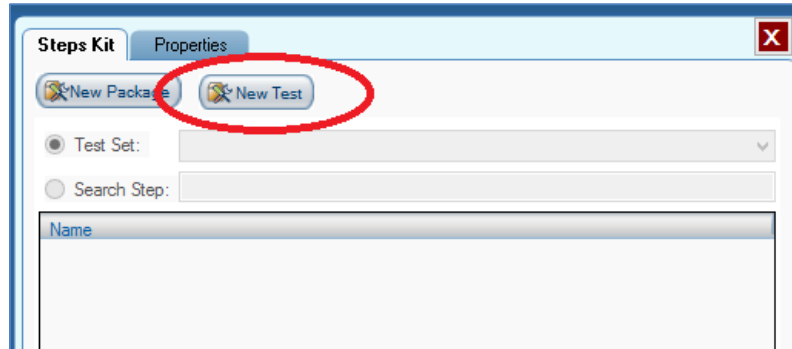
1. Choose **Package > New**. The Package Design Mode panel appears on the left side of the PETS interface.



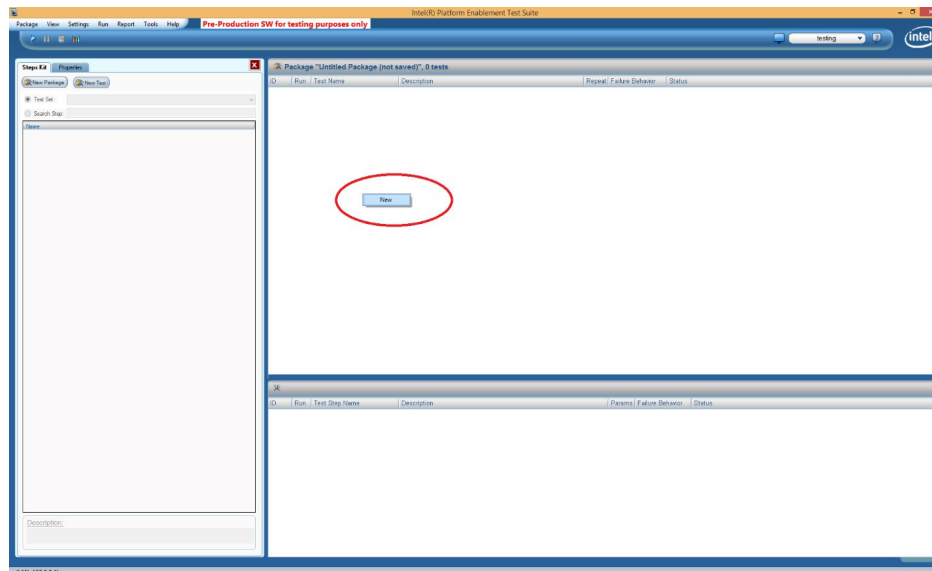
2. Add tests to your package, as described in the next section.

## 9.2 Adding Tests to a New Package

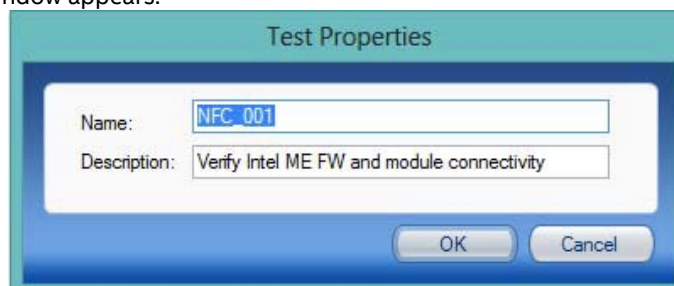
1. Click **Steps Kit > New Test**.



2. Right-click the test pane and select **New**.



The Test Properties window appears.

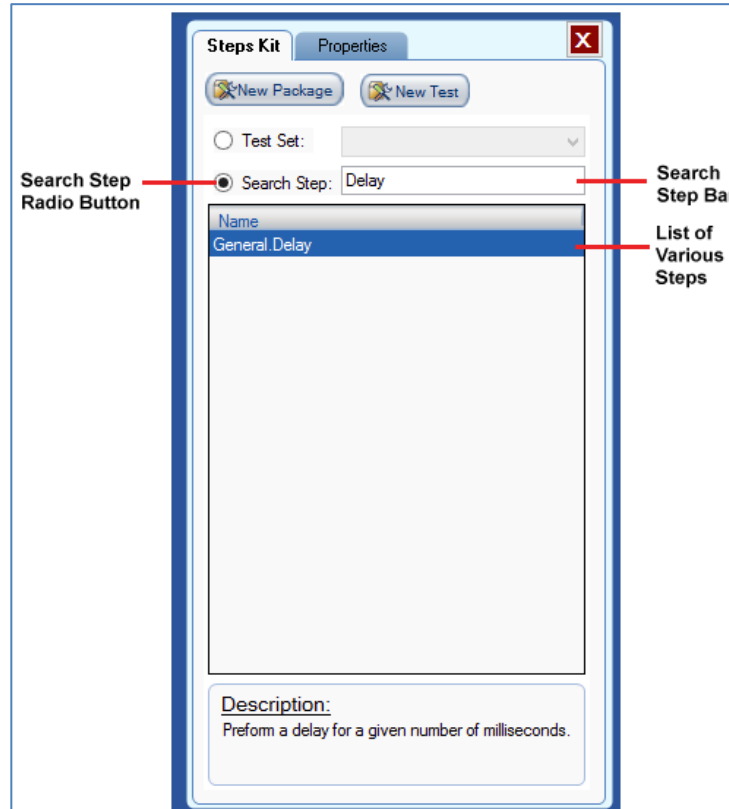


3. Enter a unique **Name** for your new test, add a **Description**, and click **OK**.
4. The new test is loaded and displayed in the test pane. Add test steps, as described in the next section.
5. Repeat this procedure whenever you wish to add a new test.



## 9.3 Adding Steps to the New Test

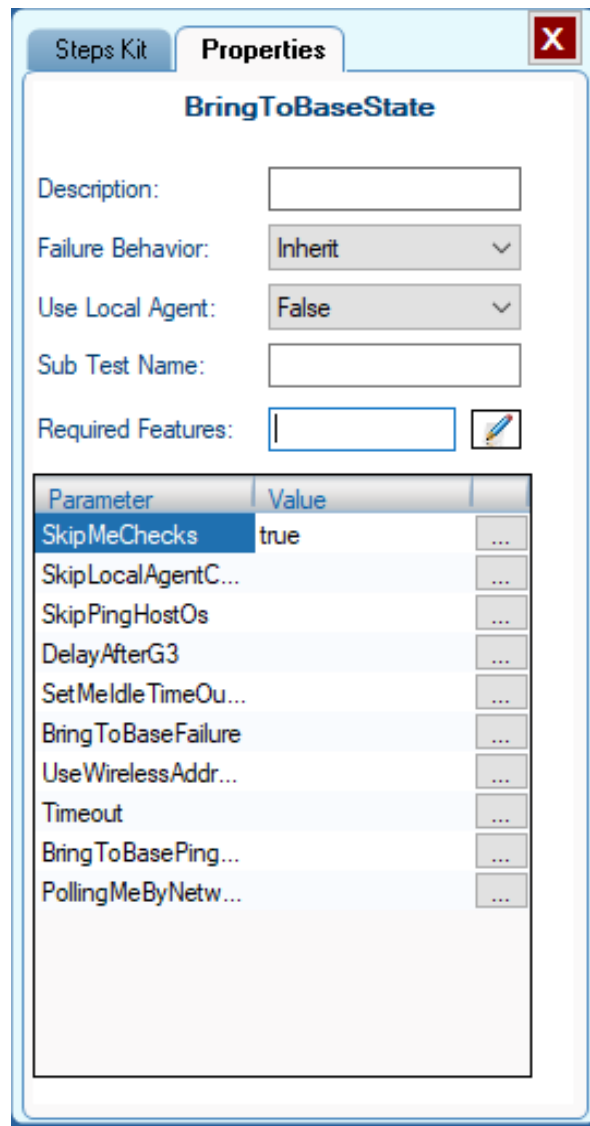
- Select **Search Step** under the Steps Kit tab.



### 9.3.1 Configuring Step Properties

You can edit the following step properties in the Properties tab of the Package Design Mode panel:

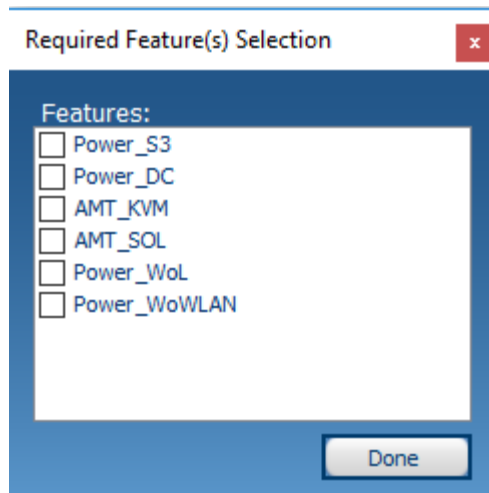
- **Step Description.**
- **Failure Behavior** - Enables you to instruct PETS how to proceed after a test step has failed. Possible values: {Inherit, Next Step, Next Sub Test, Next Iteration, Next Test, Stop, Manual}.
- **Use Local Agent** - Indicates whether the step should be executed on the local agent (SUT). Possible values: {True, False}.
- **Sub Test Name** - Indicates whether the selected step is the beginning of a sub test. (Sub test is another level of hierarchy and will result in GUI and reports). You only need to set this value for the first step in the sub test - do not configure this parameter for the other steps in the sub test.
- **Setting Step Parameters** – Each step has its own set of parameters depending on the functionality it provides. For example, the Ping step has the IP to ping and the Delay step has the total delay in seconds. For more information, refer [Configuring Parameters](#).



Parameter	Value
SkipMeChecks	true
SkipLocalAgentC...	
SkipPingHostOs	
DelayAfterG3	
SetMeldleTimeOu...	
BringToBaseFailure	
UseWirelessAddr...	
Timeout	
BringToBasePing...	
PollingMeByNetw...	

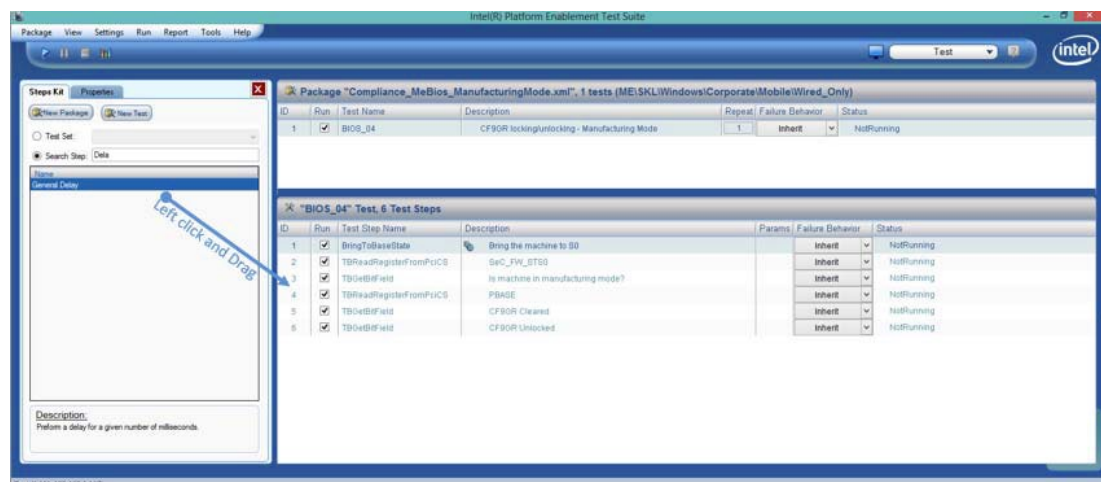
**Required Features** let you Add Required Features to the selected test step in two ways:

1. Either by writing the Required Features in the text box
2. Or by clicking the **Configure Feature(s) selection** button which will open the below window that enables you to choose Prerequisites from the list to be added to the selected test step.



### To add a step to a test:

Select the step to be added and drag it from the Package Design Mode panel to the Test Steps Data Grid view, as shown in the following figure.



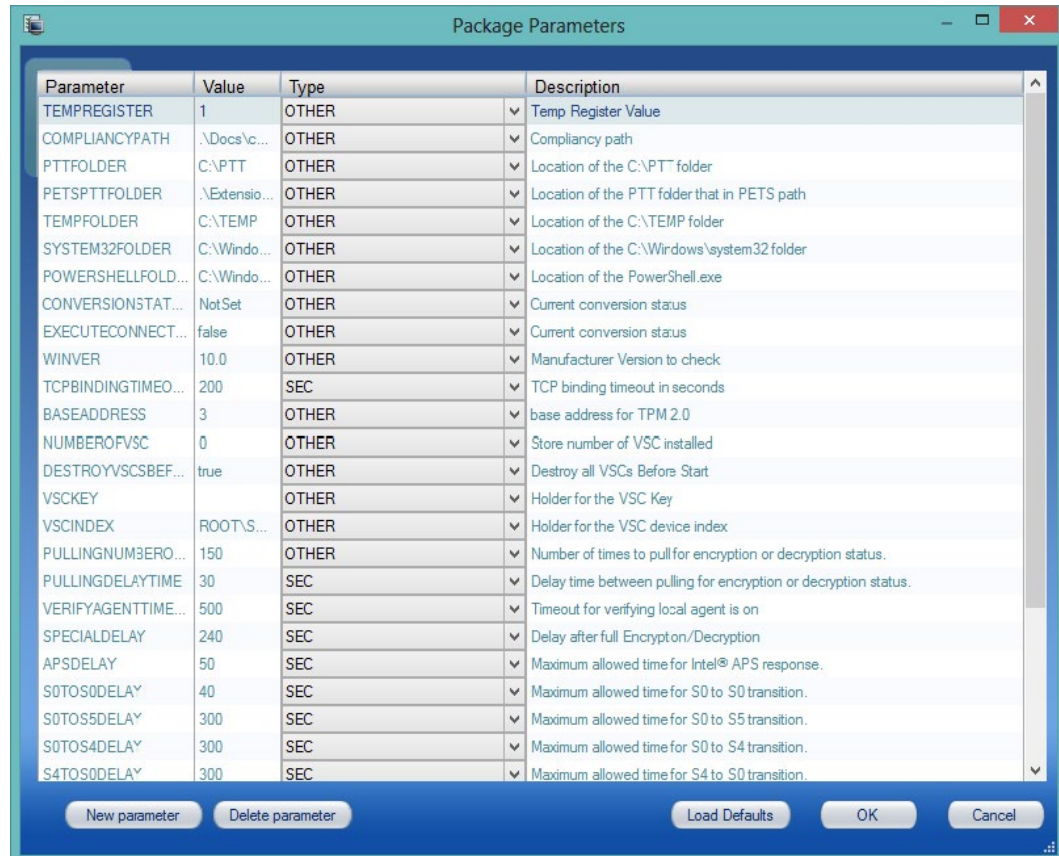
The step is added, the step IDs are rearranged, and the Steps Grid Header is updated.



## 9.4 Configuring Parameters

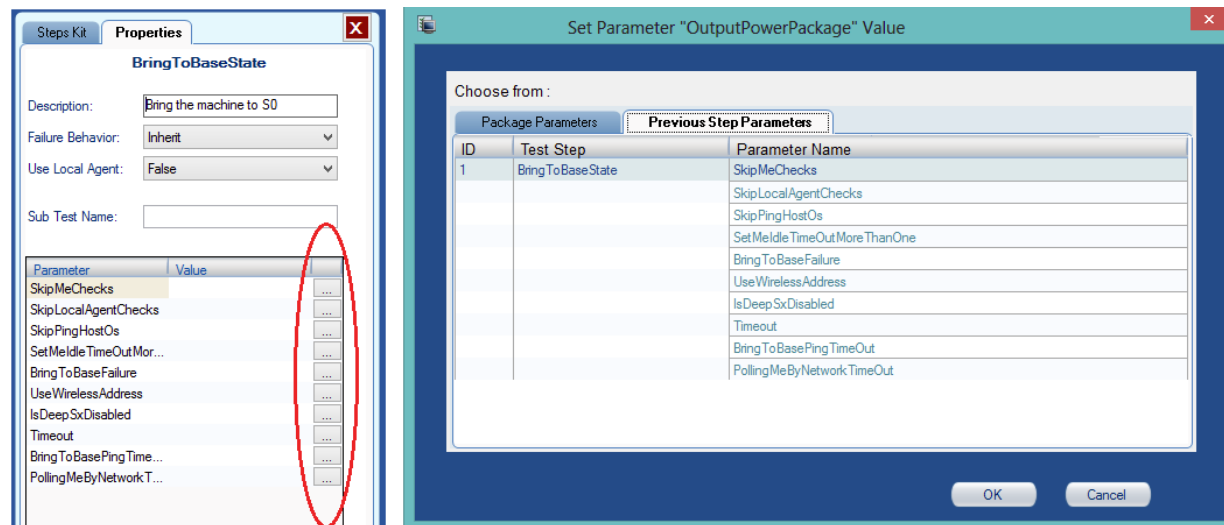
### 9.4.1 Configuring Package Parameters

When Package Design Mode is active you can modify, add, or remove any of the Package Parameters shown in the following figure.



### 9.4.2 Configuring Step Parameters

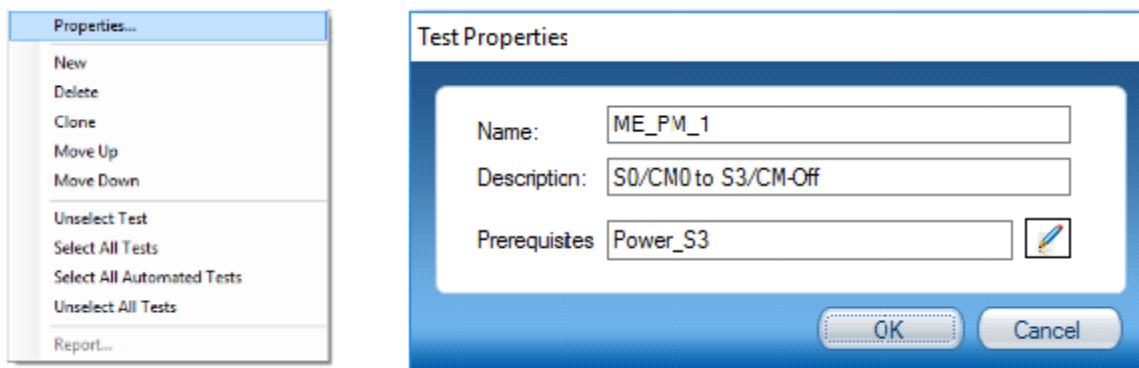
1. Access the Properties tab of the Package Design Mode panel.
2. Select a parameter to modify.
3. Do one of the following:
  - Insert an immediate value.
  - Click on the Browse symbol next to the parameter and select a value from the Package Parameters or the previous Step Parameters tabs



## 9.5 Modifying Test Properties

Right-click a test and select **Properties**. The Test Properties dialog appears, enabling you to change the test properties.

The test is modified according to the user's changes.



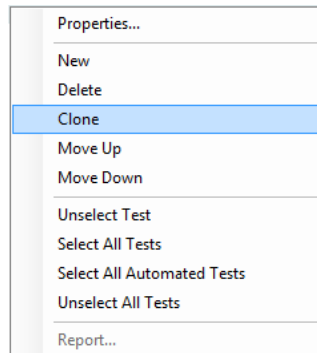


## 9.6 Cloning a Test

You can clone tests within a package by creating copies of all the test steps, including step parameters, and applying the same settings.

### To clone an existing Test:

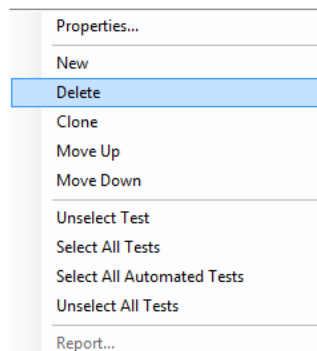
1. Right-click the desired test and select **Clone**.



2. Enter a unique name for the new test.
3. The test and its steps are cloned, including all the step parameters.

## 9.7 Removing a Test or Test Step

1. Right-click the desired test/step and select **Delete**.



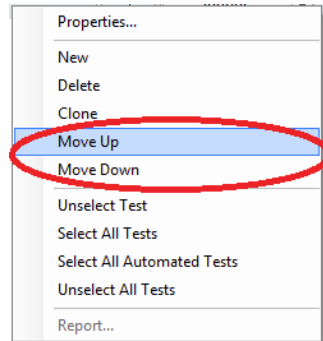
2. The test/step is removed from the test list. The step IDs are rearranged and the Steps Grid Header is updated.

**Note:** If any following steps use the step you wish to delete, the system prompts you for confirmation before proceeding with the deletion.

## 9.8 Moving a Test or Test Step Up/Down

**To rearrange (reposition) items within a test list:**

Drag and drop the desired test to the desired position in the list or right-click the test and select **Move Up** or **Move Down**.



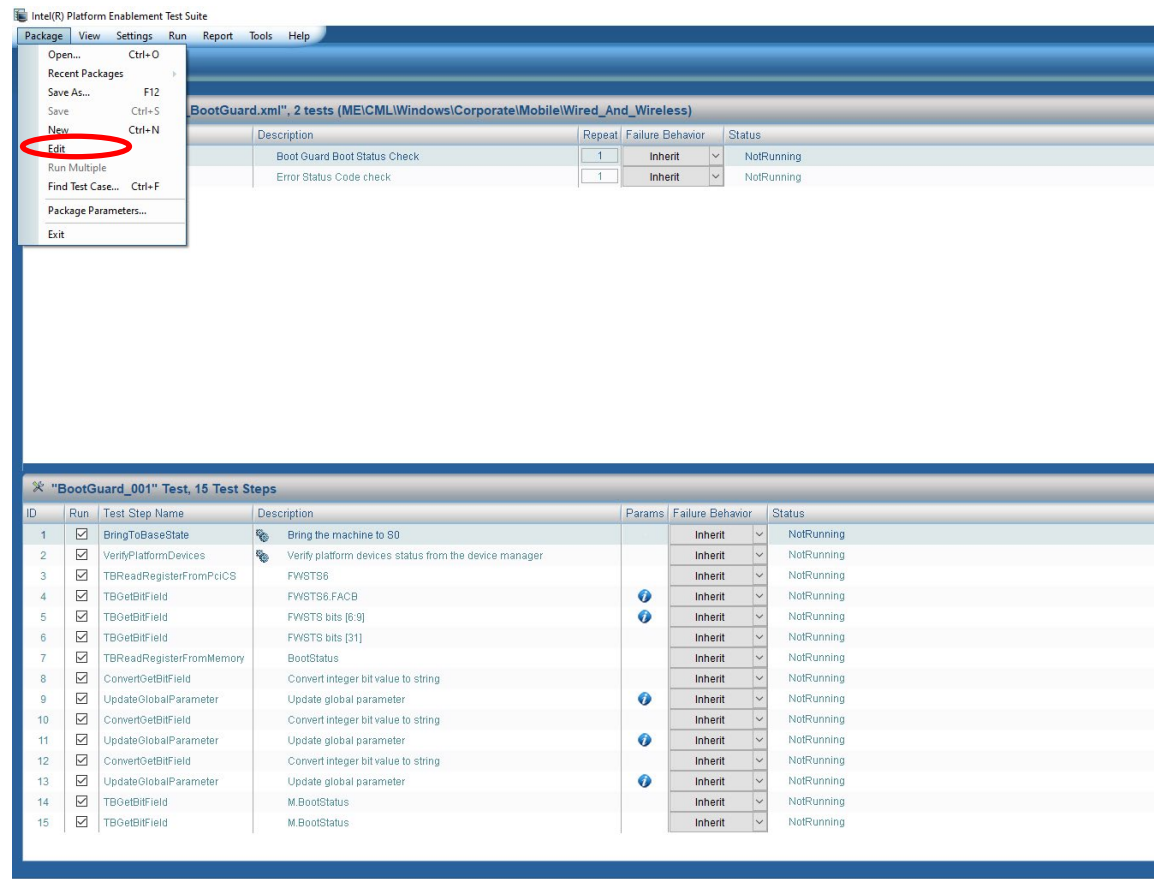
**Note:** If any following steps use the step you wish to reposition, the system prompts you for confirmation before proceeding with the move.

## 9.9 Modifying an Existing Package

You can modify an open package and apply the changes as needed.

**To edit an open/existing package:**

1. If the package is not open, choose **Package > Open** from the Package menu to open the package.
2. From the main menu, choose **Package > Edit**



- 3. From the Open window, select a package.
- 4. Make the desired changes using the editing tools.

**Note:** The Edit Package option is only active if you have a package open in the GUI.

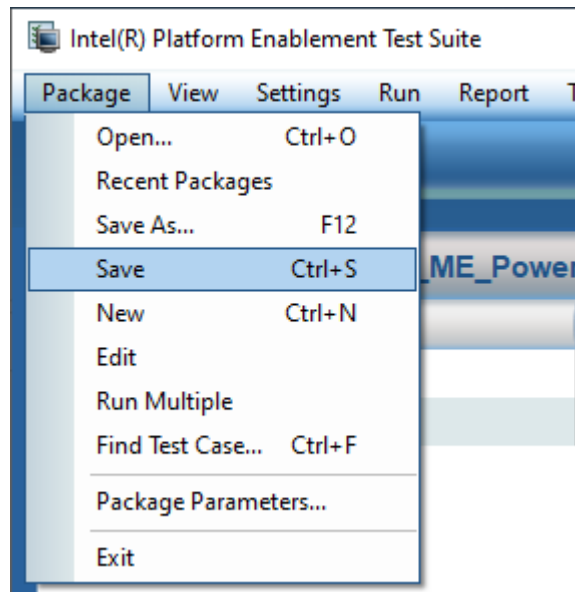


## 9.10 Saving a Package

When Package Design Mode is active, PETS checks if the package has been modified. In this case, PETS prompts you to save unsaved changes.

**To save the changed configuration of a test package as an original version:**

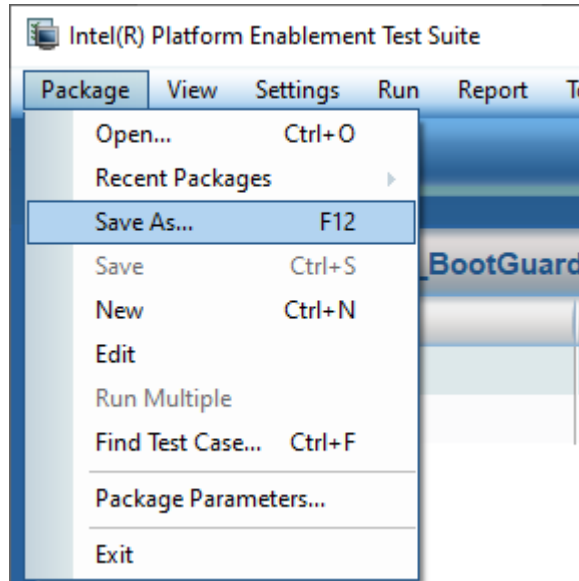
- Choose **Package > Save**.



Typically, you can use this function when editing an existing package and you wish to save changes as you work (prevent loss of information if a crash occurs). **Save** replaces the original file.

**To save the changed configuration of a test package as a new version:**

1. Choose **Package > Save As**.



When you have created a new package from scratch or you are basing the new package on one you have already opened/edited. This is useful if you want to save an existing package with a new name and then make changes rather than start from scratch. **Save As** leaves the original package unchanged.

2. Enter the name of the new package in the **Name** field of the Save As dialog.
3. Specify the desired location and click **Save**. The new version of the test package is saved in the desired location.

**Note:** PETS prevents you from saving an invalid package with a prompt message explaining the reason. The Package might be invalid for one of the following reasons:

- A test in the package has steps that use an invalid or non-existent package parameter.
- A test step is trying to read an input parameter from a step that does not exist.

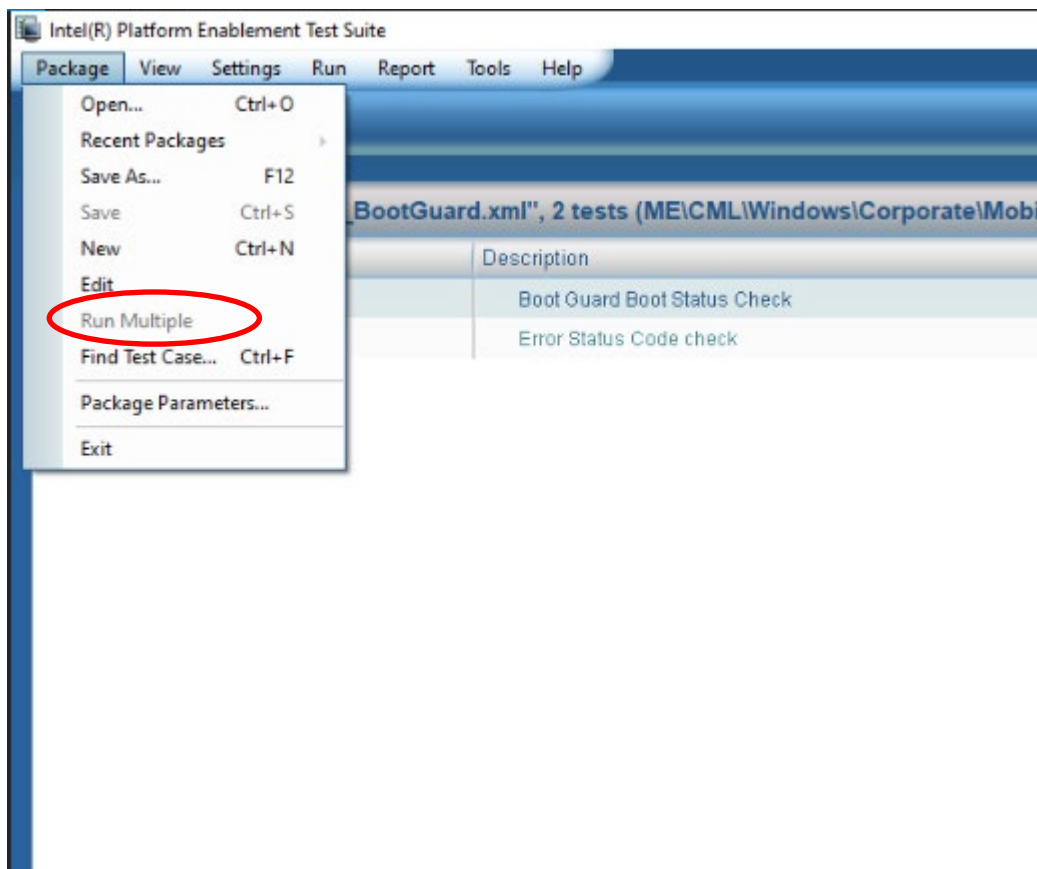
## 10 Running Multiple Packages

The PETS framework enables you to run multiple packages and save all the test results in an Excel file. This facilitates running automated tests overnight and reviewing the results when convenient.

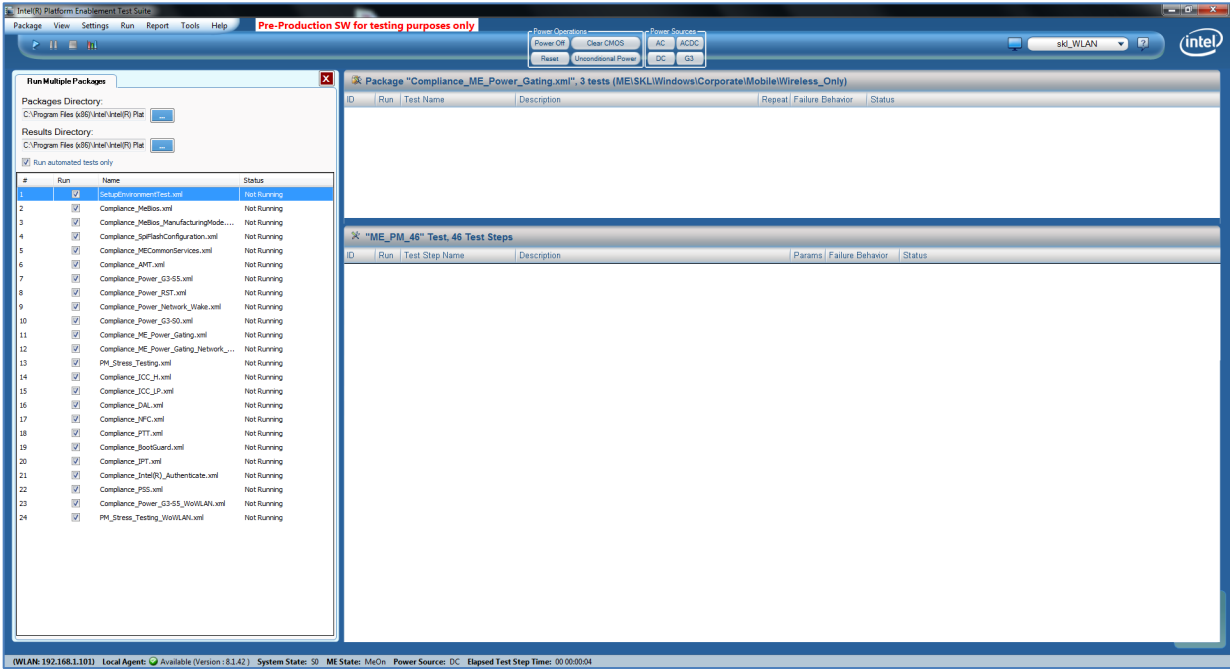
**Note:** To use this feature, you must configure a SUT with an Intel® PETS Automated Power Provider and this should be initialized; if the Power Provider is not Configured correctly, the menu item **Package > Run Multiple** is disabled.

### To use run multiple packages:

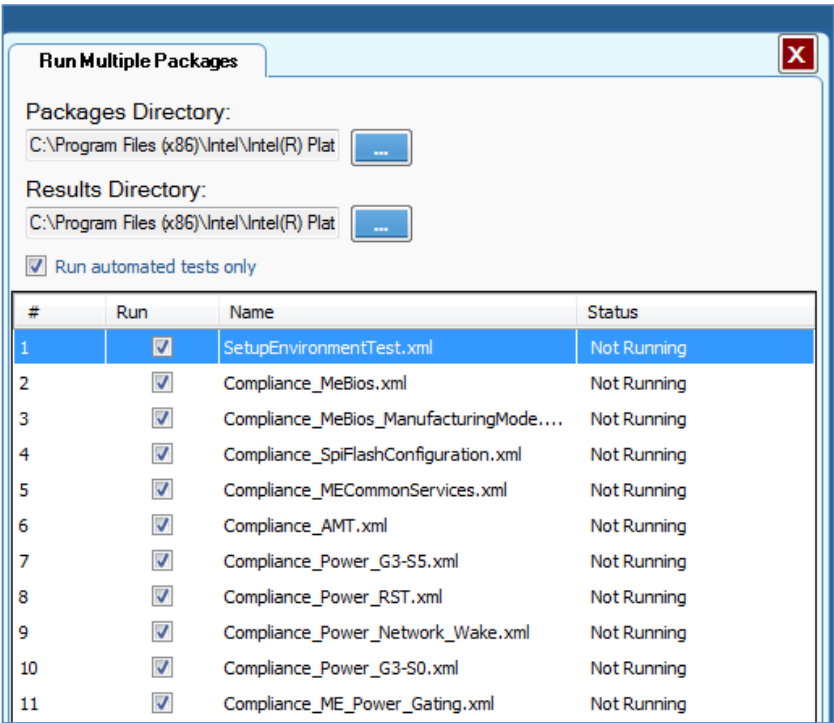
1. Click **Package > Run Multiple**.



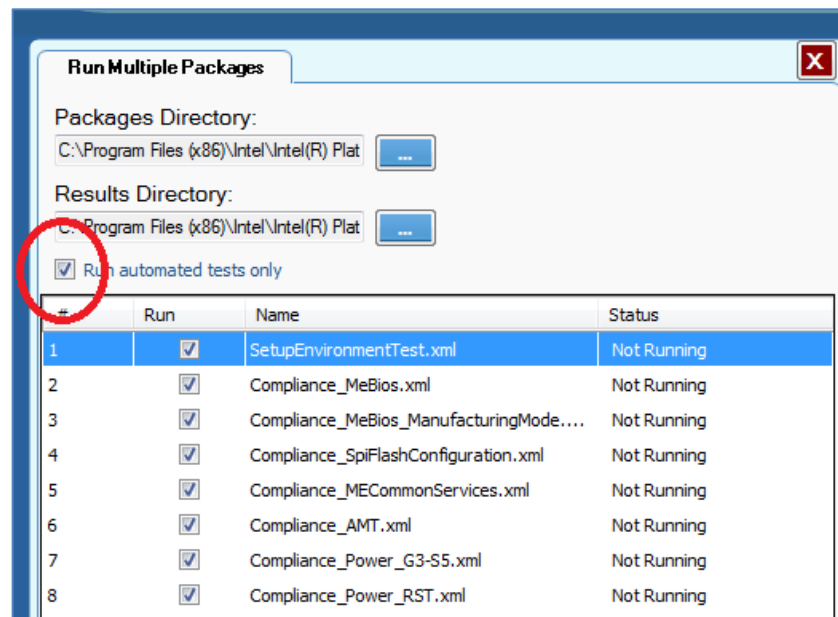
The Run Multiple Packages panel appears on the left side of the PETS interface displaying all the packages in the default directory of the selected SUT.



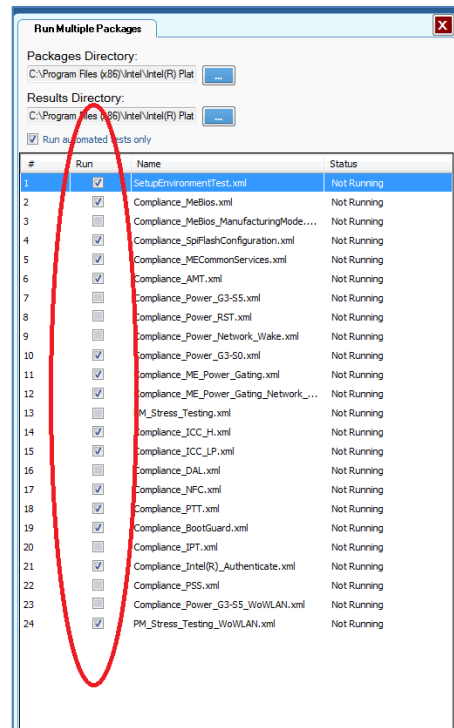
2. Set the **Packages Directory** (where package/s XML files are stored) and **Results Directory** (where tests results are stored.)



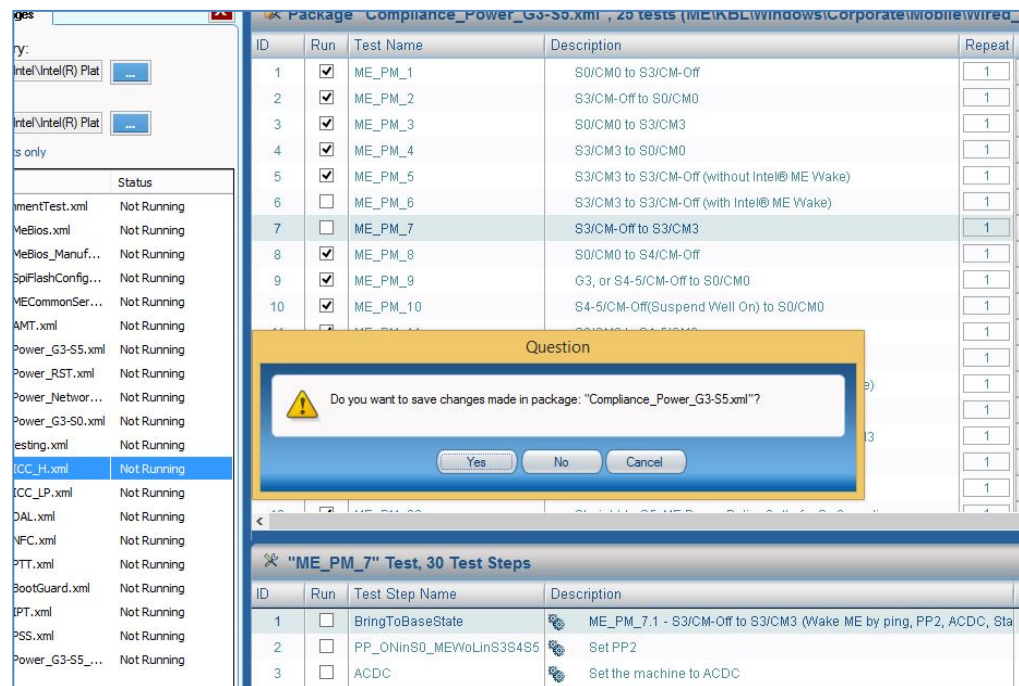
3. Check **Run automated tests only** to run only automated tests or clear this option to run all the selected tests both manual and automated).



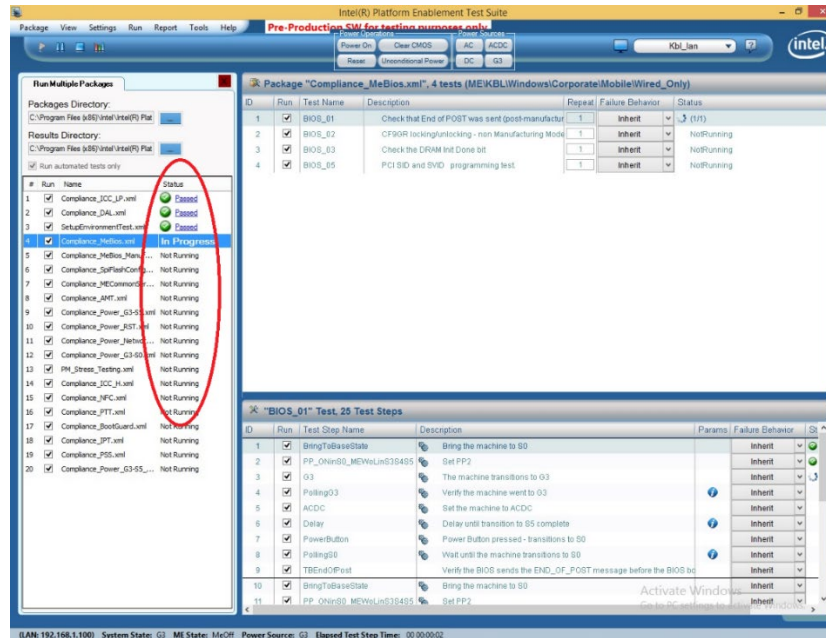
4. Select or deselect packages, drag-and-drop packages to change the order of execution, and click the **Play** icon.



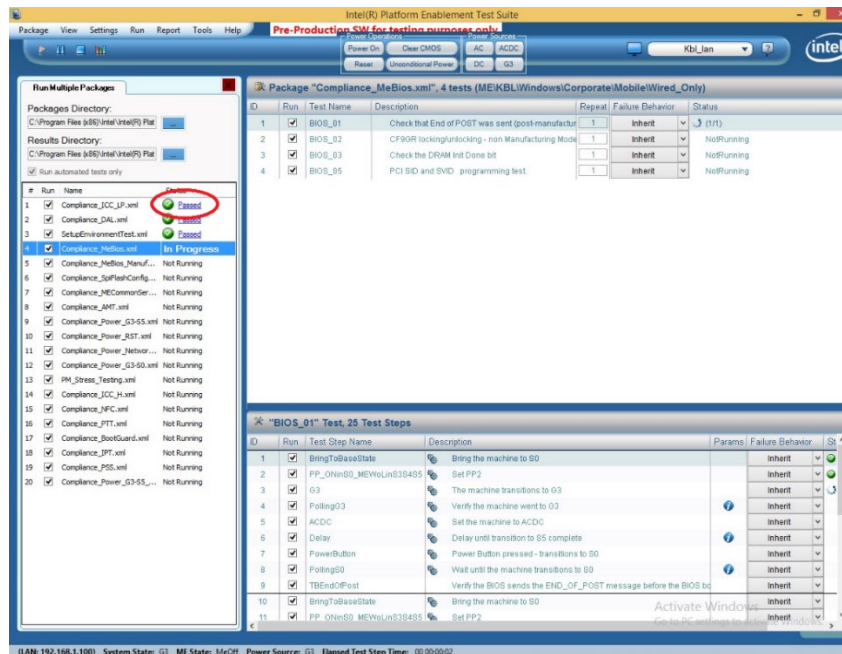
**Note:** When you make changes to any package, the system prompts you for confirmation before proceeding.



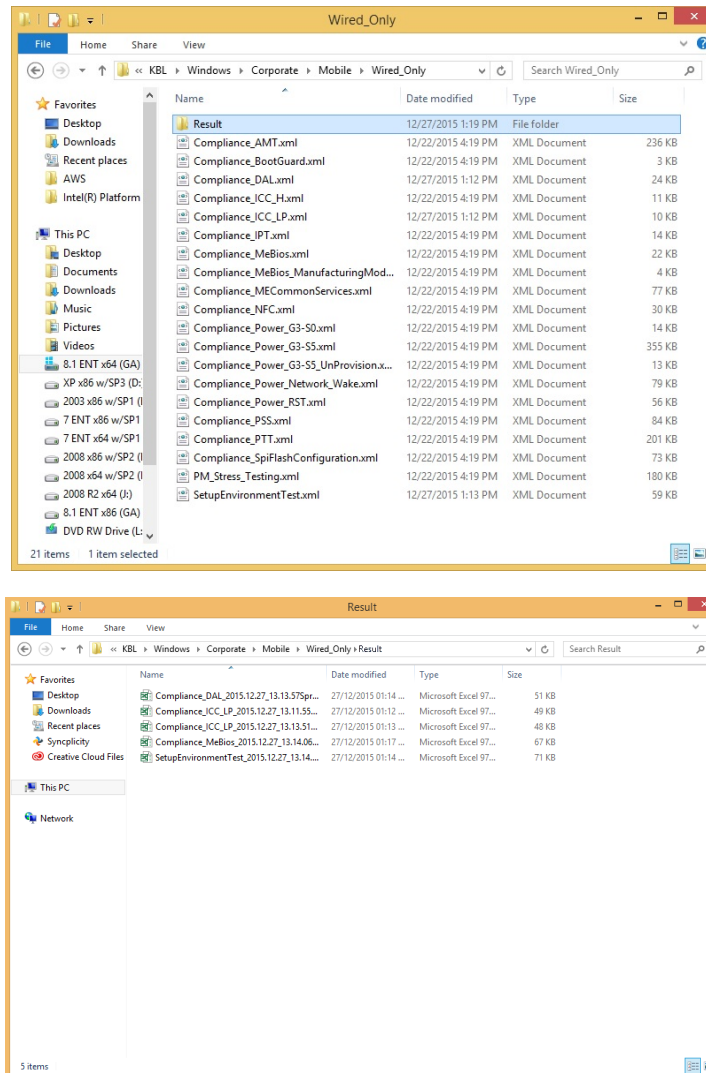
PETS runs the packages and displays a progress bar beside each package.



- When a package has completed execution, you can click its status and open the standard PET results log.



PETS creates Excel logs simultaneously and stores them by default in the Results folder nested under the packages folder.



**Note:**

- The **Skipped** status is displayed if the package has no automated test (all tests are manual) or if the tests have been deselected.
- The **Passed** status is displayed if all the tests passed successfully.
- The **Failed** status is displayed all package tests failed.
- The **Partially Failed** status is displayed if some tests failed.
- The **Aborted** status is displayed when the user clicks Stop.
- The default value for the **Packages Directory** field is the default package folder for the selected SUT.





## 11 Changing SPI Flash Parts Manufacturer Specifications

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Starting with PETS version 5.10.0, TXE SPI flash parts manufacturer specifications are now stored in an XML file named "TXESpiFlashManufSpecs.xml". This file can be found in the root folder of PETS Local Agent.

Migrating to XML should provide the ability for PETS customers to add their own SPI flash parts specifications to be checked during related tests (e.g., SPI\_004).

## 12 Adding Specifications

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To add your own specifications:

1. Locate the file named "TXESpiFlashManufSpecs.xml" at "(Program Files Directory)\Intel Platform Enablement Test Suite Local Agent\"
2. Edit the file with your favorite xml editor. (Recommended: Notepad++)
3. Scroll till the end of the file, and make sure whatever you want to insert (from the next step) will be before the </SpiFlashDevices> tag.
4. Use this example to guide you in entering the properties of your SPIFlashDevice.


```
<SpiFlashDevice>
  <DeviceName>MX25U6435F</DeviceName>
  <ManufName>Micron</ManufName>
  <ManufId>C2</ManufId>
  <JedecId>C22537</JedecId>
  <FlashSize>8388608</FlashSize>
  <FlashSpeed>50</FlashSpeed>
  <FlashBlockEraseSize>4096</FlashBlockEraseSize>
  <Vsccs>
    <Vscc>2005</Vscc>
    <Vscc>2001</Vscc>
  </Vsccs>
</SpiFlashDevice>
```

5. Save your changes.

### 12.1 Validating XML Contents

The validation of the XML contents is done during the test that uses the XML file. For example, if you make a mistake in one of the tags while entering your information, PETS should indicate the exact tag that is missing,

as shown in the following example:

 **NewSpiFlash.TBReadSPIFlashInfoFile**  
 ( 12 ) Read Flash device spec from XML file

5/8/2013 12:56:42 PM
<b>Action</b>
==ERROR==
<b>Result</b>
ERROR LOAD_FILE_FAILED (Status code 0xF004): Failure to load SPI flash devices manufacturer specific ations from XML file is due to following: System.Xml.XmlException = The 'Vscs' start tag on line 89 position 6 does not match the end tag of 'SpiFlashDevice'. Line 92, position 5. at System.Xml.XmlTextReaderImpl.Throw(Exception e)  at System.Xml.XmlTextReaderImpl.Throw(String res, String[] args)  at System.Xml.XmlTextReaderImpl.ThrowTagMismatch(NodeData startTag)  at System.Xml.XmlTextReaderImpl.ParseEndElement()  at System.Xml.XmlTextReaderImpl.ParseElementContent()  at System.Xml.XmlTextReaderImpl.Read()  at System.Xml.XsdValidatingReader.Read()  at System.Xml.XmlLoader.LoadNode(Boolean skipOverWhitespace)  at System.Xml.XmlLoader.LoadDocSequence(XmlDocument parentDoc)  at System.Xml.XmlLoader.Load(XmlDocument doc, XmlReader reader, Boolean preserveWhitespace)  at System.Xml.XmlDocument.Load(XmlReader reader)  at vProSystemAccessLib.PlatformInformation.LoadSpiFlashManufSpecs(String xmlFileName, String xsdF ileName, String xsdNamespace)

Otherwise, the test will successfully load all the parts found in the XML.



# A. Intel® Platform Enablement Test Suite Package List

## A.1 Introduction

This document lists the packages provided with Intel® Platform Enablement Test Suite, and includes the name of the XML file of each package.

Note that some packages require you to have the Intel® PETS Automated Power Provider connected to the system under test. These packages are marked as such in the document. The other packages can be run without an Intel® PETS Automated Power Provider.

When you load a package into Intel® Platform Enablement Test Suite, the tests included in the package are listed in the main window. The package's tests map directly to tests in your Intel® AMT platform's Compliance Requirements and Testing Guide, which contains a detailed description of each test.

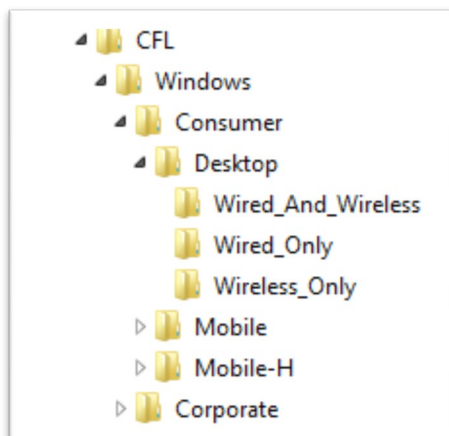
**Note:** For most packages, each Test Name maps to the Test ID of a test in the Compliance Requirements and Testing Guide. The exception is the Setup and Configuration package (SetupEnvironmentTest.xml); for details on the tests in this package, see section: 5.4.1

## A.2 Package List

Test packages are arranged by folders, as follows:

- PCH
- Operating system (OS) for some platforms
- Firmware size
- Desktop/mobile/workstation/All-in-one/Mobile-H
- Wired\_and\_Wireless/Wired\_Only/Wireless\_Only
- A full list of relevant packages and tests

For example, clicking the Package Open menu for CoffeeLake (CFL) opens a dialog showing the supported PCHs, as described below:





The packages themselves bear simple names, whereas in the past their names reflected all of the above options. Below is a complete list of the packages that are included in different folders. Only packages relevant to that folder will be included, and only tests relevant to that folder will be included within the packages (i.e., a package in 2 different folders may not have the same contents, even if it has the same name).

The Test Packages include:

**ME Packages**

- Platform Controller Hub (PCH) SoftStrap Configuration (Compliance\_PSS.xml)
- Intel® Platform Trust Technology (Compliance\_PTT)
- SPI Flash Configuration (Compliance\_SpiFlashConfiguration.xml)
- Intel® Management Engine BIOS Compliance (Compliance\_MeBios.xml)  
This package requires that the Intel® PETS Automated Power Provider be connected to the system under test.
- ME BIOS Manufacturing mode package (Compliance\_MeBios\_ManufacturingMode.xml)  
This test runs in pre-manufacturing mode.
- Setup Environment Check of Intel® Platform Enablement Test Suite (SetupEnvironmentTest.xml)  
Checks ME Connectivity setup, Local Agent and SUT connectivity setup, and the Intel® PETS Automated Power Provider setup.  
Should be run after Intel® Platform Enablement Test Suite is installed and whenever the environment is changed.  
This package requires that the Intel® PETS Automated Power Provider be connected to the system under test.

**Note:** For details on the tests in this package, see section: 5.4.1 ["Testing the Configuration of the SUT"](#).

- Intel® AMT Tests
- Intel® AMT and KVM Compliance (Compliance\_AMT.xml)
- Secure Boot package (SecureBoot.xml) Part of Compliance AMT.
- Intel® Management Engine FW Power Management - BIOS Setting: S5 after Exiting G3 (Compliance\_Power\_G3-S5.xml) (Compliance\_Power\_G3-S5\_UnProvision.xml)  
This package requires that the Intel® PETS Automated Power Provider be connected to the system under test.

**Note:** Before running this package, you need to ensure that the BIOS settings on the platform under test cause it to transition to S5 after it exits G3.

- Intel® Management Engine FW Power Management - BIOS Setting: S0 after Exiting G3 (Compliance\_Power\_G3-S0.xml)  
This package requires that the Intel® PETS Automated Power Provider be connected to the system under test.

**Note:** Before running this package, you need to ensure that the BIOS settings on the platform under test cause it to transition to S0 after it exits G3.

- Integrated Clock Controller (ICC) - H (Compliance\_ICC\_H.xml)
- Integrated Clock Controller (ICC) - LP (Compliance\_ICC\_LP.xml)



- PM with WOL tests
- (Compliance\_Power\_Network\_Wake.xml)  
This package includes PM tests with trigger of WOL (Magic packet) and Deep SX disabled.  
This package requires that the Intel® PETS Automated Power Provider be connected to the system under test.
- ME Common Services (Compliance\_MECommonServices.xml)  
This package requires that the Intel® PETS Automated Power Provider be connected to the system under test.
- Intel® Dynamic Application Loader (Intel® DAL) (Compliance\_DAL.xml)  
This package includes the DAL tests in the Compliance Guide. It is used for testing PM flows in Power Policy 2.

**Note:** Before running these packages, you need to ensure that the BIOS settings on the platform under test cause it to transition to S5 after it exits G3.

**Note:** Before running these packages, you need to ensure that the BIOS settings on the platform under test cause it to transition to S0 after it exits G3.

- ME Power Gating tests  
(Compliance\_ME\_Power\_Gating.xml)  
(Compliance\_ME\_Power\_Gating\_Network\_Wake.xml)  
(Compliance\_ME\_Power\_Gating\_Provision.xml)  
Before running this package please make sure that the system is configured to **Connected** standby mode.
- Intel® Power Reset (Compliance\_Power\_RST)
- Intel® Authenticate Solution (Compliance\_Intel(R)\_Authenticate)
- Intel® Boot Guard (Compliance\_BootGuard)
- Intel® Power Wake On Wireless LAN tests (Compliance\_Power G3\_S5\_WoWLAN) (Coexistence mode)
- Intel® ME Power Management Stress Test Coverage Summary (PM\_Stress\_Testing)
- Intel® Wake on Wireless LAN Stress tests (PM\_Stress\_Testing\_WoWLAN) (Coexistence mode)

#### TXE Packages

- TXE Compliance MFM (TXE\_Compliance\_MFM.xml)  
This package tests certain BIOS operations that are not performed when TXE is in Manufacturing Mode.
- TXE\_Compliance\_SpiFlashConfiguration (TXE\_Compliance\_SpiFlashConfiguration.xml)  
Test Environment for Serial Flash (SPI) Configuration.
- TXE Compliance PM (TXE\_Compliance\_PM.xml).  
This package provides detailed tests for TXE FW Power Management.
- TXE Compliance PM RST (Compliance\_Power\_RST.xml).
- TXE Compliance PM Stress (Compliance\_Power\_Stress.xml).
- TXE Compliance Signed Master Image Profile (Compliance\_SMIP.xml).
- TXE Compliance ISH (Compliance\_ISH.xml).
- TXE Compliance BIOS (TXE\_Compliance\_BIOS.xml)  
This package serves as a checklist for the environment setup for the host BIOS and Intel® TXE interface testing and validation.
- TXE Compliance PTT (TXE\_Compliance\_PTT.xml)  
This package tests the implementation of TCG TPM 2.0 standard in firmware.



- TXE Compliance Audio (TXE\_Compliance\_Audio.xml)  
This package tests general audio functionality including Bluetooth and HDMI audio.
- TXE Compliance Wi-Fi (TXE\_Compliance\_WiFi)  
This package provides test cases for Wi-Fi / WLAN functional tests.

## **A.4 Modifying PETS Configuration file**

The name of the PETS configuration file is "IntelPlatformEnablementTestSuite.exe.config". It can be found under PETS console installation directory.

### **A.4.1 AMT connection timeout**

The AMT connection timeout is separated in PETS configuration file into two different keys, one for the LAN and the other one for the WLAN. The configuration keys are:

1. "WSMANTimeoutLAN" with default value of 10 seconds for LAN
2. "WSMANTimeoutWLAN" " with default value of 30 seconds for WLAN

In most cases, these values can be ignored unless "Unable to connect" error messages are seen in the Intel® AMT test steps. When such errors occur, which would be due to connection instability, you can adjust these settings and expand the timeout as needed.

**Note:** Expanding the default timeout will impact the time needed to stop or pause the related test steps and PETS might take more time than usual during the stop or pause operations.